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**Deladurantaye et al.**

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(54) **METHODS FOR STABILIZING THE OUTPUT OF A PULSED LASER SYSTEM HAVING PULSE SHAPING CAPABILITIES**

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(\* ) Notice: Subject to any disclaimer, the term of this patent is extended or adjusted under 35 U.S.C. 154(b) by 179 days.

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(22) Filed: **May 18, 2010**

(65) **Prior Publication Data**

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**Related U.S. Application Data**

(63) Continuation-in-part of application No. 12/493,949, filed on Jun. 29, 2009, now Pat. No. 8,073,027.

(60) Provisional application No. 61/076,337, filed on Jun. 27, 2008.

(51) **Int. Cl.**  
**H01S 3/10** (2006.01)

(52) **U.S. Cl.** ..... 372/25; 372/29.02; 372/30; 372/31

(58) **Field of Classification Search** ..... 372/25, 372/29.02, 30

See application file for complete search history.

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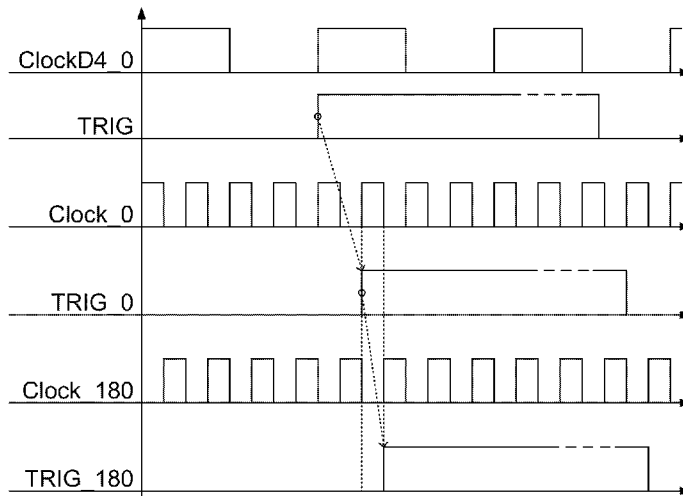
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(57) **ABSTRACT**

Methods stabilize the output of a pulsed laser system using pulse shaping capabilities. In some embodiments, transient effects following a transition between a QCW regime and a pulse shaping regime are mitigated by ensuring that the average QCW optical power substantially corresponds to the average pulsed optical power outputted in a steady-state operation of the pulsed laser system in the pulse shaping regime. The QCW signal or the pulse shaping signal may be adapted for this purpose. In other embodiments, transient effects associated with non-process pulses emitted between series of consecutive process pulses are mitigated through the proper use of sequential pulse shaping.

**7 Claims, 46 Drawing Sheets**



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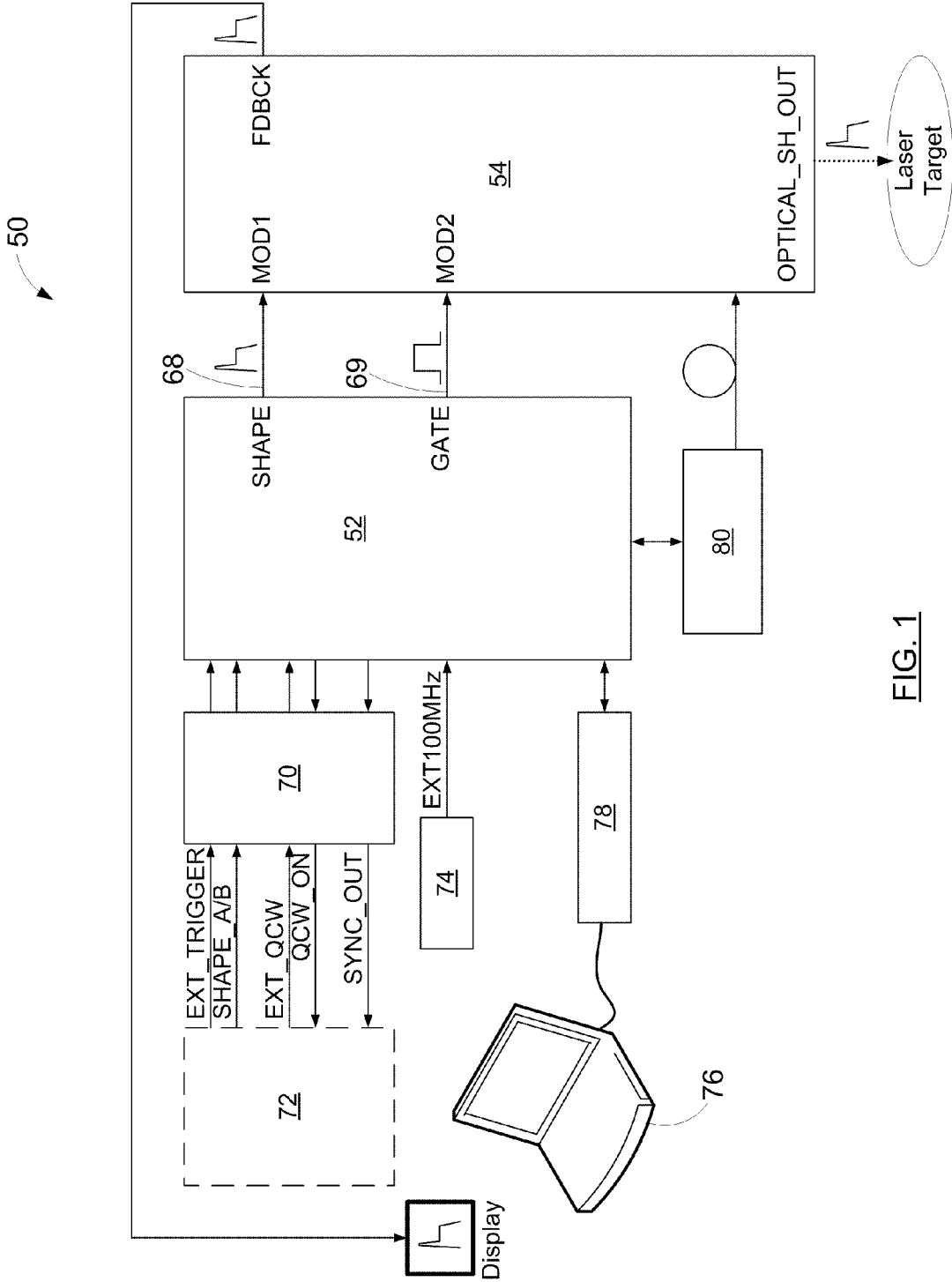


FIG. 1

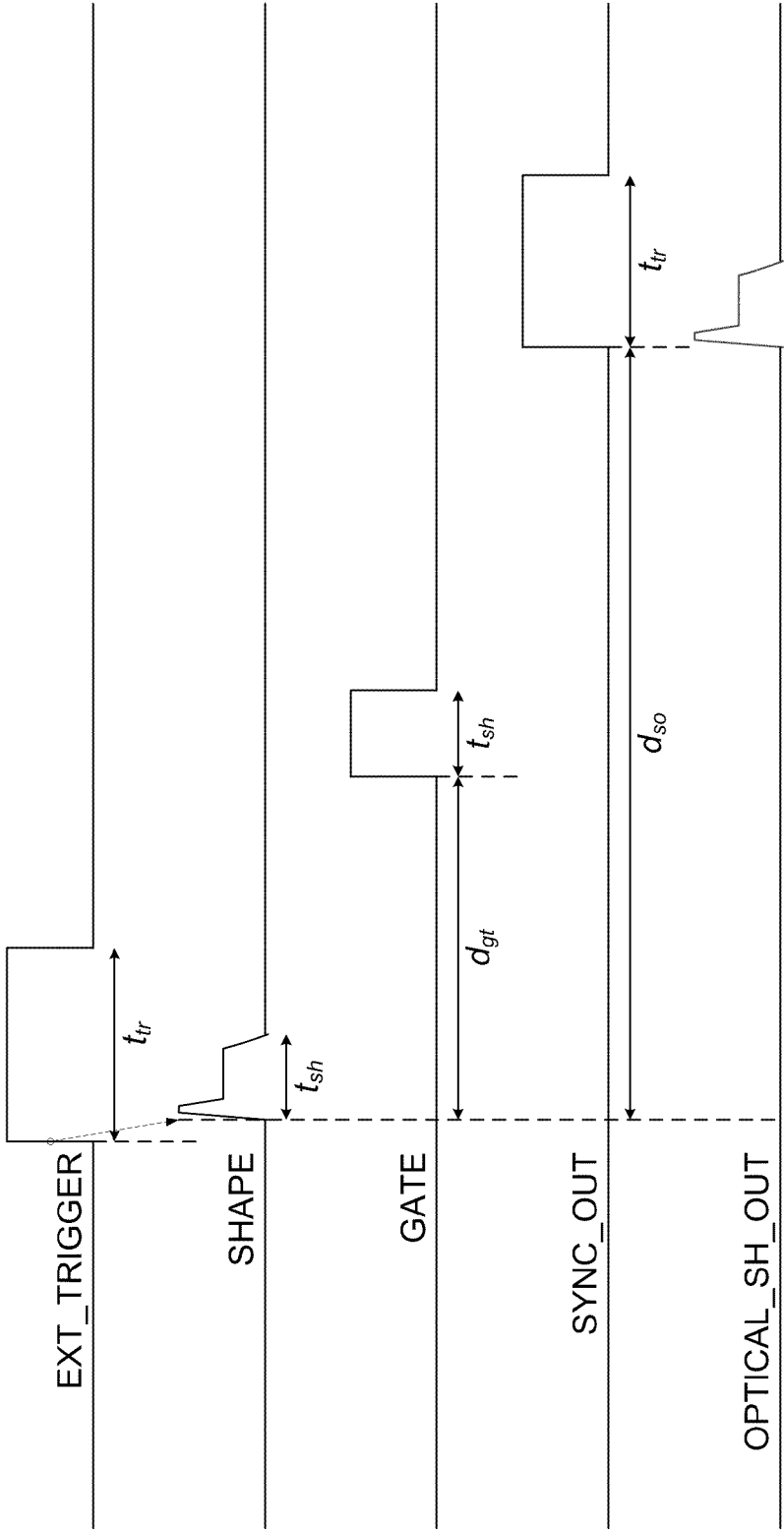


FIG. 2

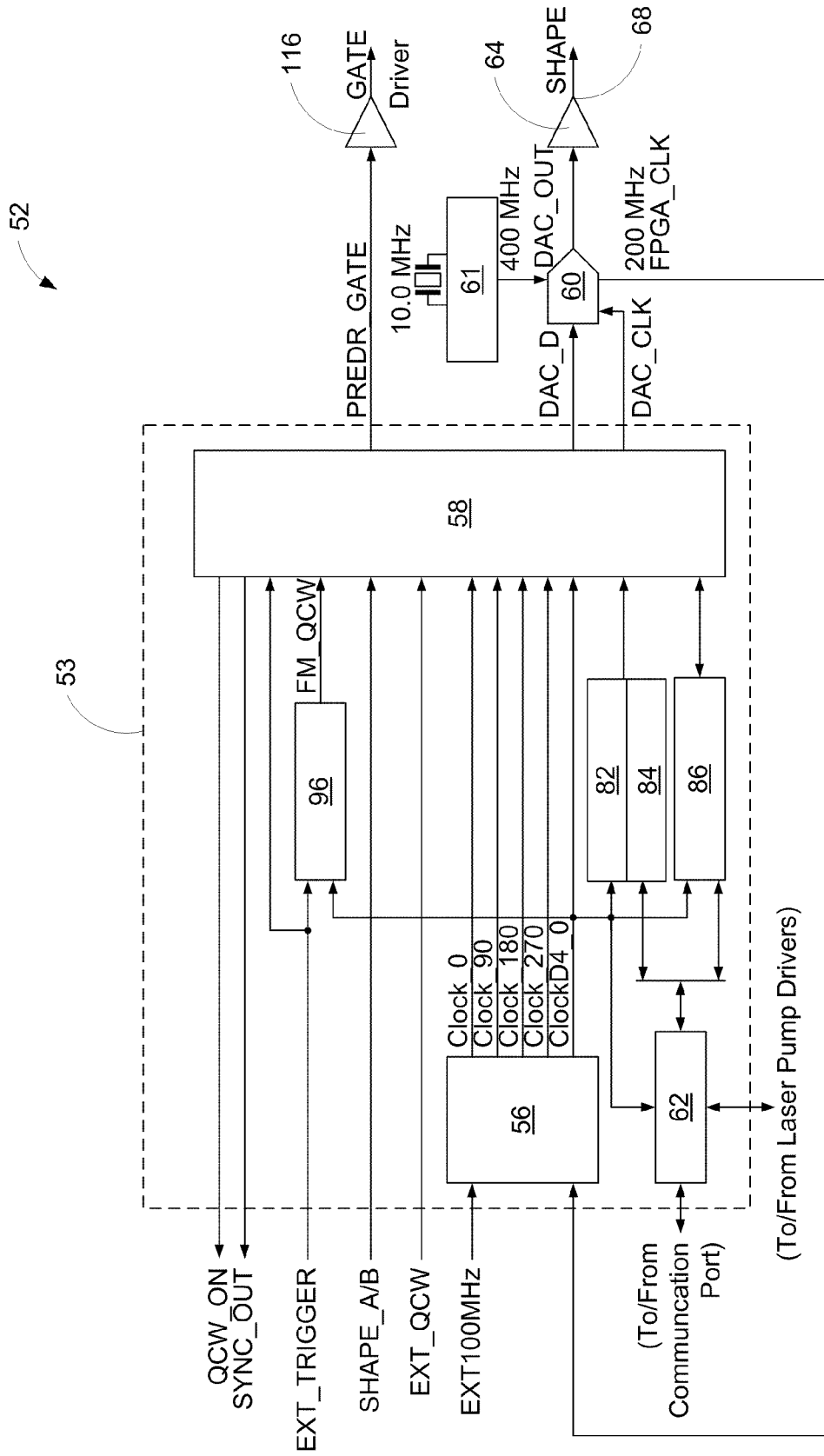


FIG. 3A

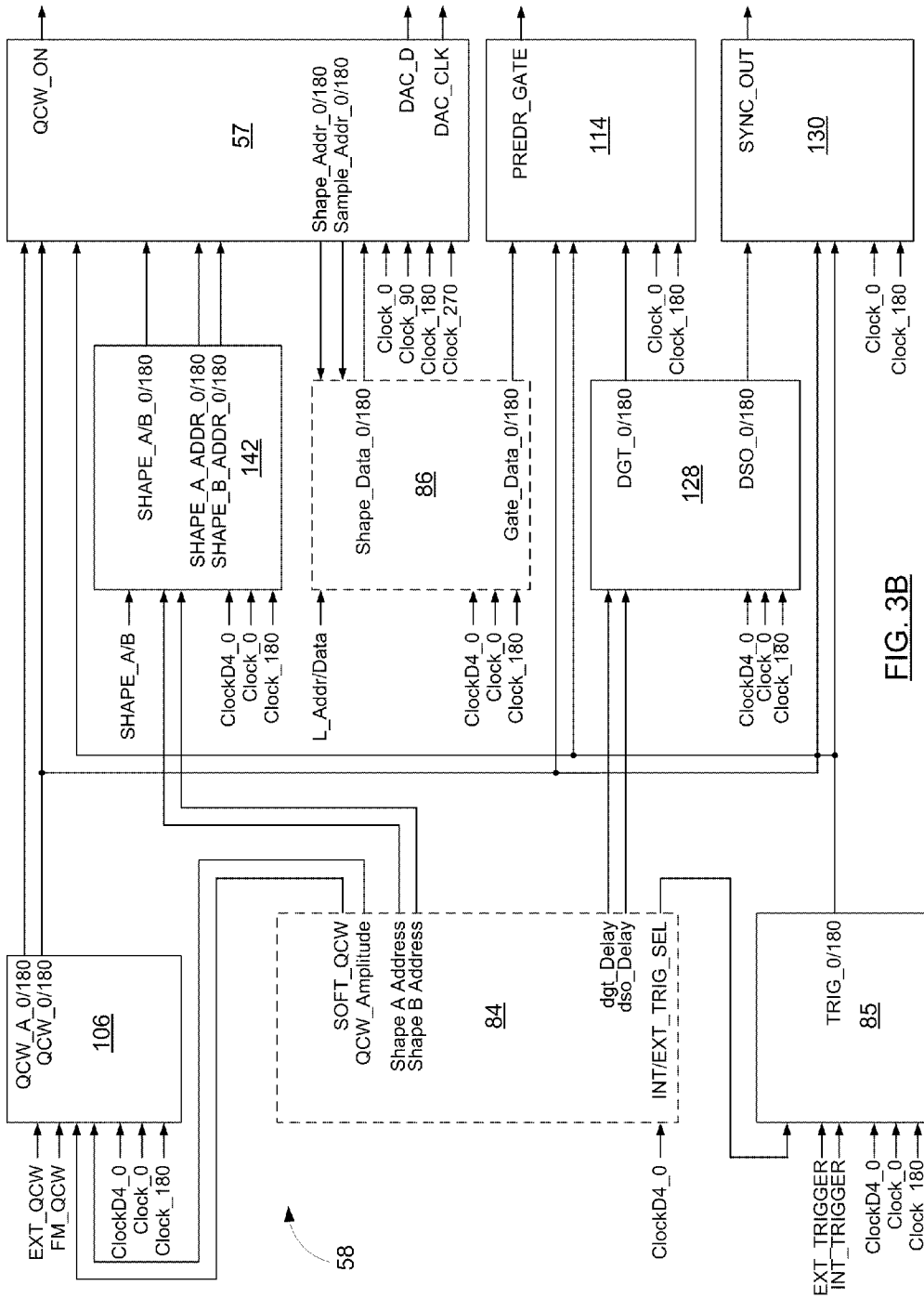


FIG. 3B

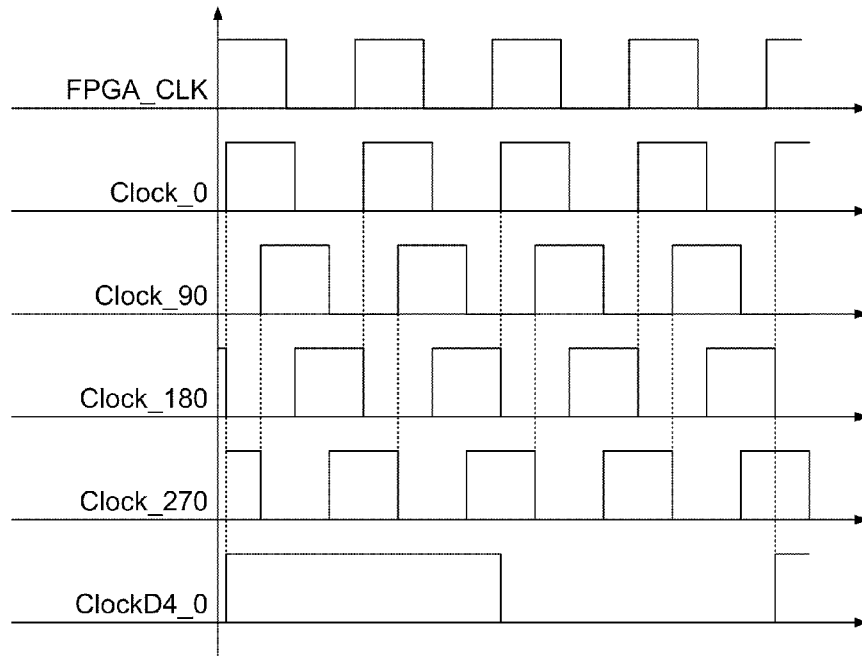


FIG. 4

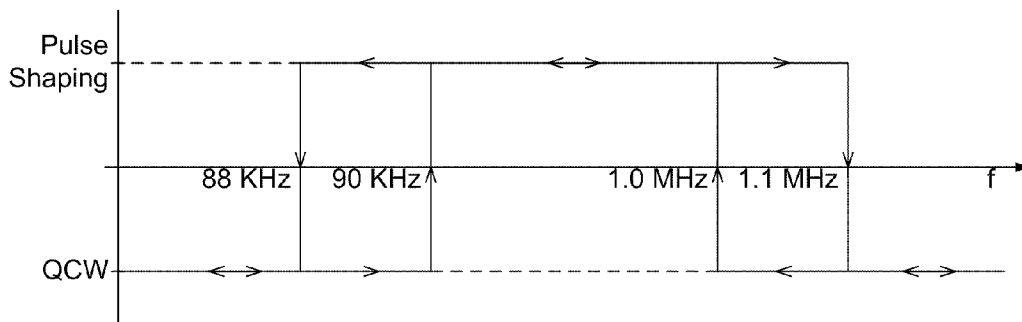


FIG. 5

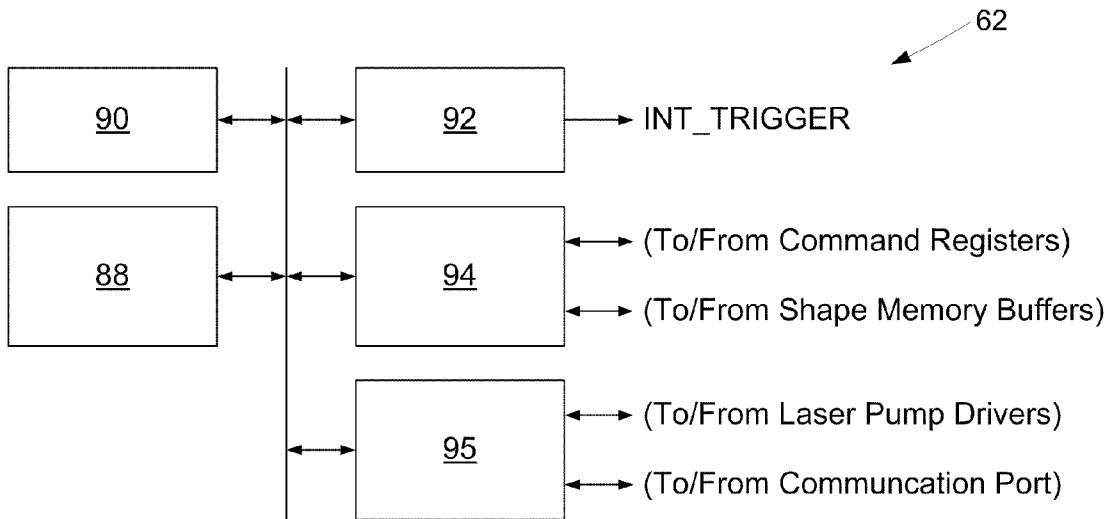


FIG. 6

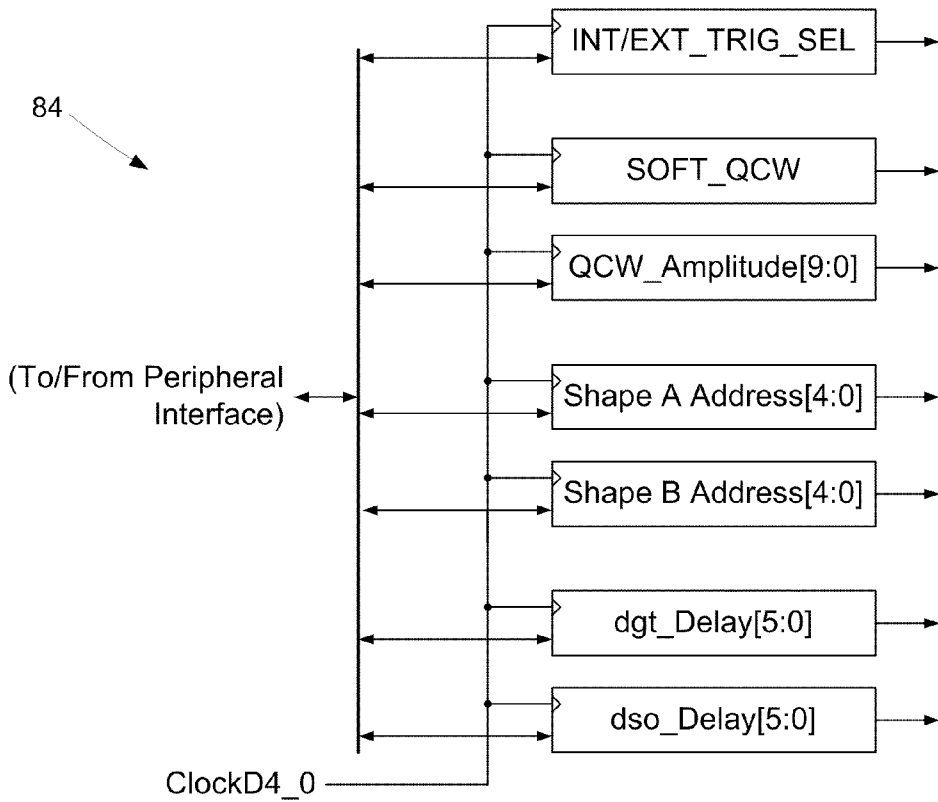


FIG. 7



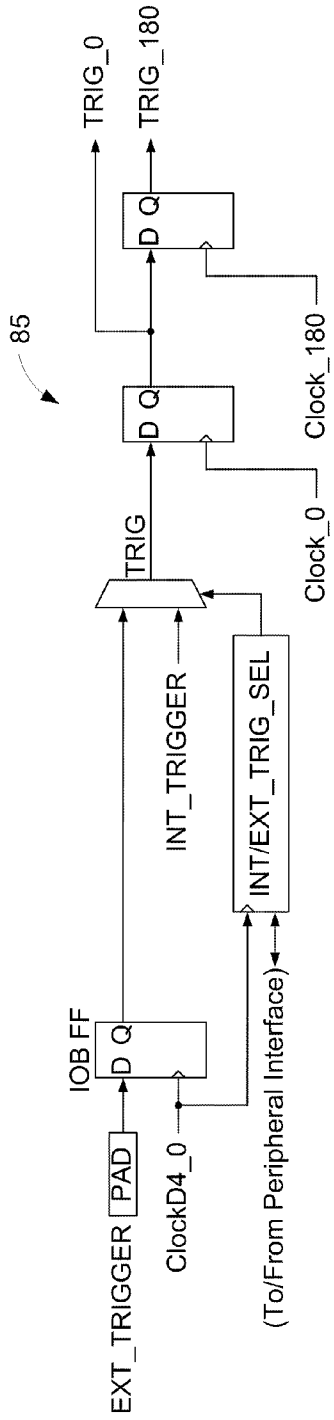


FIG. 8

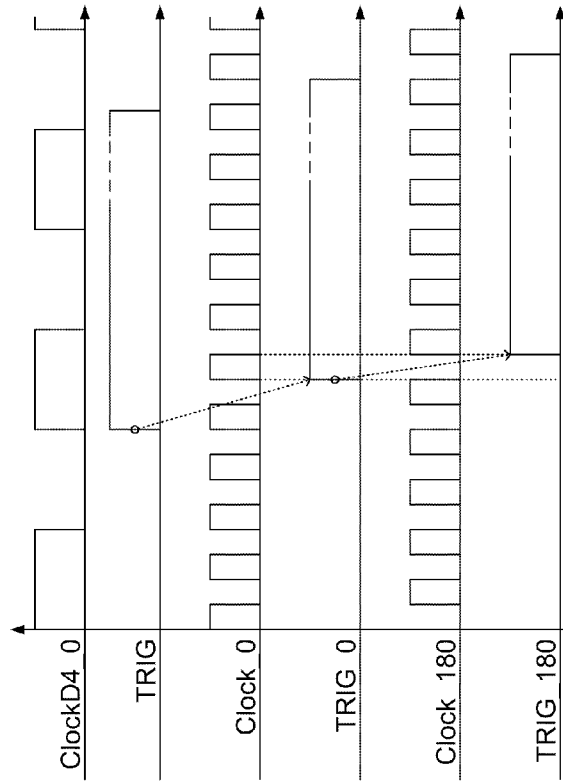


FIG. 9

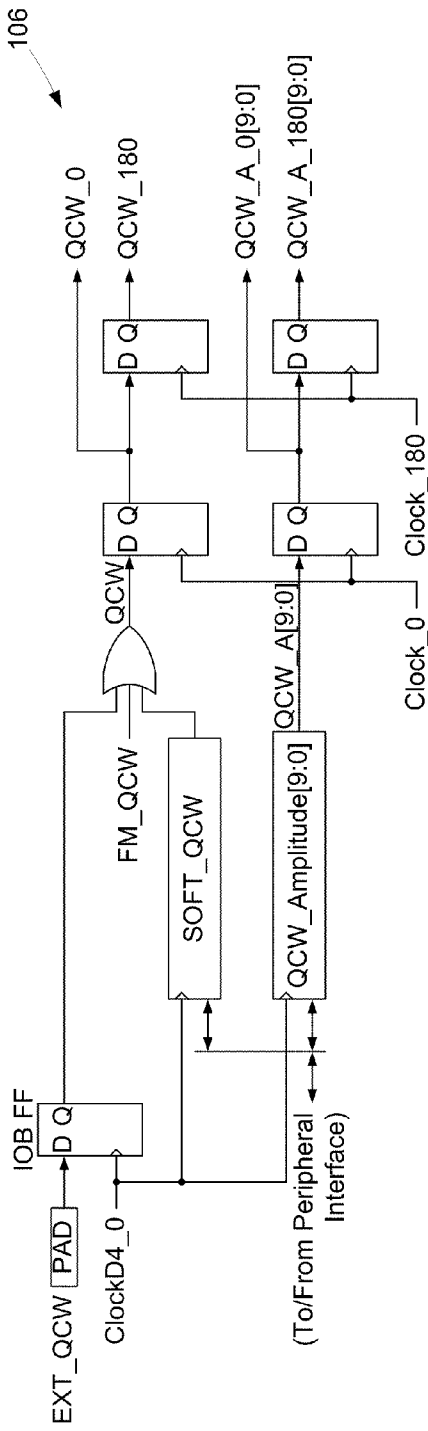


FIG. 10

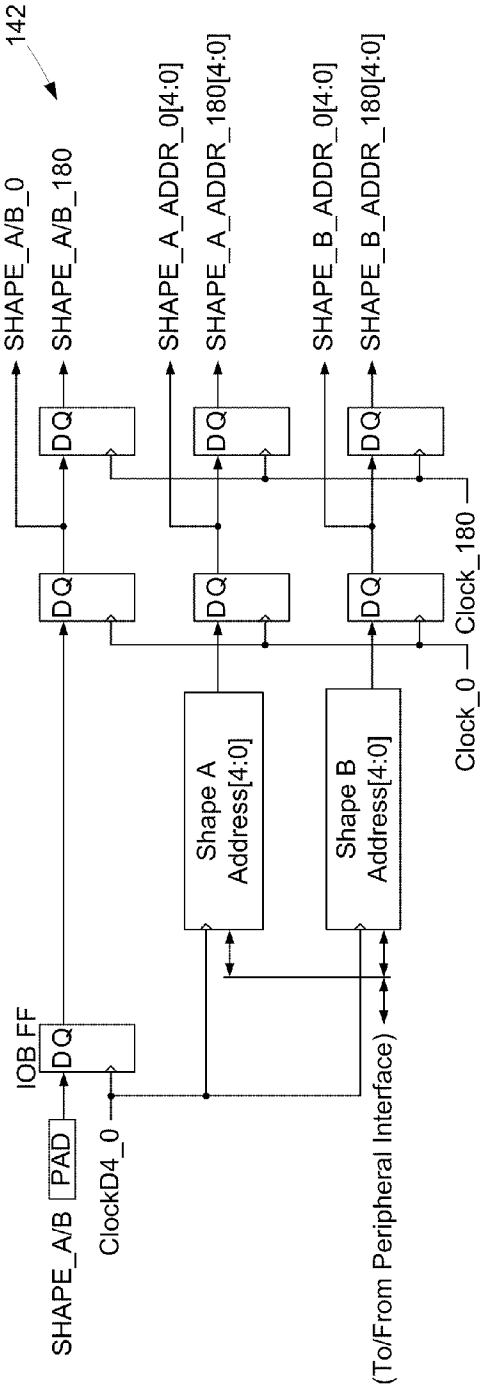


FIG. 11

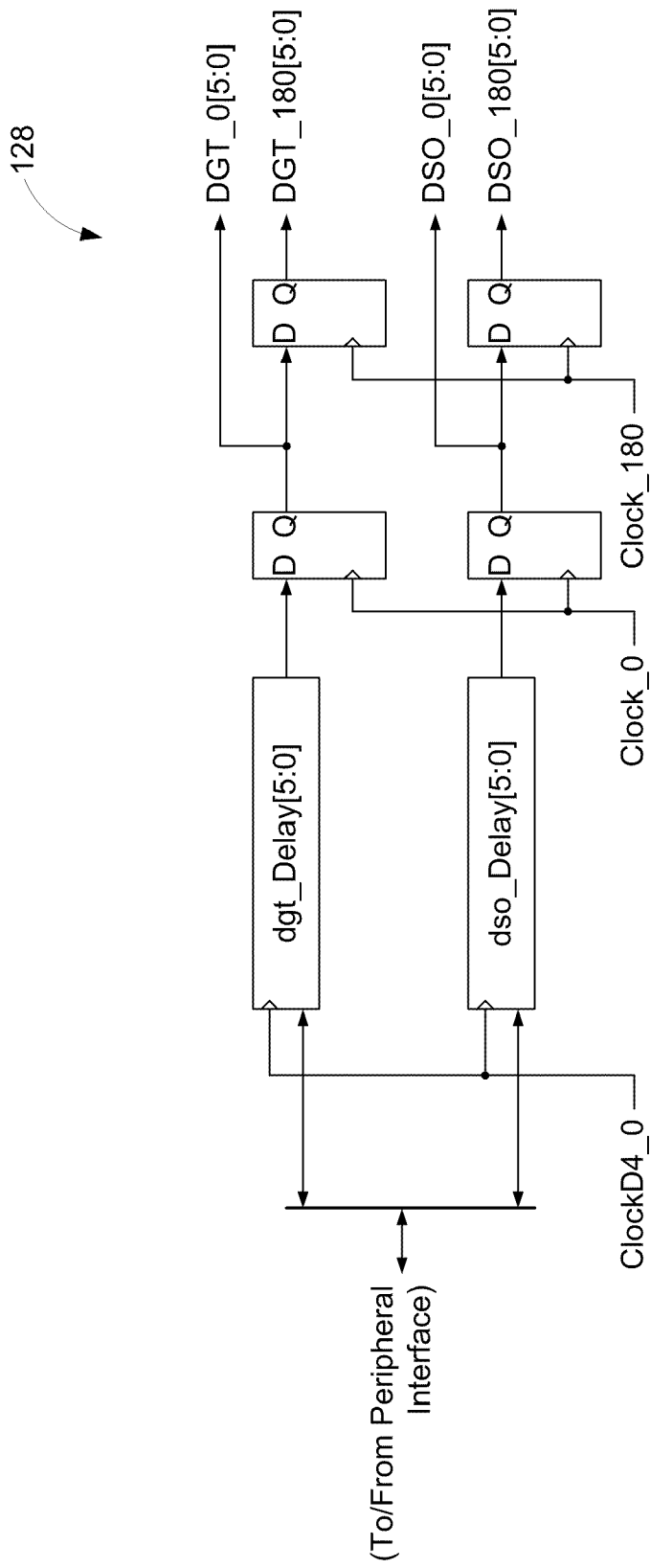


FIG. 12

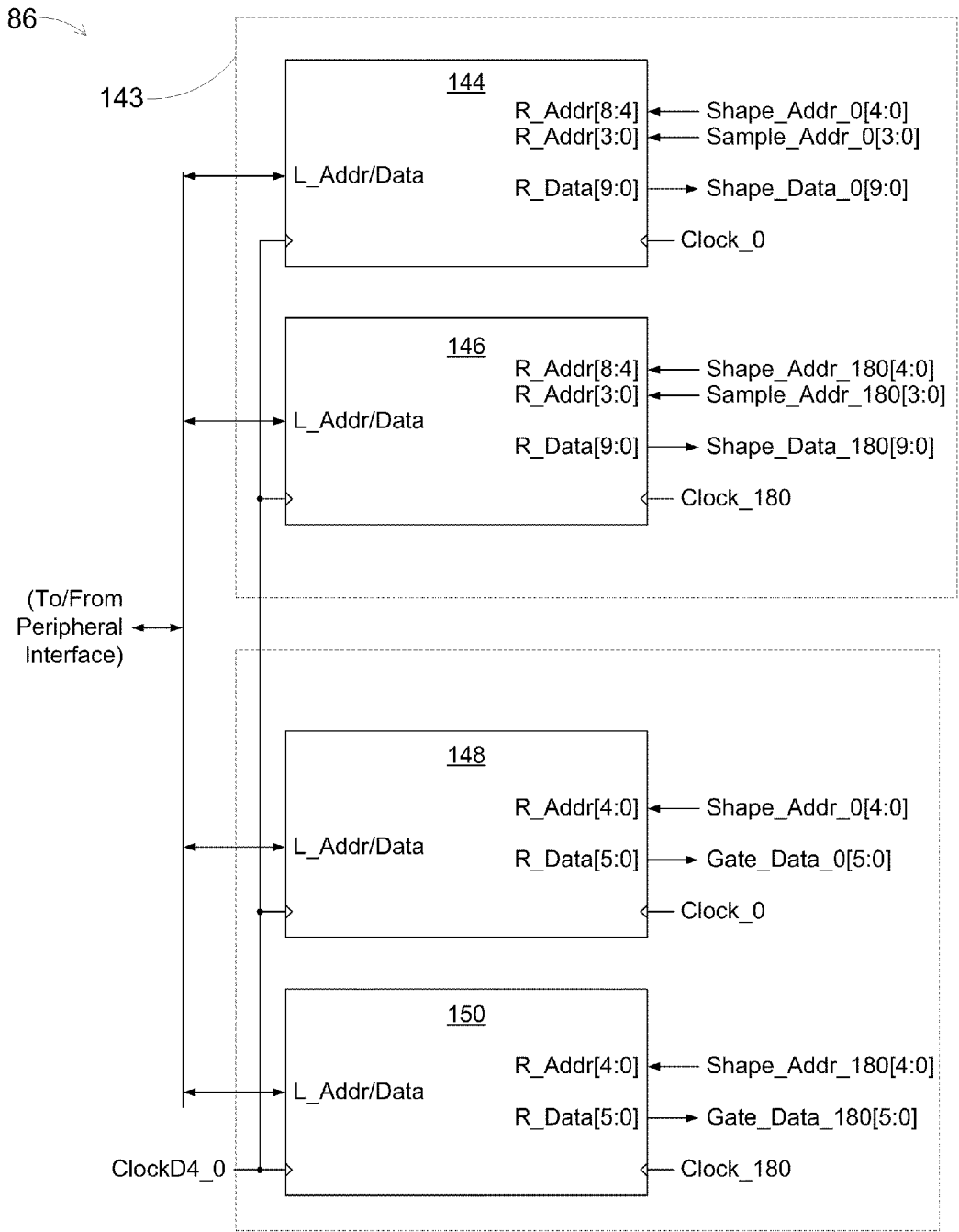
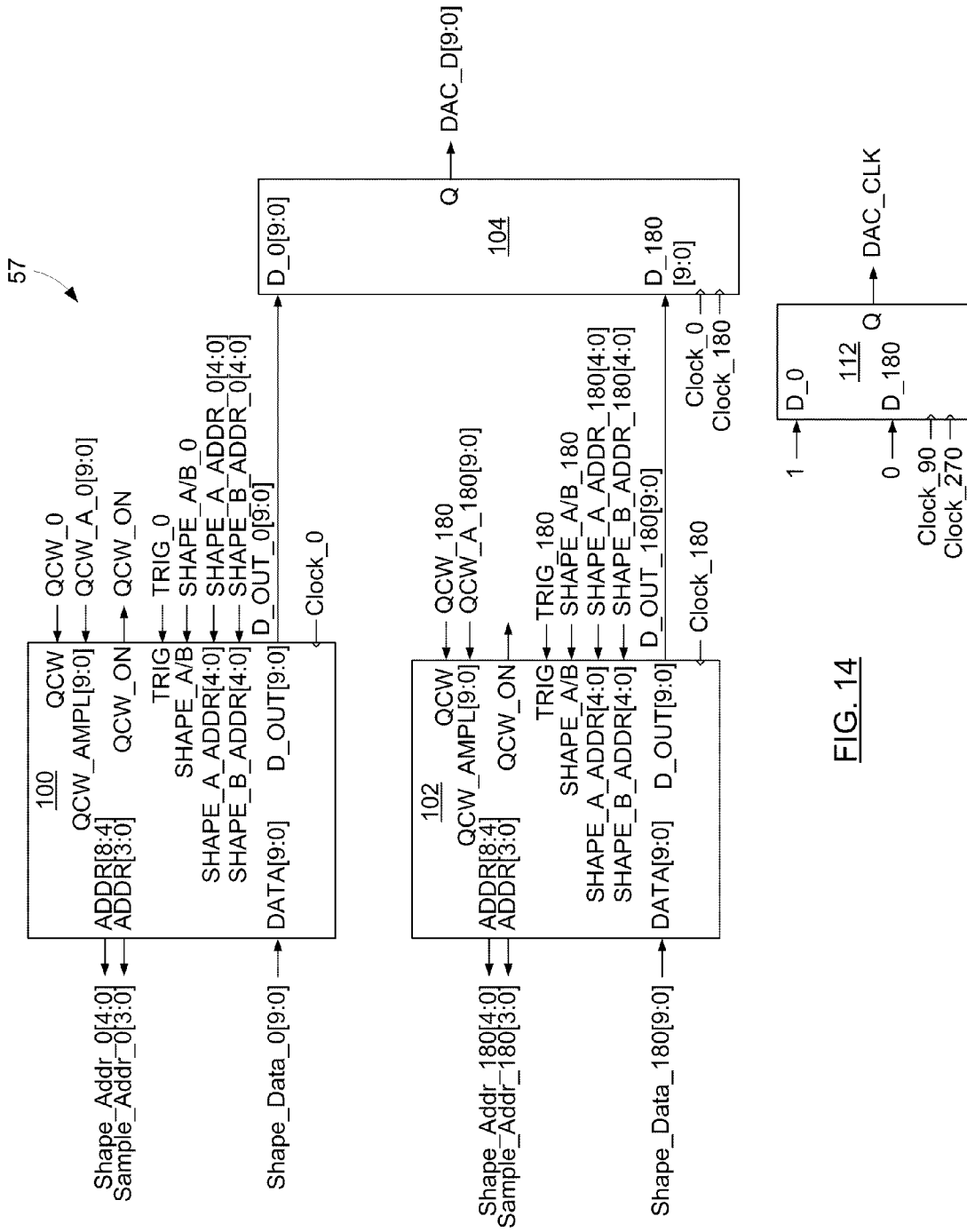


FIG. 13

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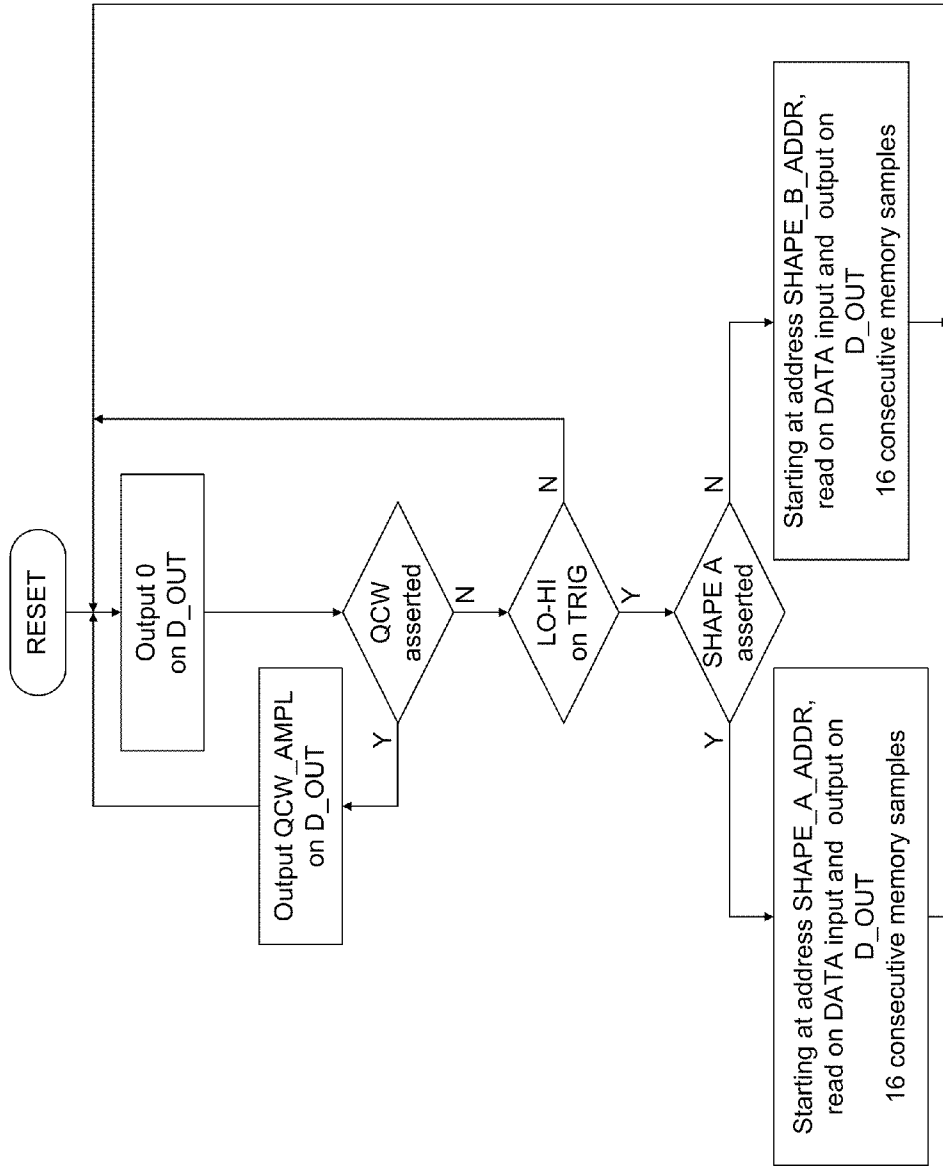


FIG. 15

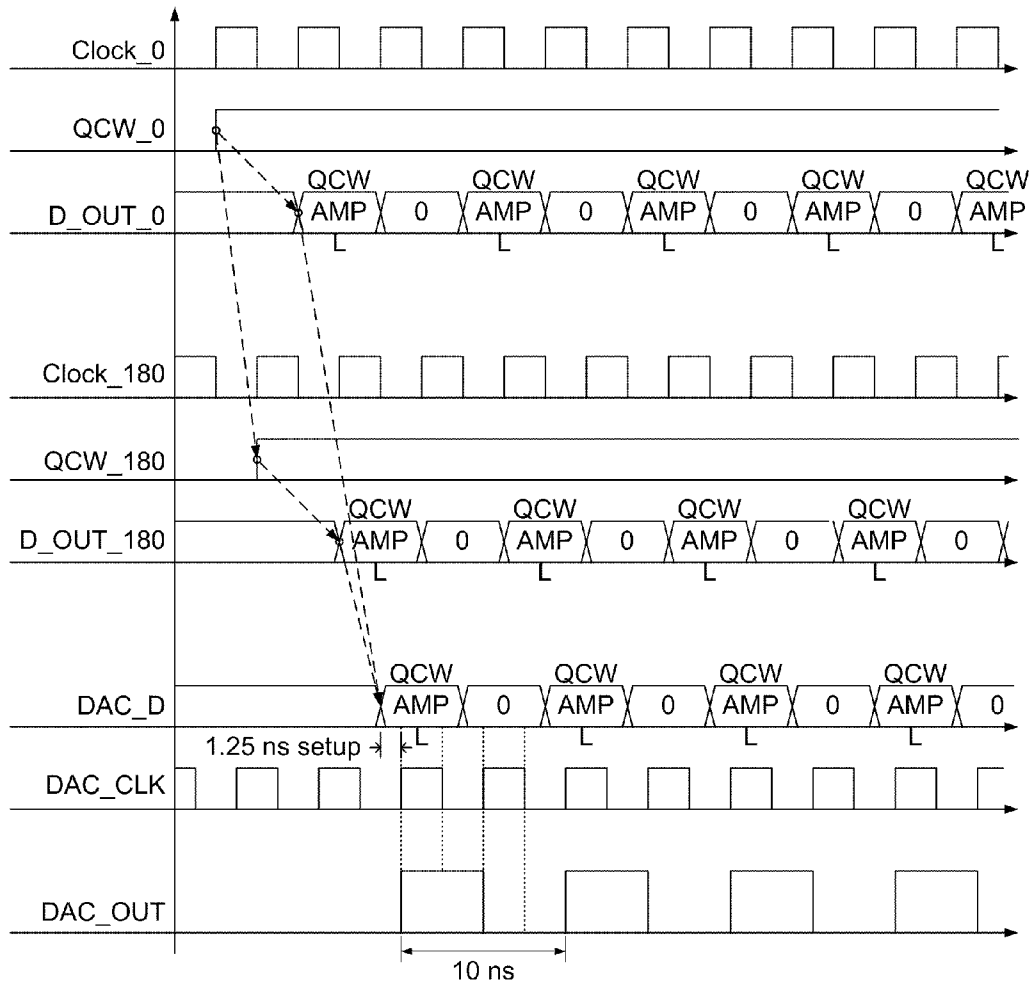


FIG. 16

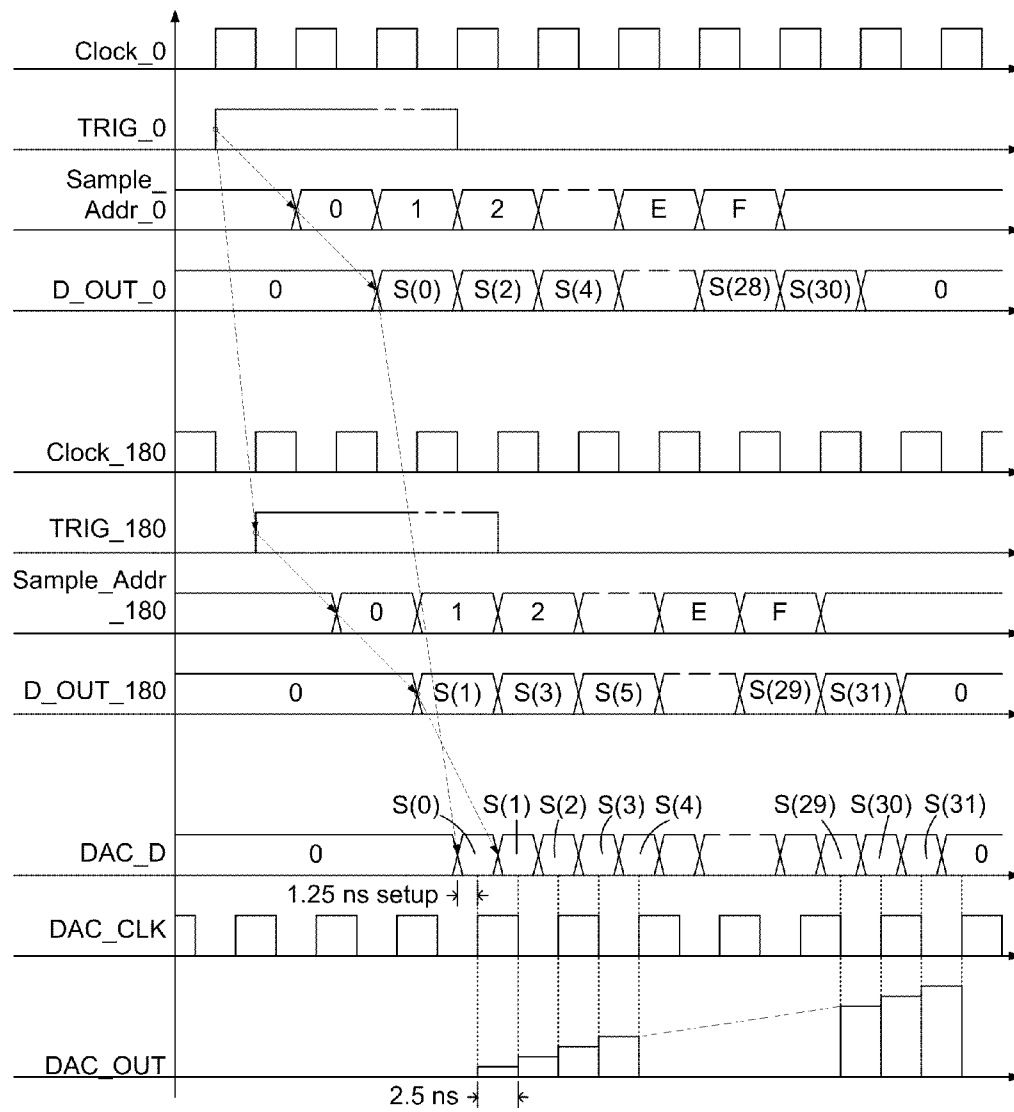


FIG. 17



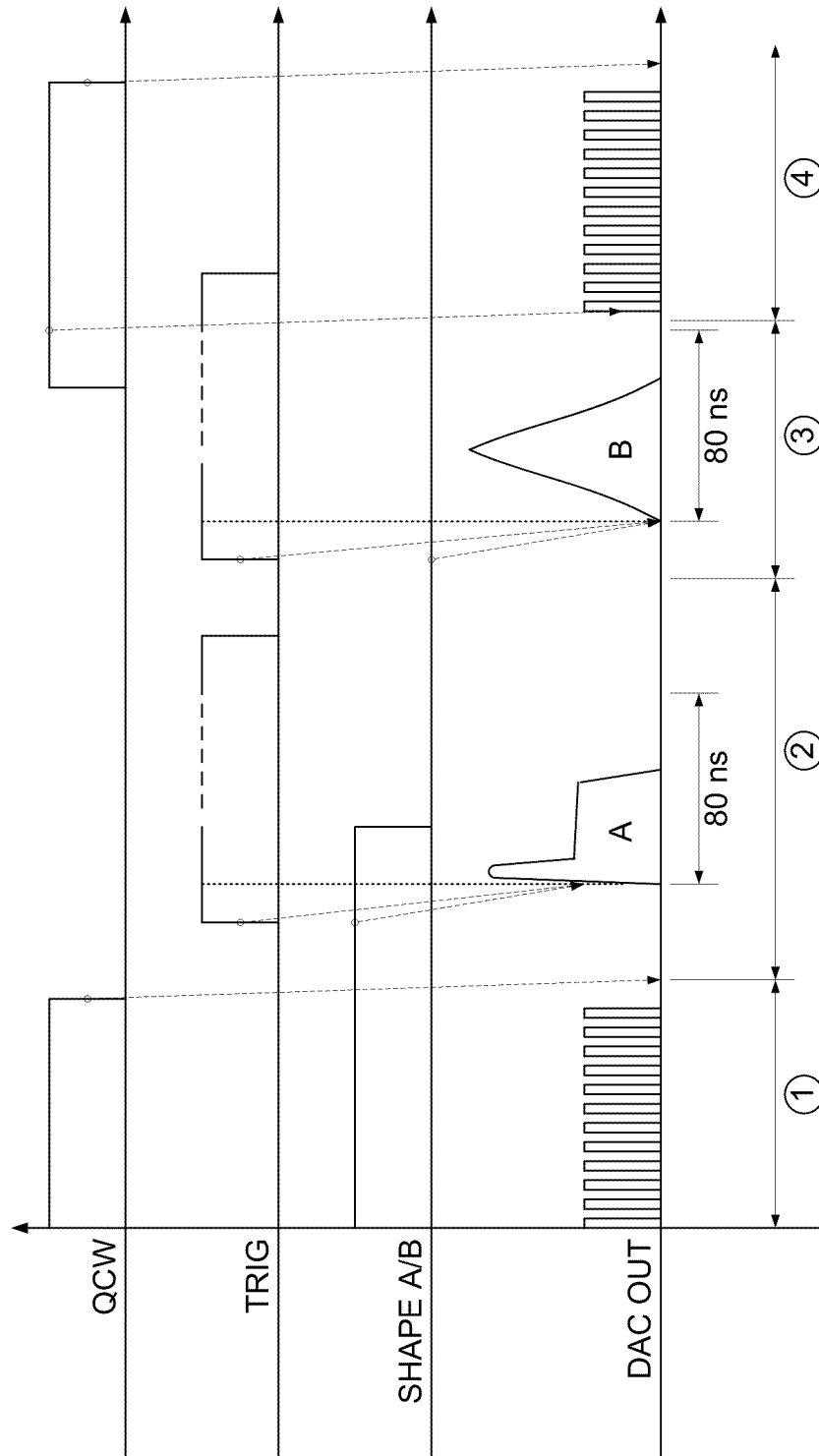


FIG. 18

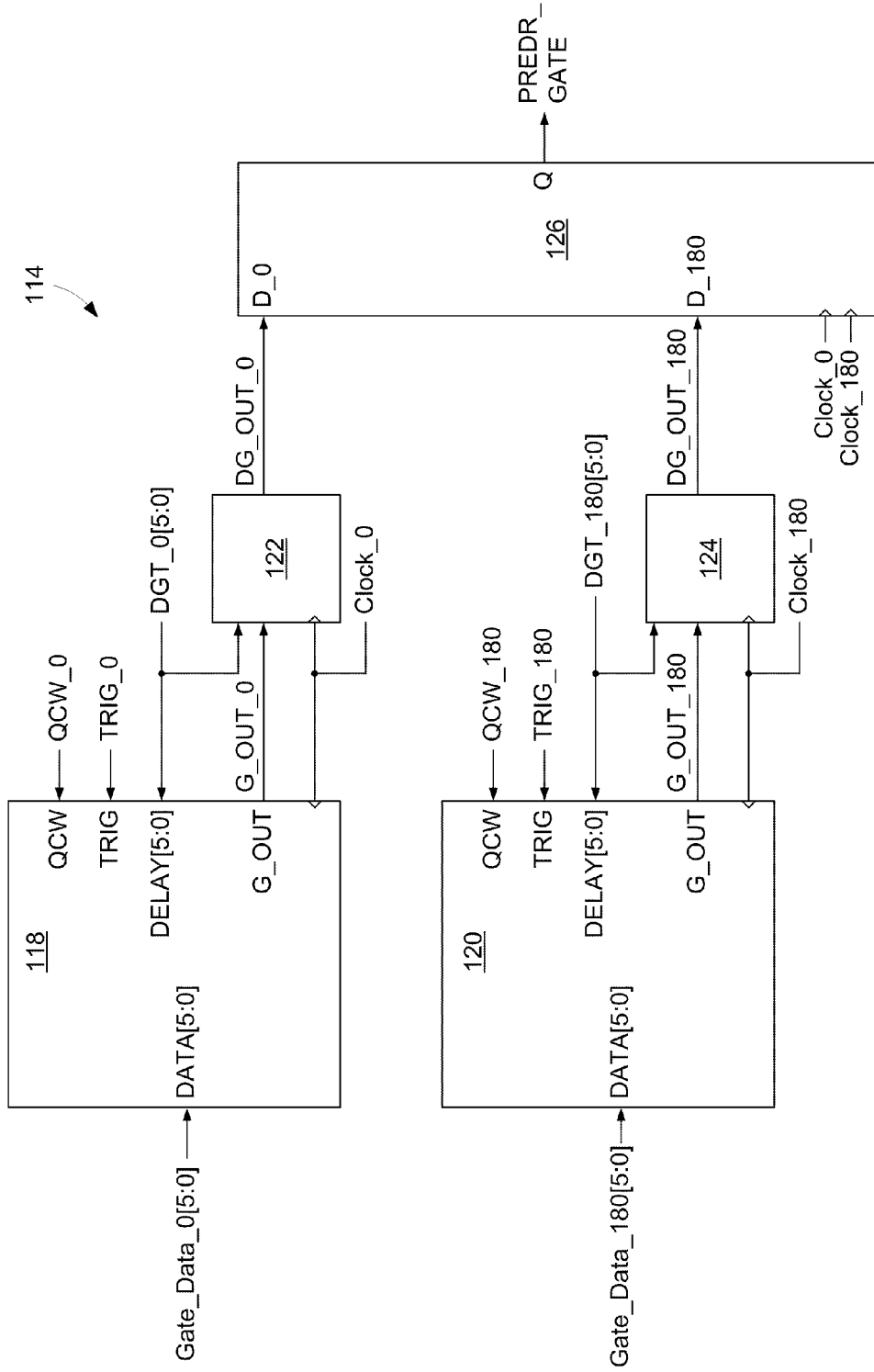


FIG. 19

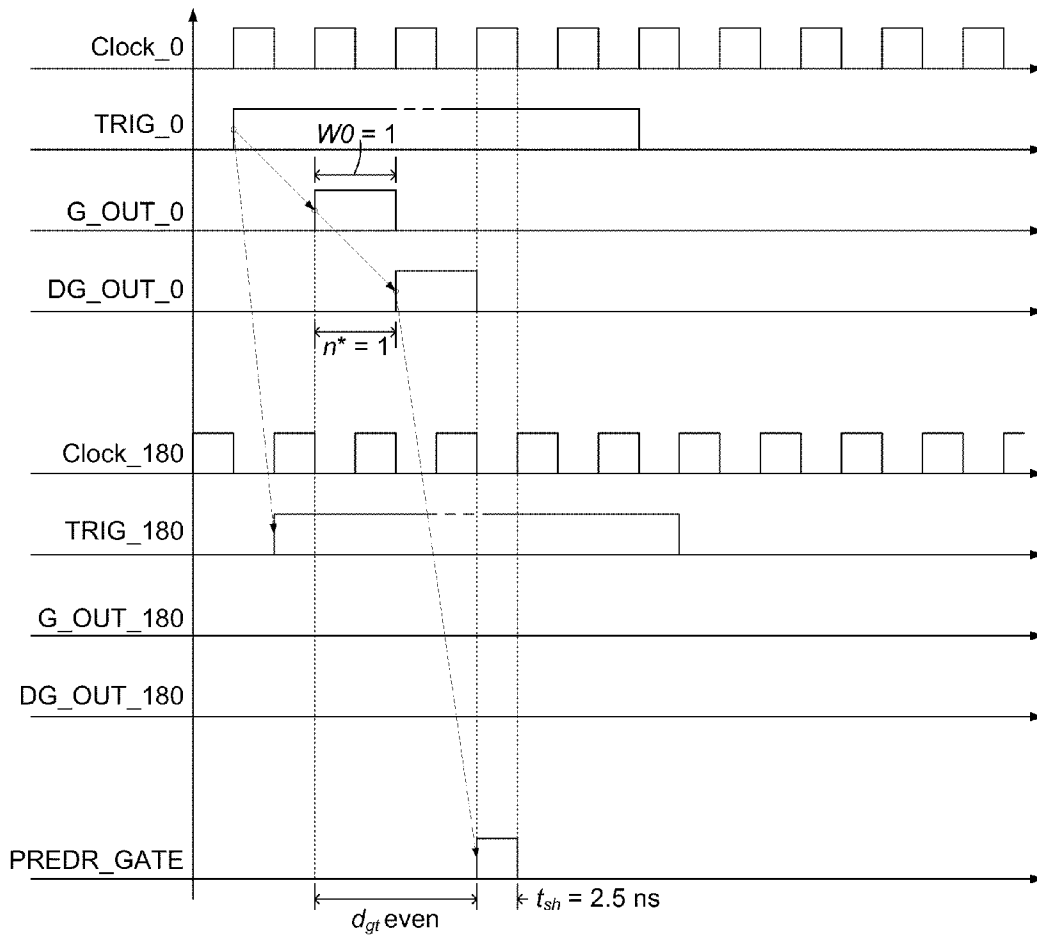


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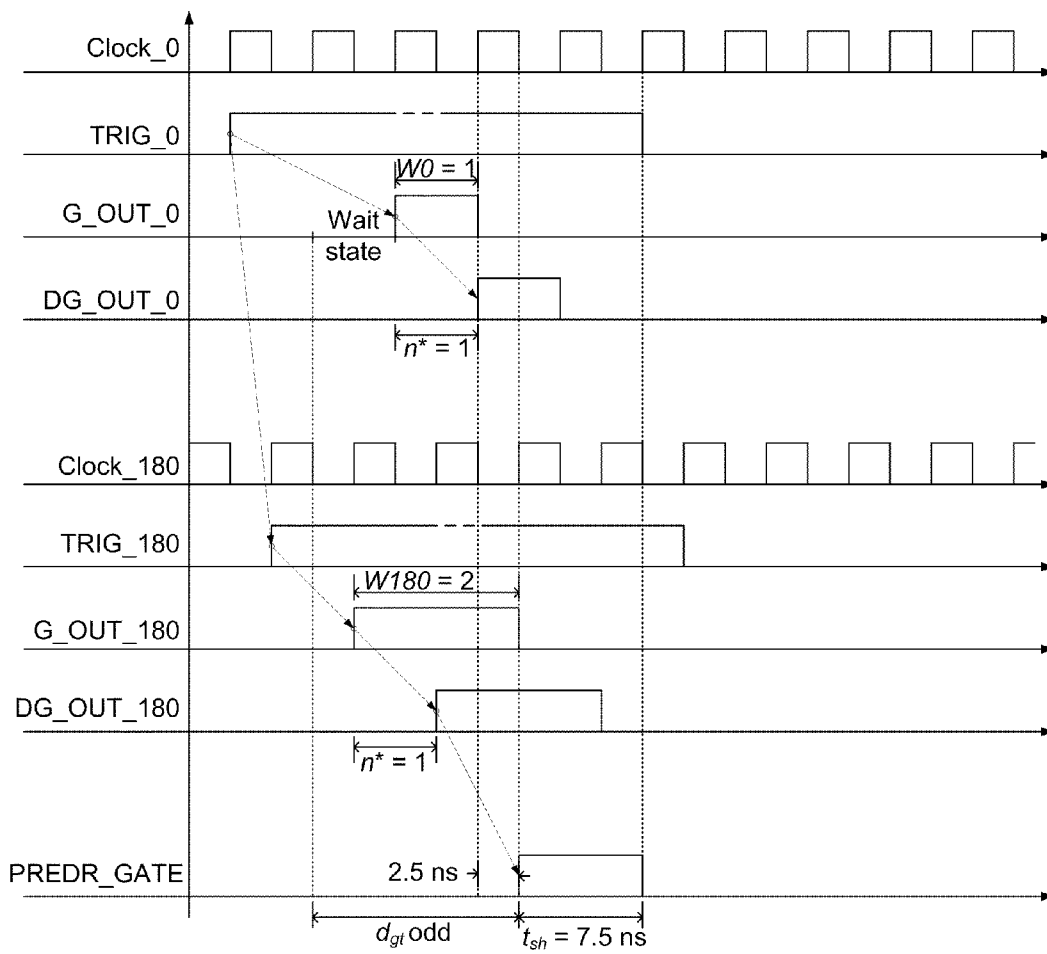


FIG. 21

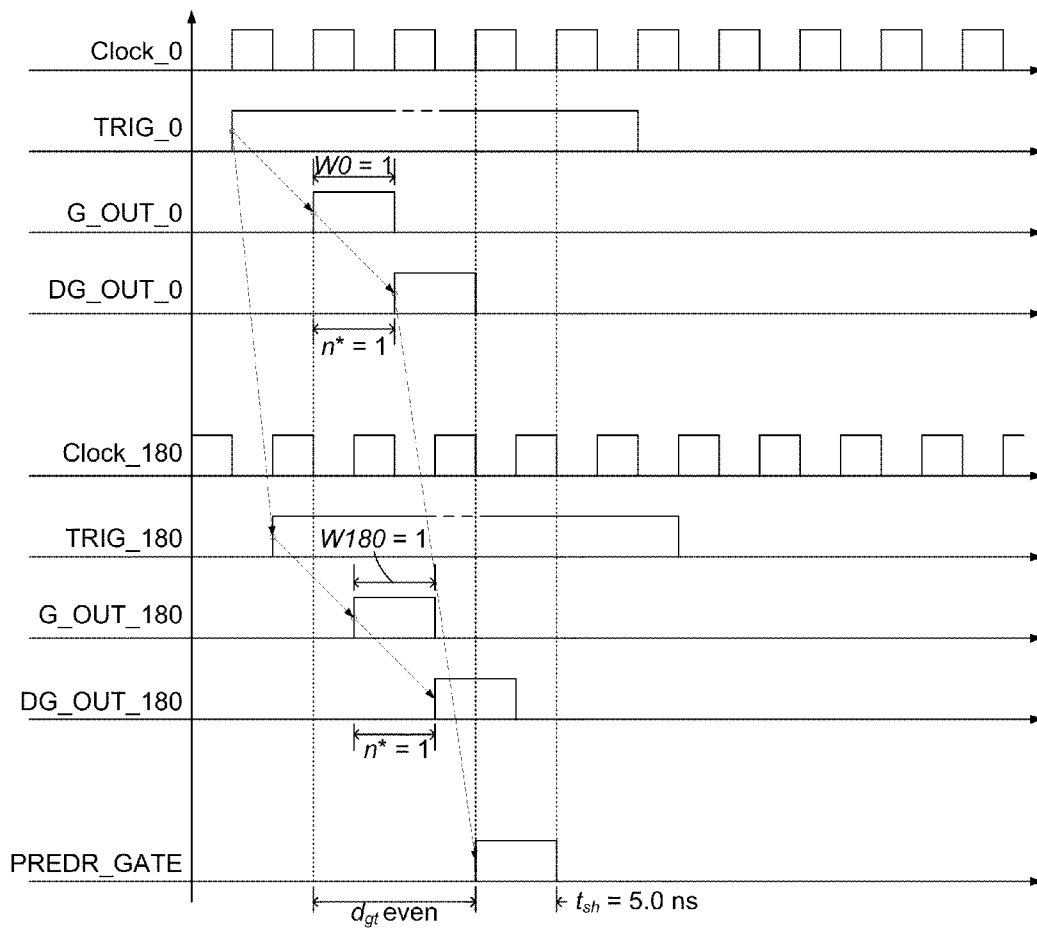


FIG. 22

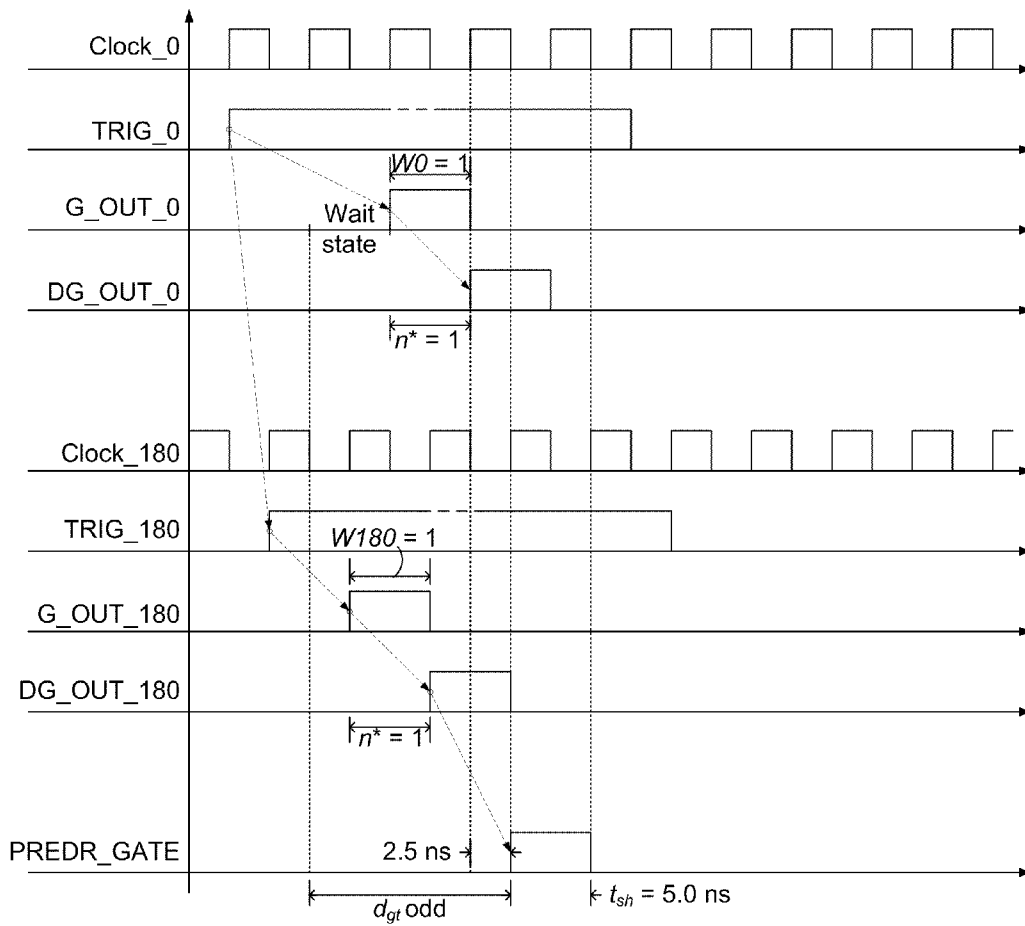


FIG. 23

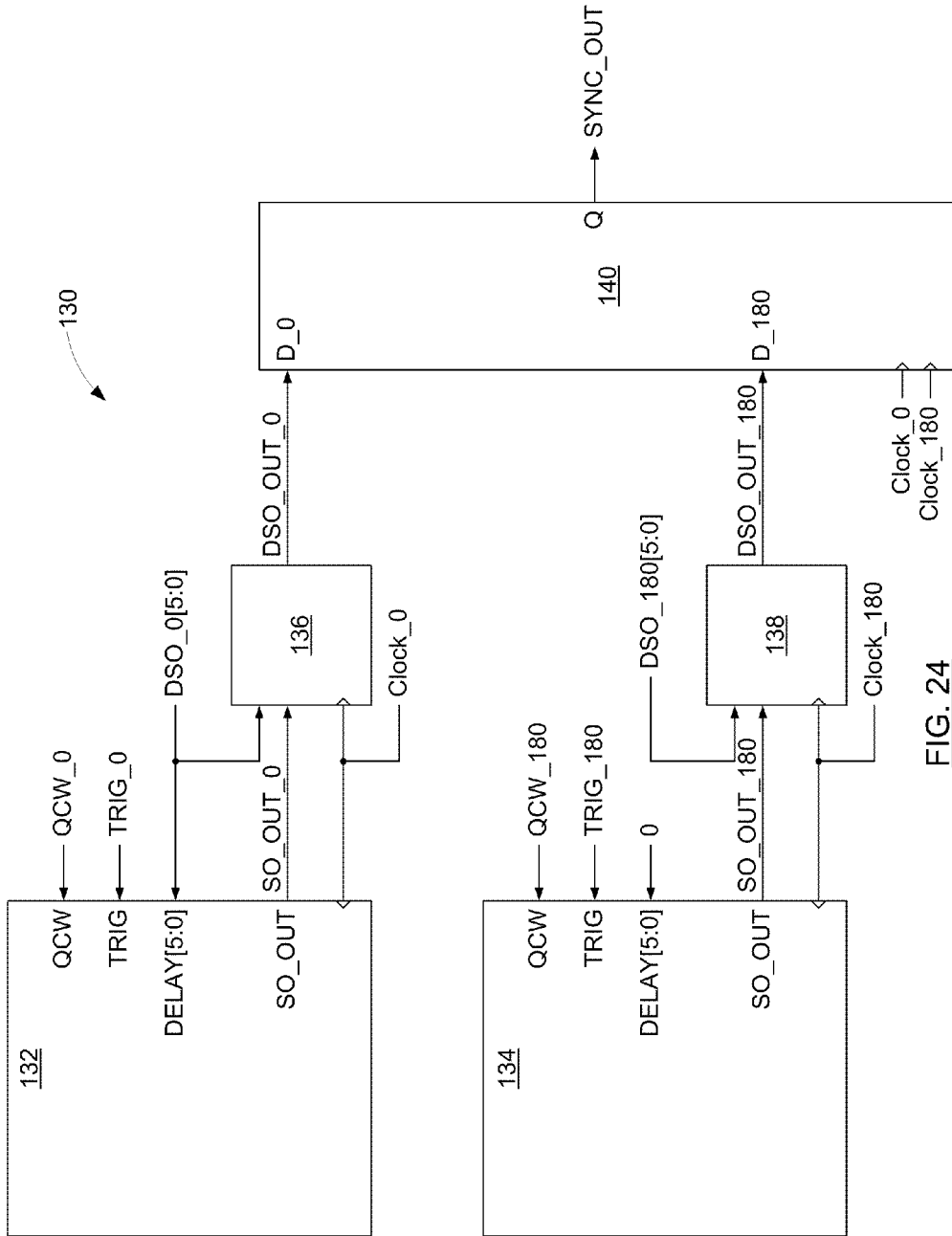


FIG. 24

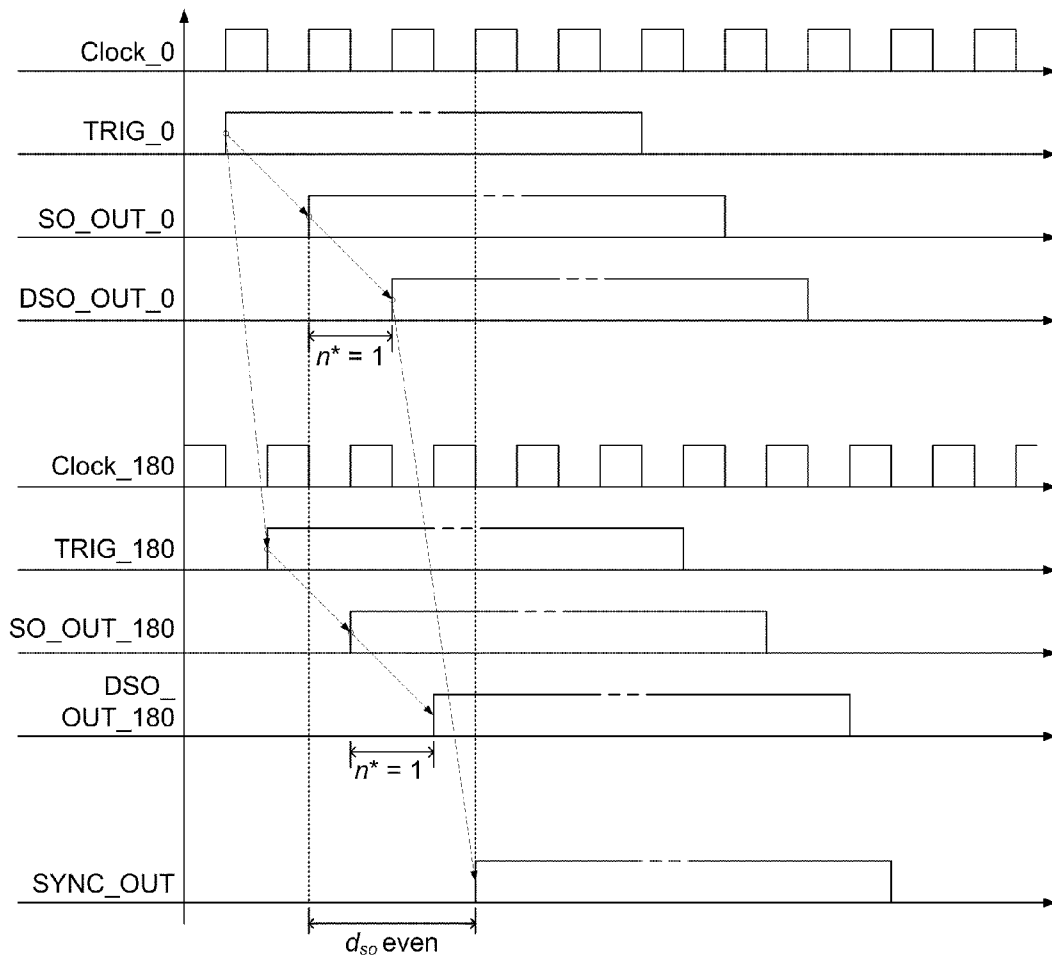


FIG. 25



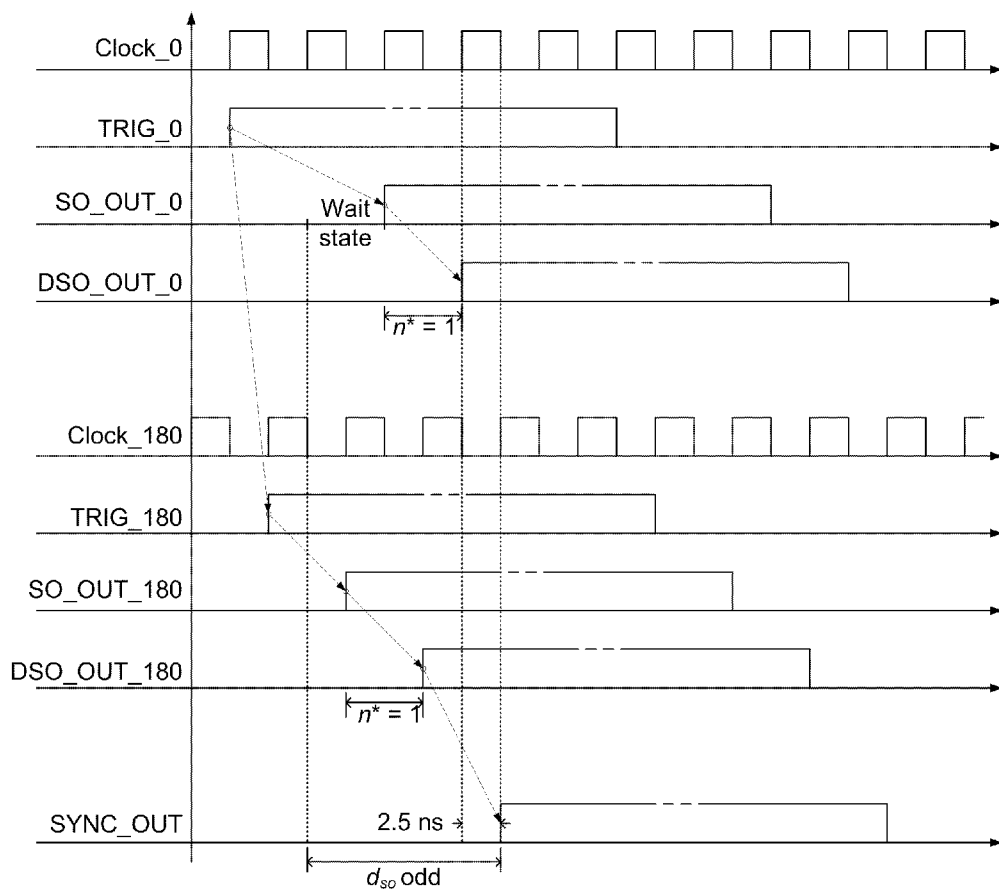


FIG. 26

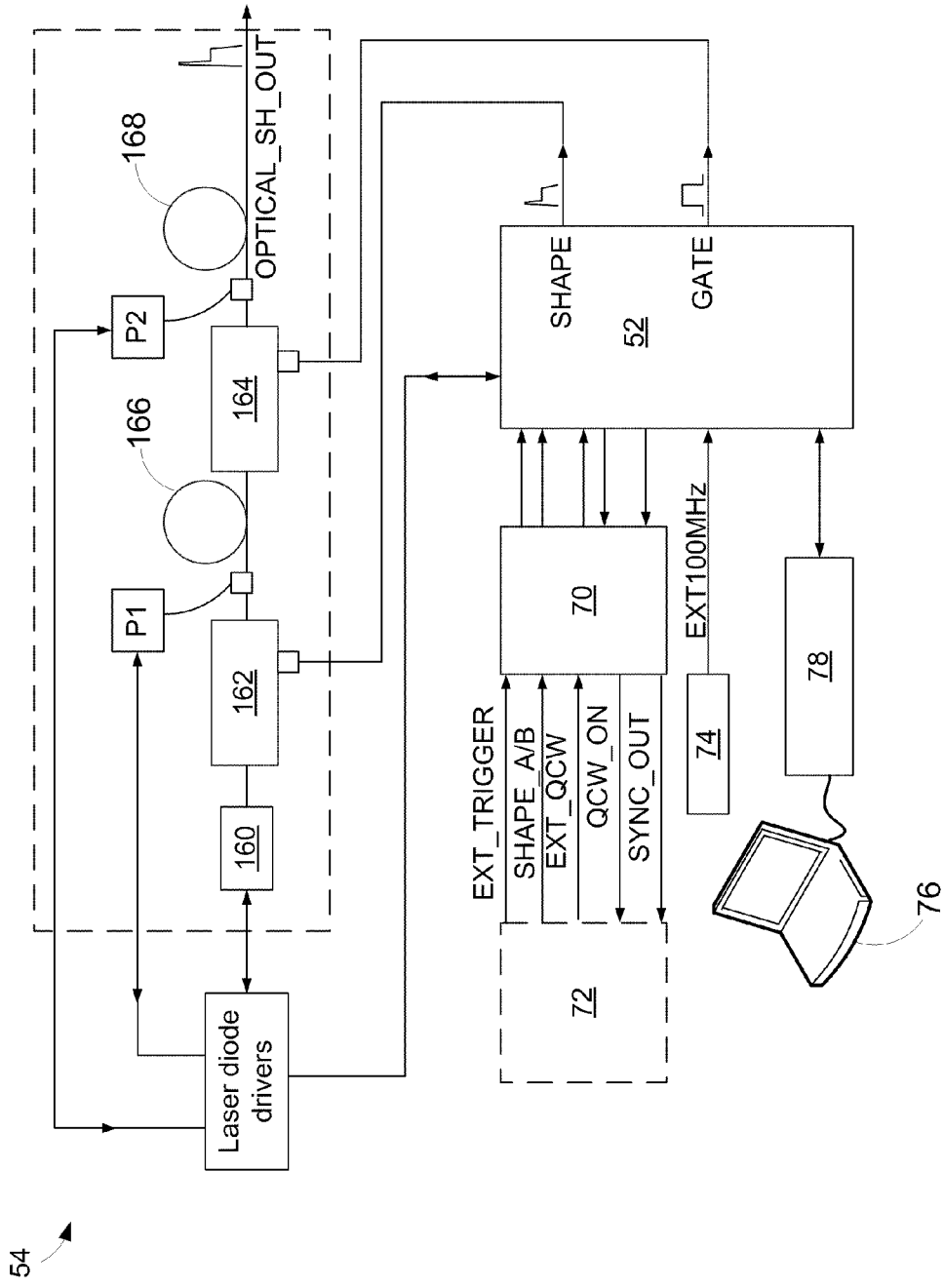


FIG. 27

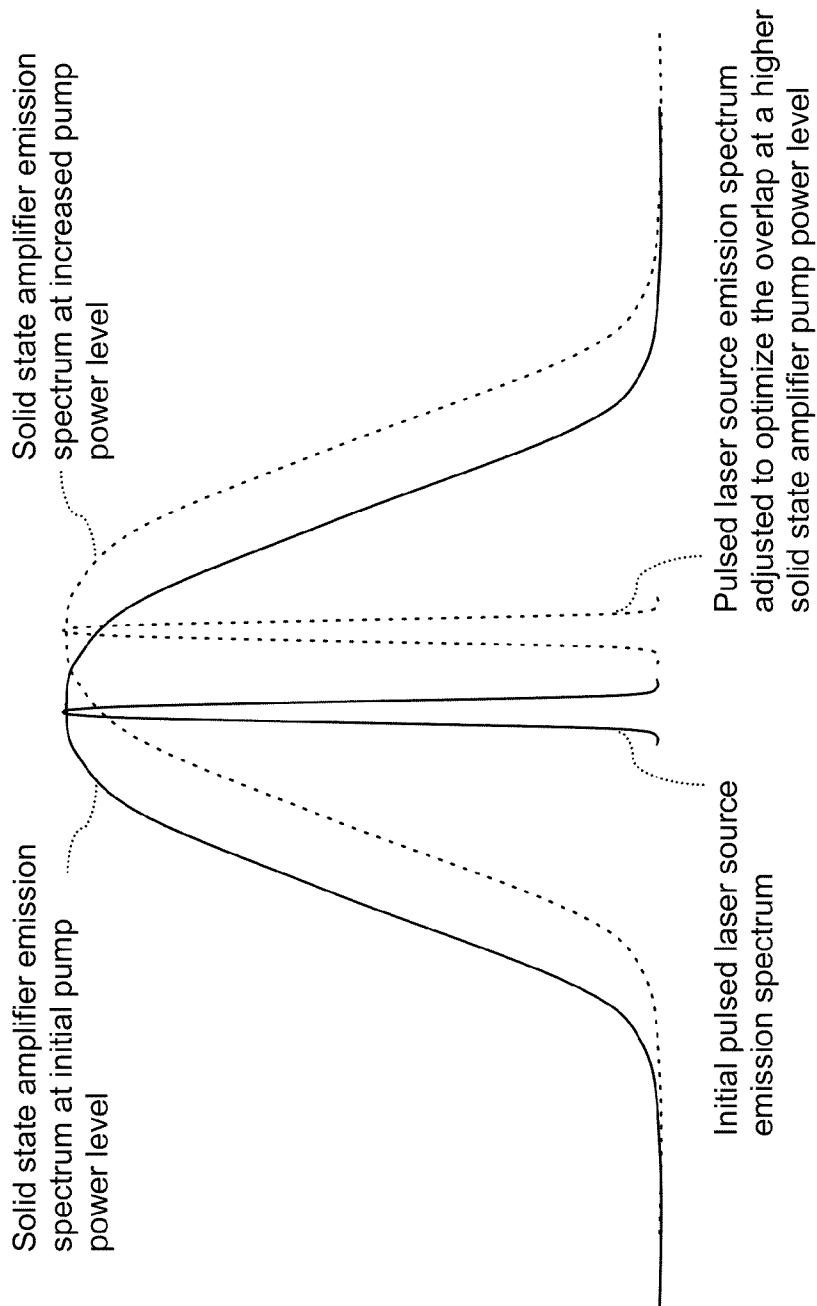


FIG. 28

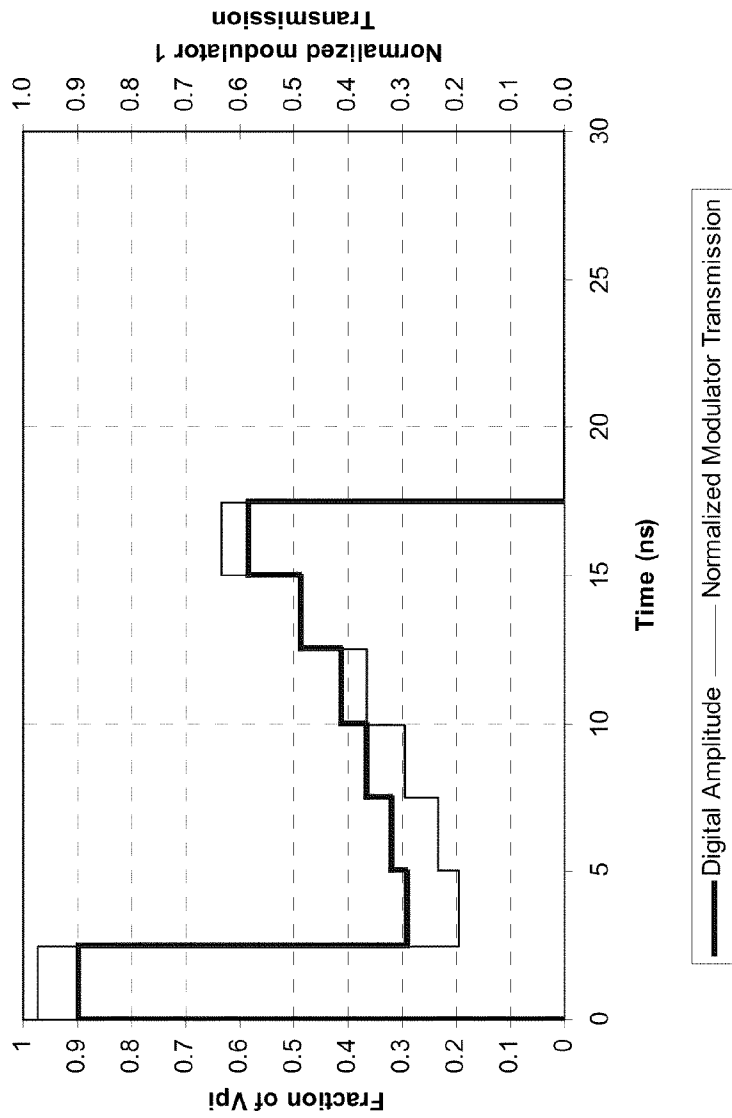


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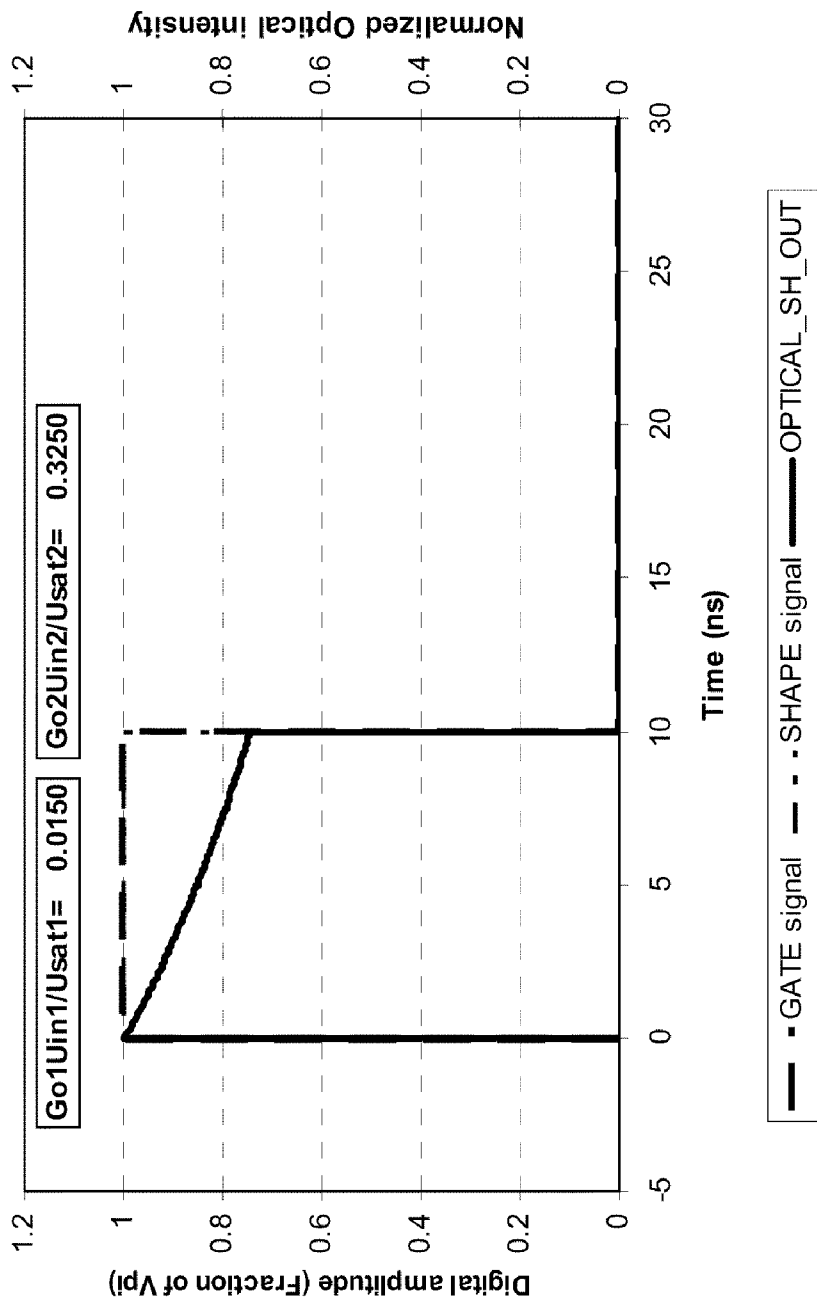


FIG. 30

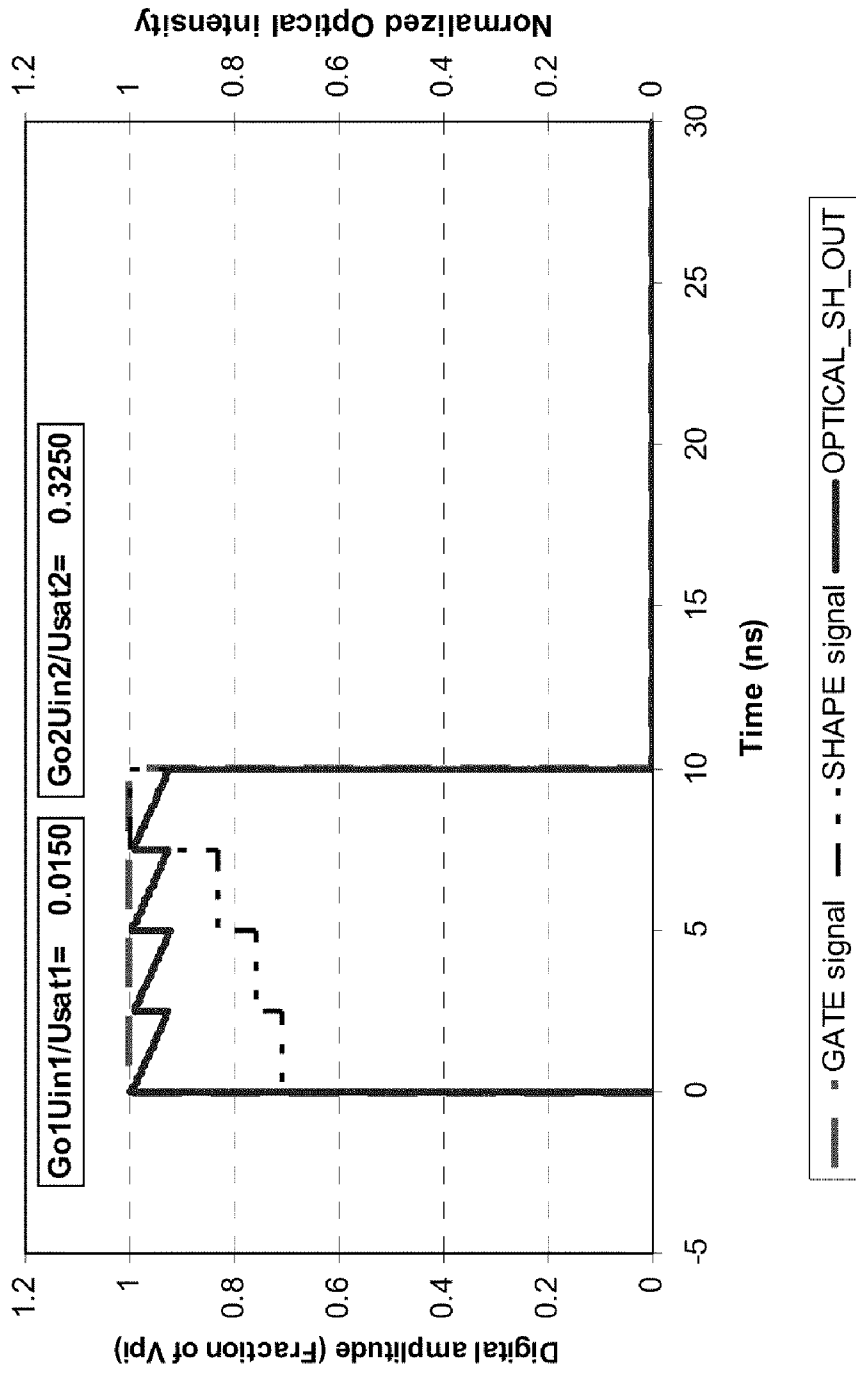
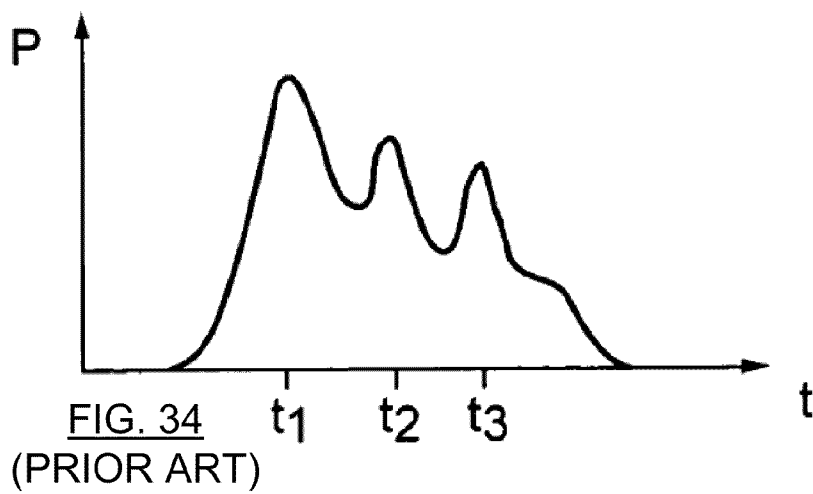
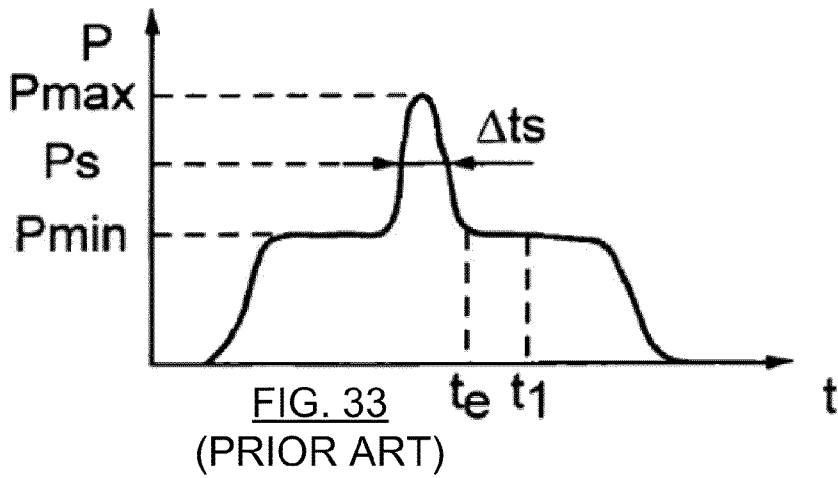
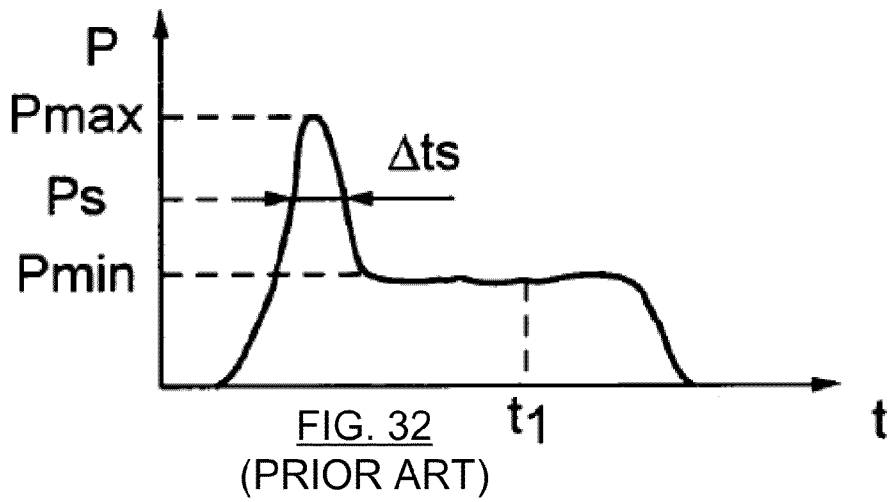


FIG. 31



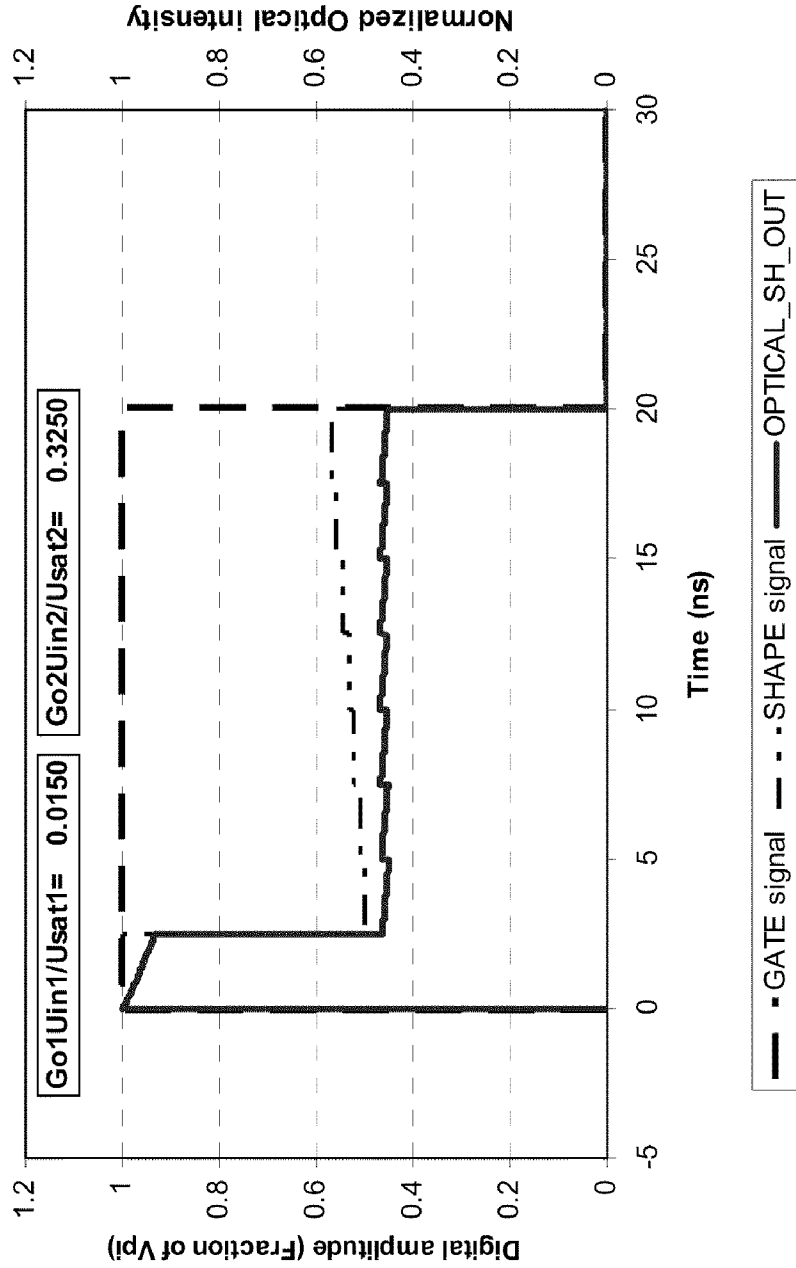


FIG. 35



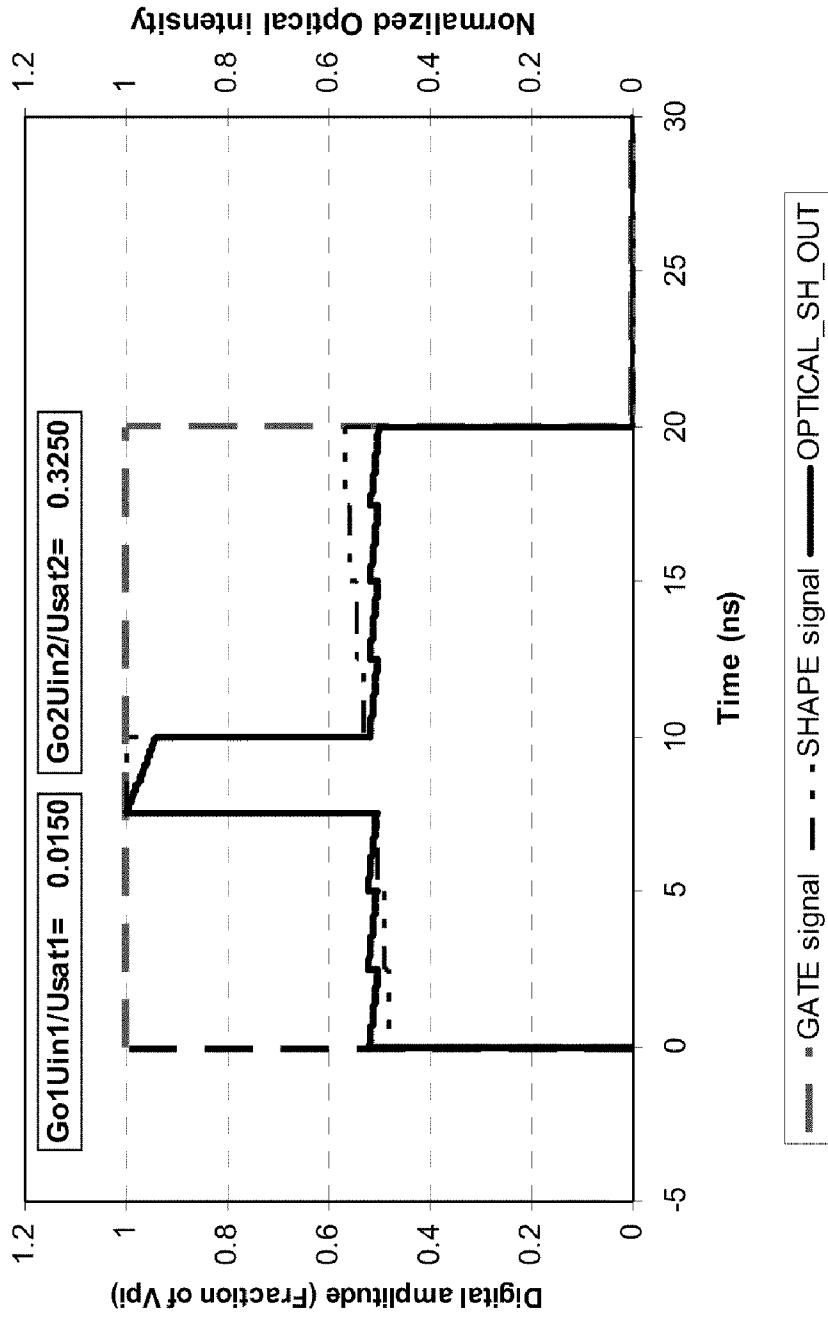


FIG. 36

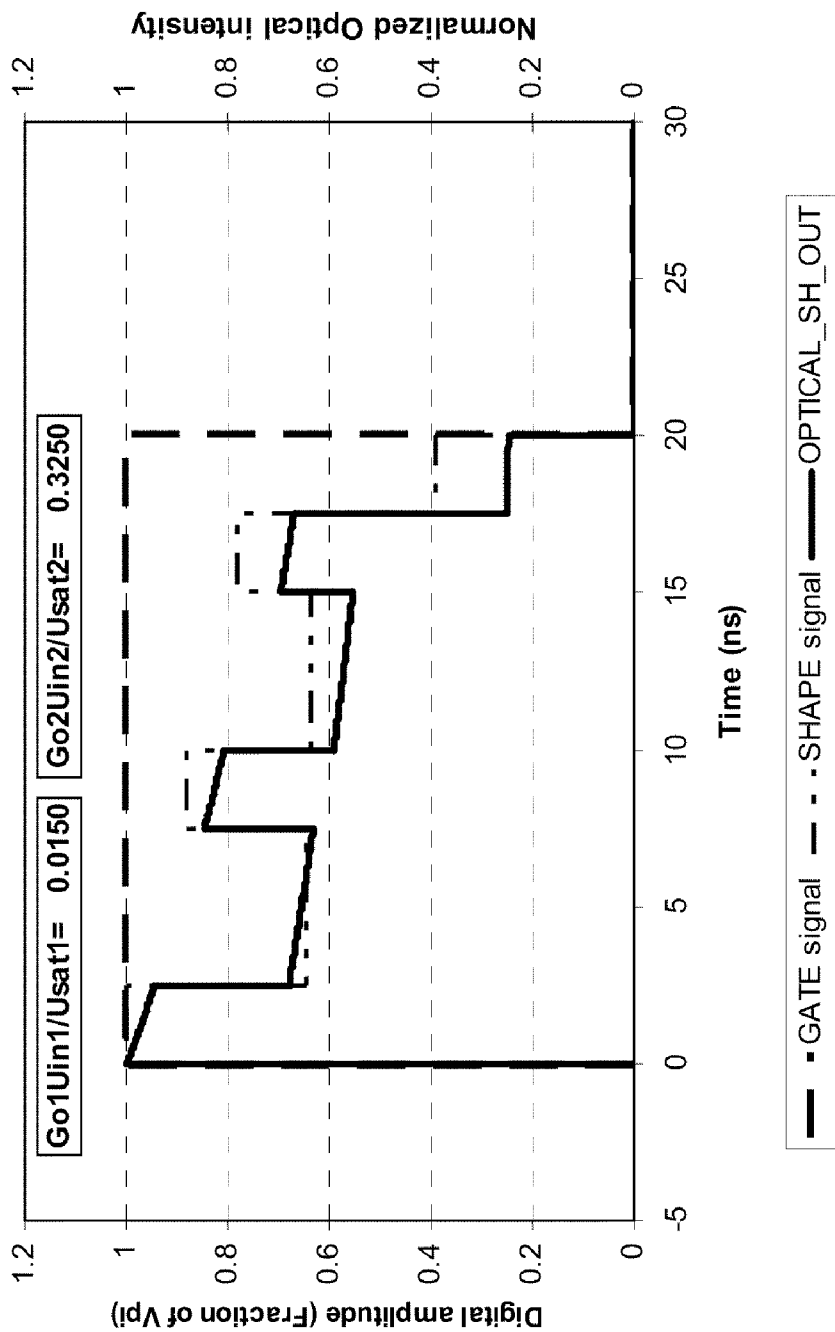


FIG. 37

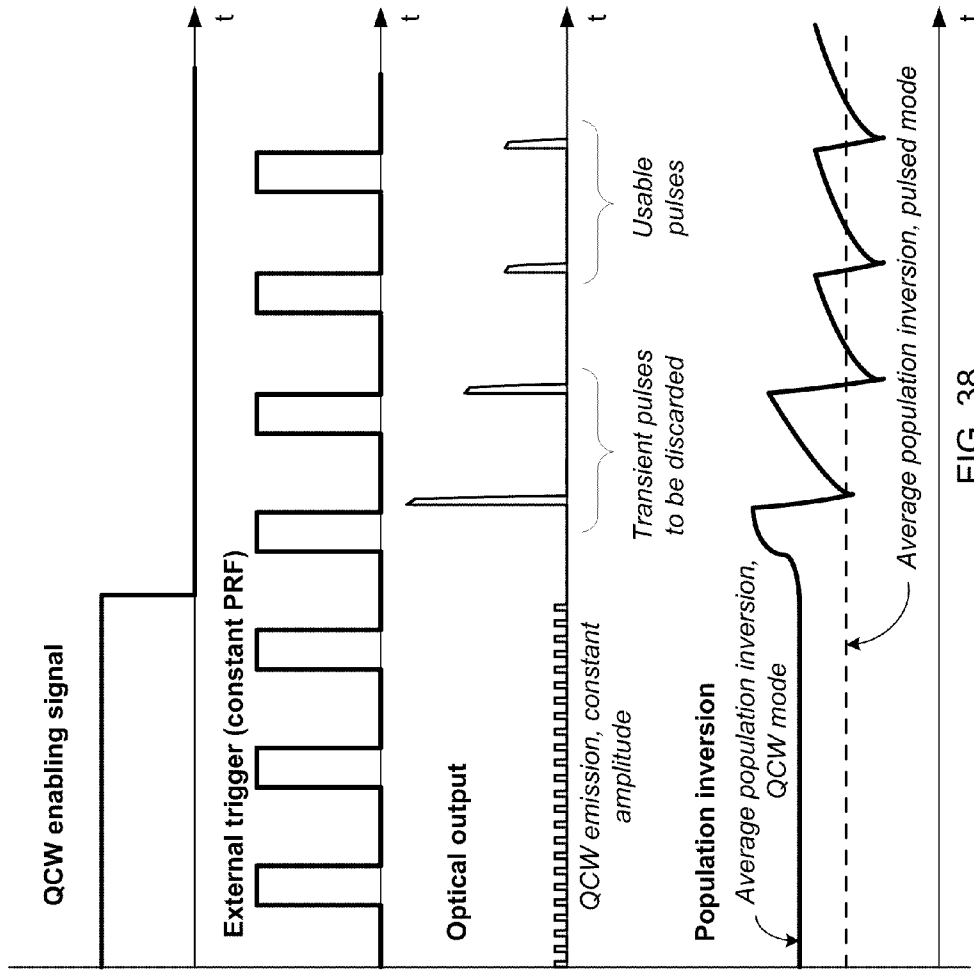


FIG. 38  
(PRIOR ART)

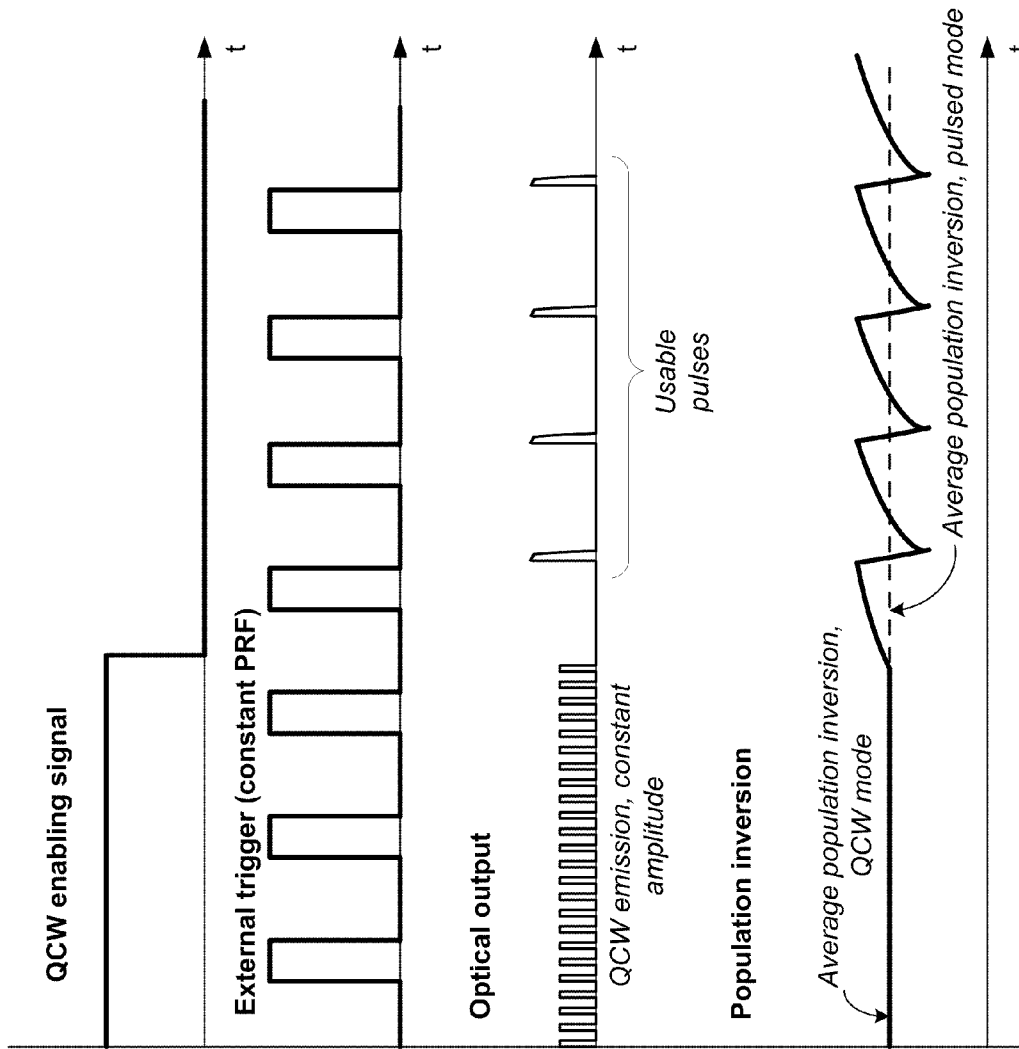


FIG. 39A

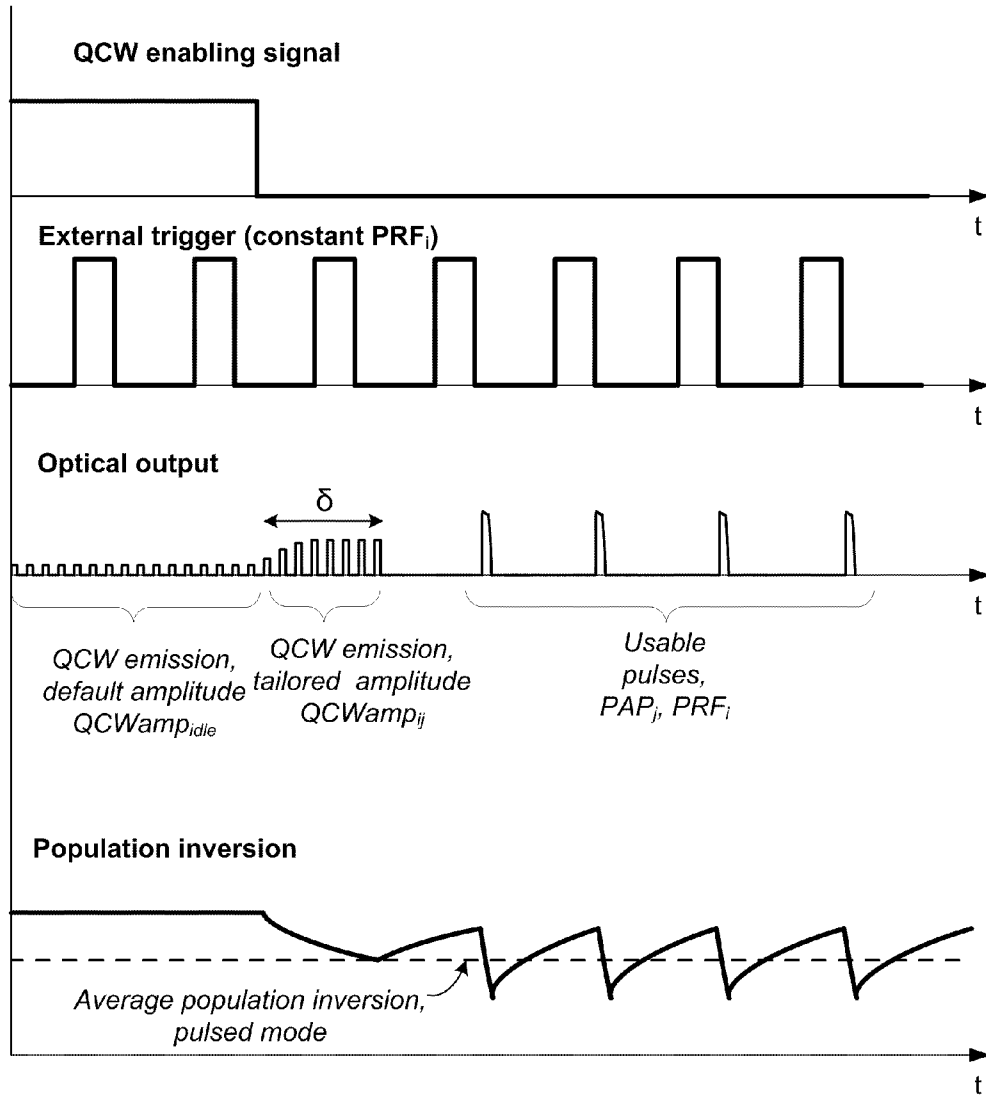


FIG. 39B

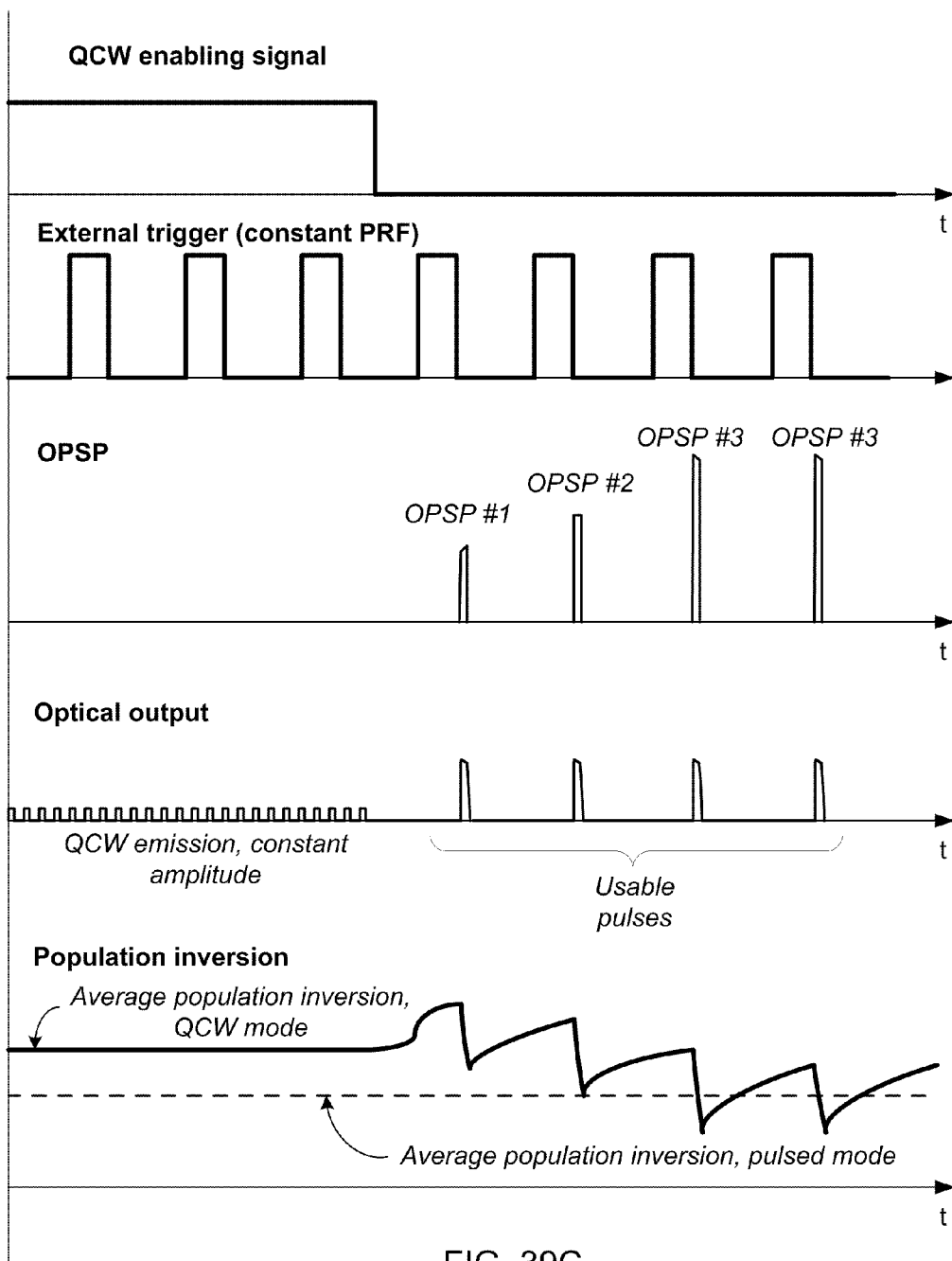
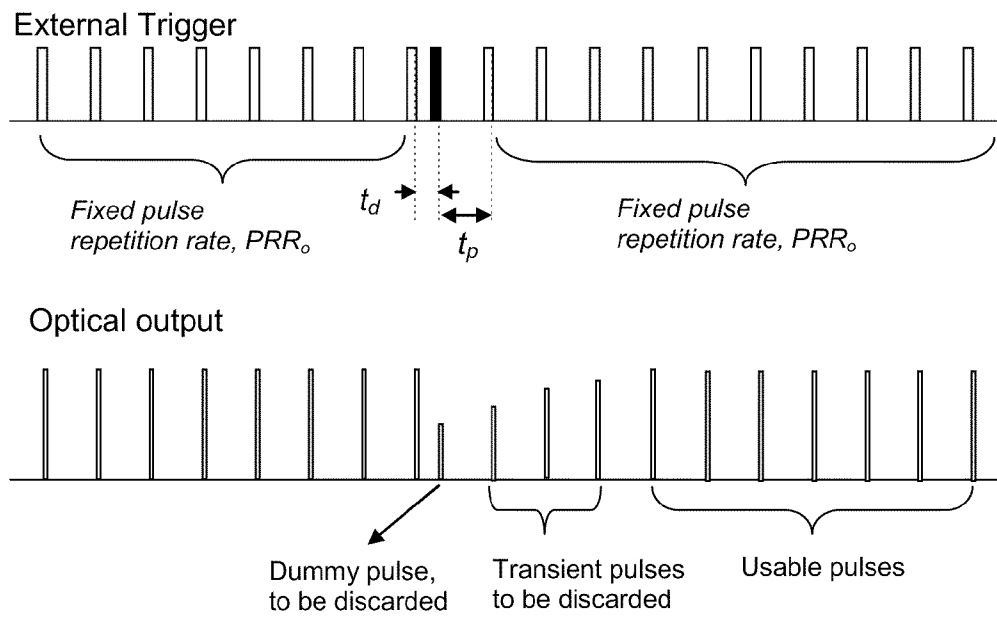


FIG. 39C



**FIG. 40**  
**(PRIOR ART)**

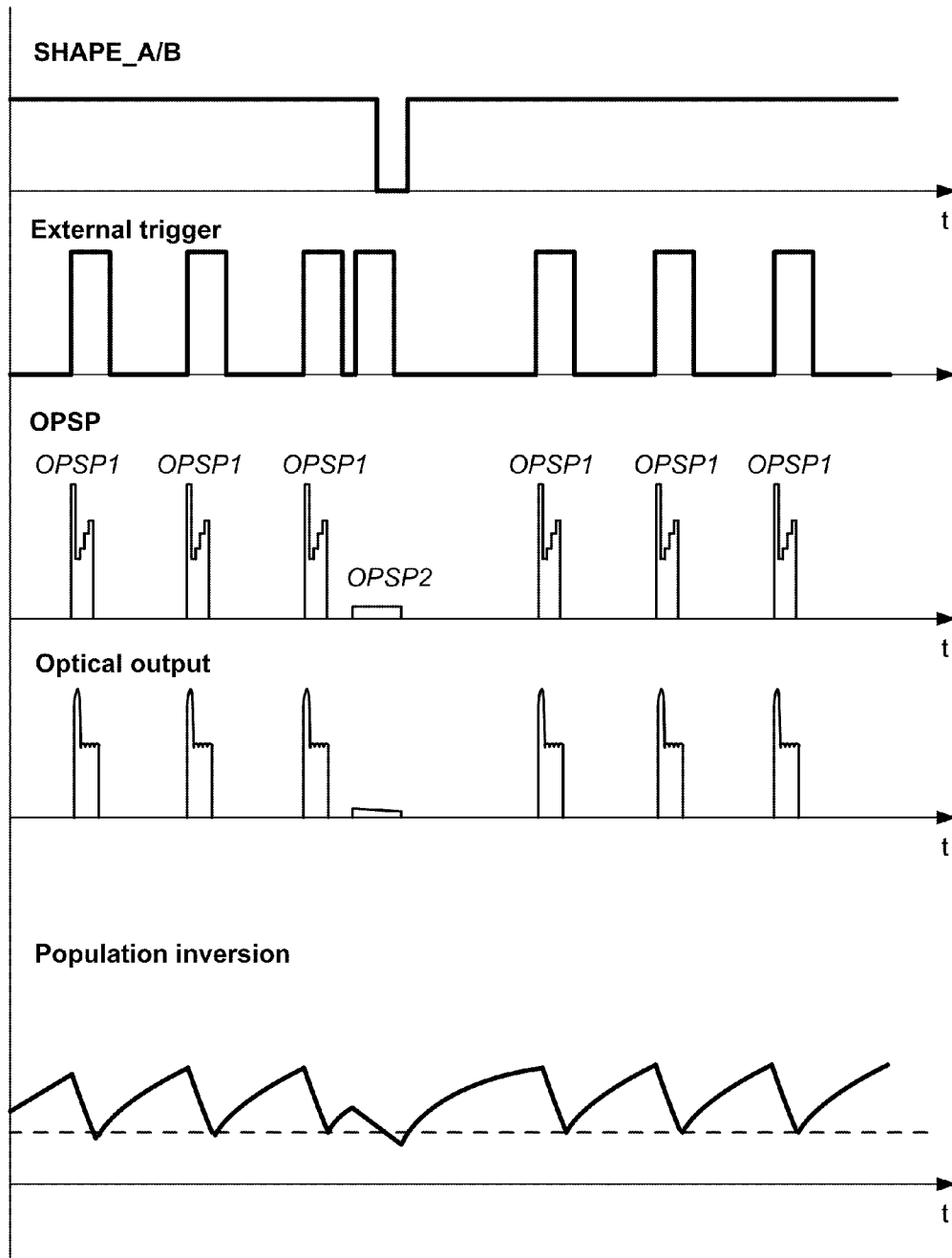


FIG. 41



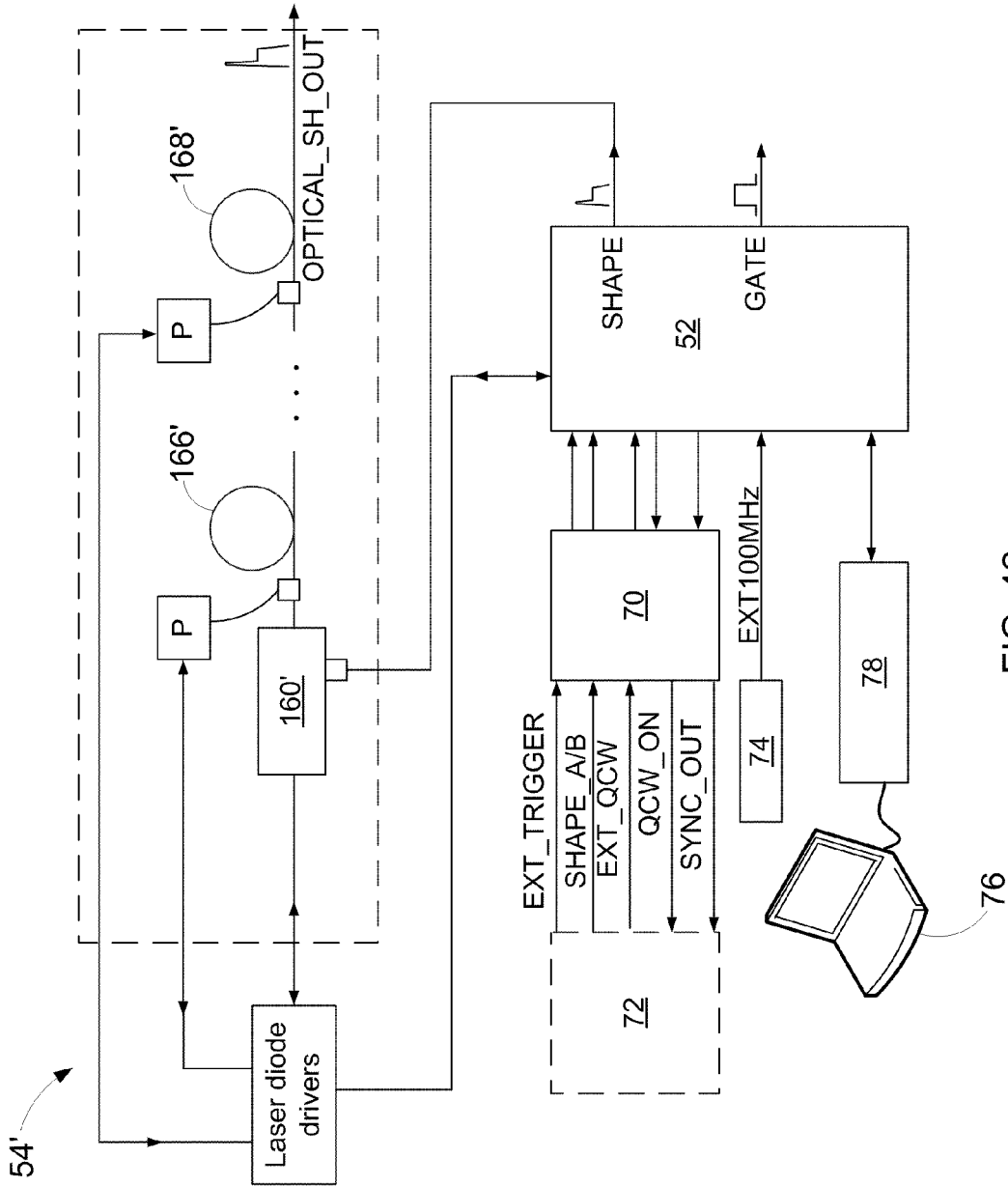


FIG. 42

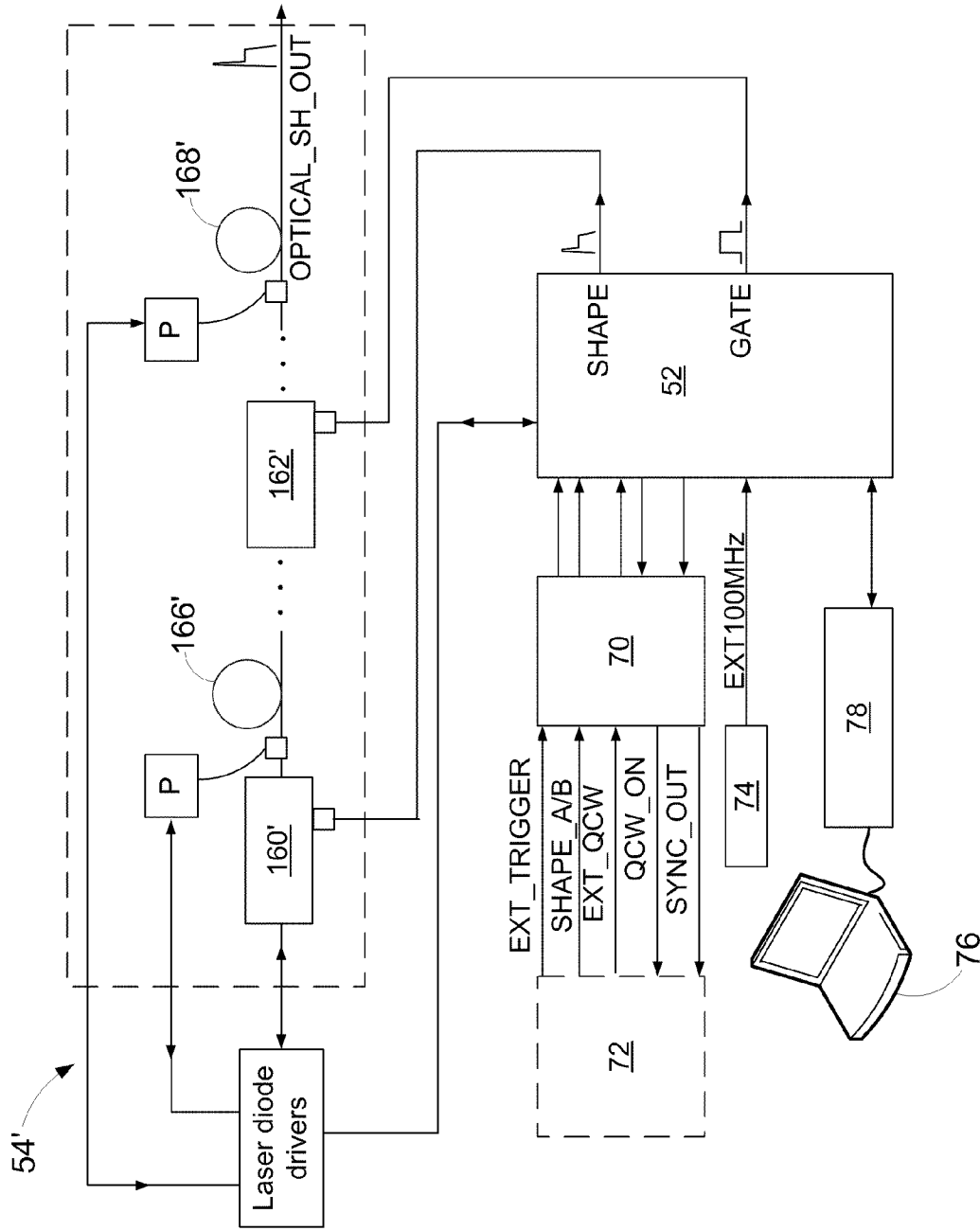


FIG. 43

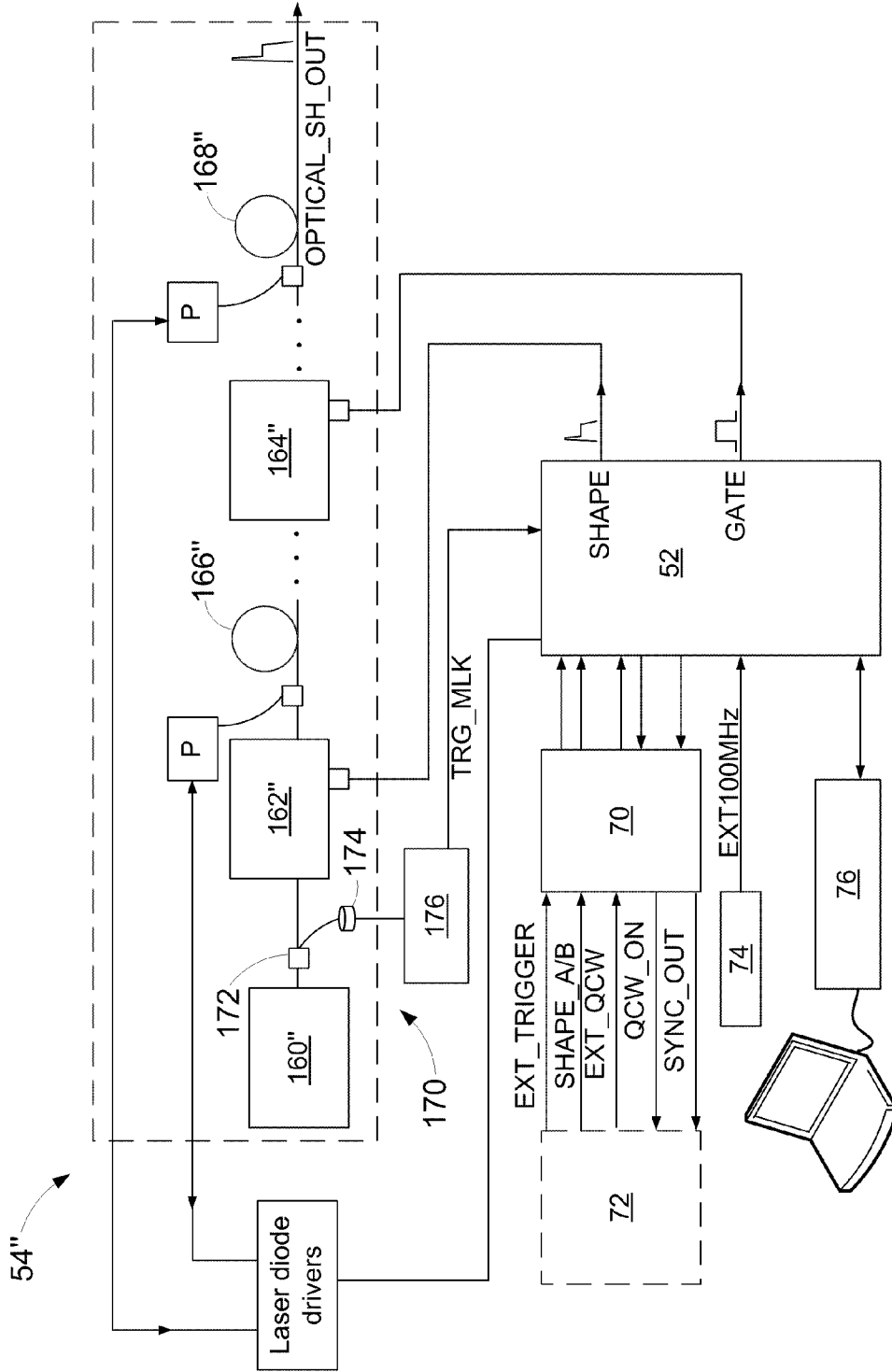


FIG. 44

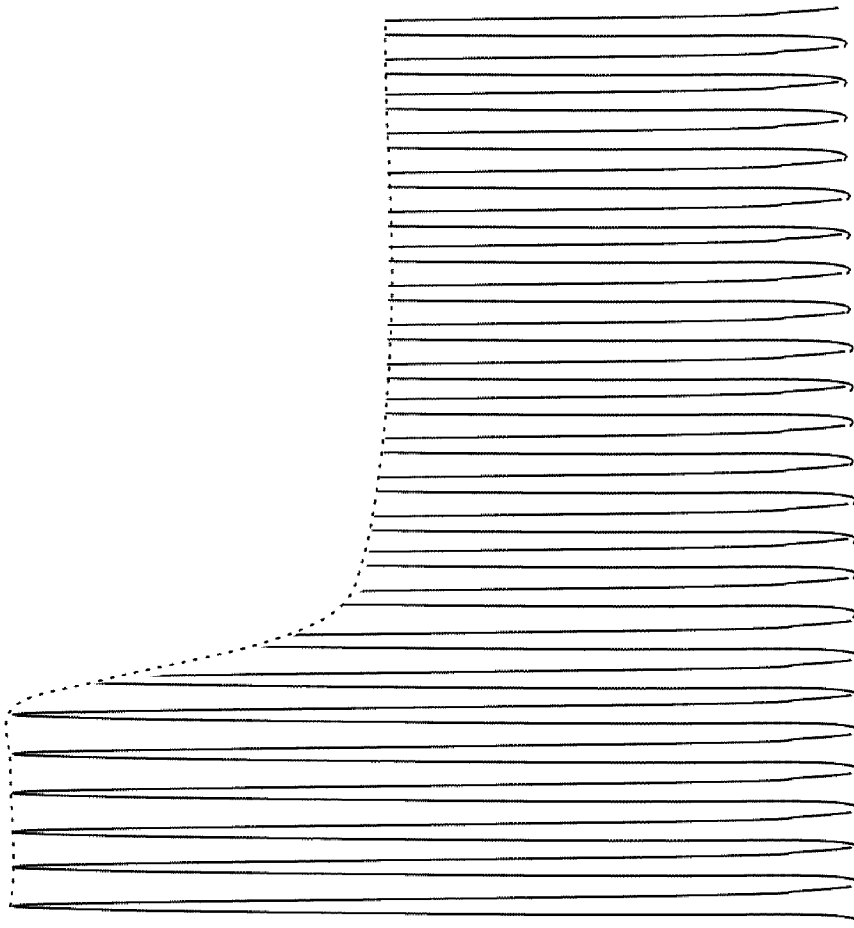


FIG. 45



Pulse Amplitude Profile (PAP)  Pulse Repetition Frequency (PRF) 	PAP #1	PAP #2	PAP #3	PAP #m-1	PAP #m
PRF #1	QCWamp <sub>11</sub>	QCWamp <sub>12</sub>	QCWamp <sub>13</sub>	QCWamp <sub>1(m-1)</sub>	QCWamp <sub>1m</sub>
PRF #2	QCWamp <sub>21</sub>	QCWamp <sub>22</sub>	QCWamp <sub>23</sub>	QCWamp <sub>2(m-1)</sub>	QCWamp <sub>2m</sub>
PRF #3	QCWamp <sub>31</sub>	QCWamp <sub>32</sub>	QCWamp <sub>33</sub>	QCWamp <sub>3(m-1)</sub>	QCWamp <sub>3m</sub>
PRF #4	QCWamp <sub>41</sub>	QCWamp <sub>42</sub>	QCWamp <sub>43</sub>	QCWamp <sub>4(m-1)</sub>	QCWamp <sub>4m</sub>
PRF #5	QCWamp <sub>51</sub>	QCWamp <sub>52</sub>	QCWamp <sub>53</sub>	QCWamp <sub>5(m-1)</sub>	QCWamp <sub>5m</sub>
PRF #6	QCWamp <sub>61</sub>	QCWamp <sub>62</sub>	QCWamp <sub>63</sub>	QCWamp <sub>6(m-1)</sub>	QCWamp <sub>6m</sub>
...	...	...	...	...	...
PRF # n-1	QCWamp <sub>(n-1)1</sub>	QCWamp <sub>(n-1)2</sub>	QCWamp <sub>(n-1)3</sub>	QCWamp <sub>(n-1)(m-1)</sub>	QCWamp <sub>(n-1)m</sub>
PRF #n	QCWamp <sub>n1</sub>	QCWamp <sub>n2</sub>	QCWamp <sub>n3</sub>	QCWamp <sub>n(m-1)</sub>	QCWamp <sub>nm</sub>

FIG. 46

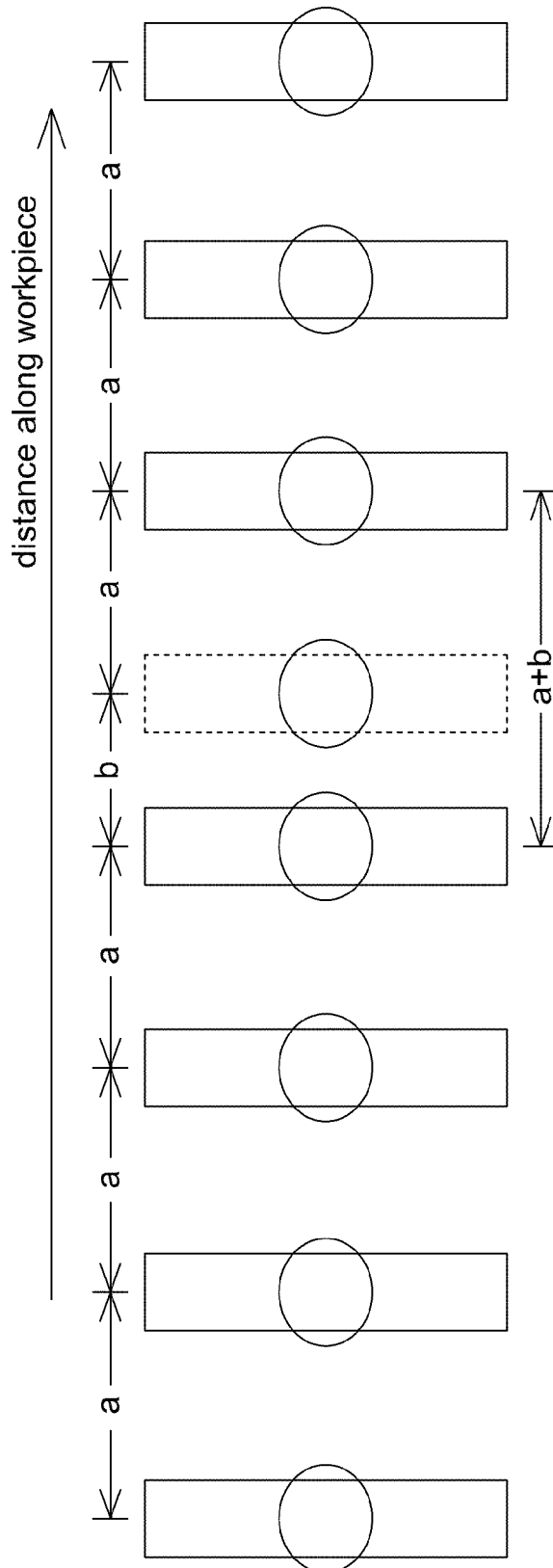


Fig. 47

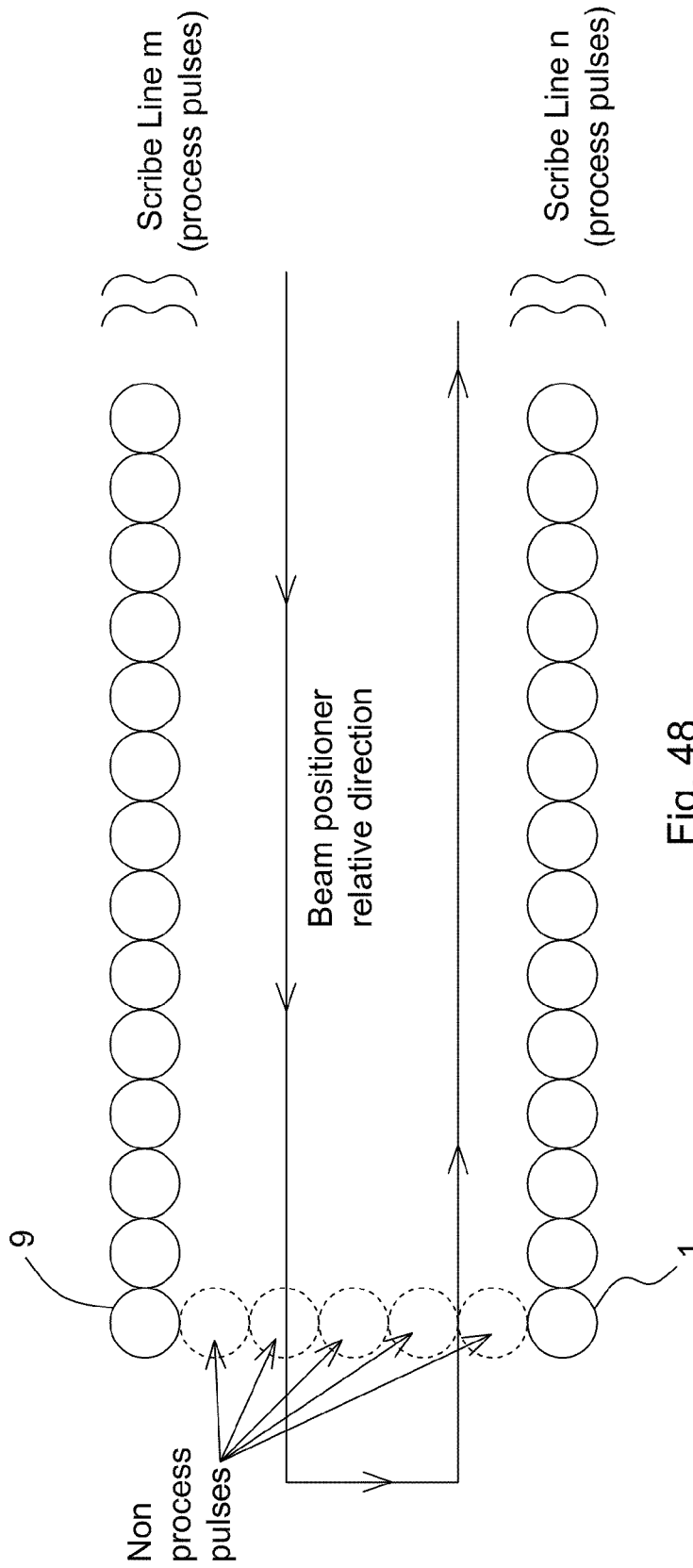


Fig. 48

Pulse Process	Pulse 1	Pulse 2	Pulse 3	· ·	Pulse n	· ·	Pulse q
Interline move 1	OPSP2	OPSP3	OPSP3		OPSP4		OPSP1
Scribe line 1	OPSP1	OPSP1	OPSP1		OPSP1		OPSP1
Interline move 2	OPSP2	OPSP3	OPSP3		OPSP4		OPSP1
Scribe line 2	OPSP1	OPSP1	OPSP1		OPSP1		OPSP1
·							
·							
·							
Scribe line n	OPSP1	OPSP1	OPSP1		OPSP1		OPSP1

FIG. 49



## METHODS FOR STABILIZING THE OUTPUT OF A PULSED LASER SYSTEM HAVING PULSE SHAPING CAPABILITIES

This application is a Continuation-in-Part of U.S. Ser. No. 12/493,949 filed 29 Jun. 2009, which claims benefit of U.S. Ser. No. 61/076,337, filed 27 Jun. 2008 and which application(s) are incorporated herein by reference. To the extent appropriate, a claim of priority is made to each of the above disclosed applications.

### FIELD OF THE INVENTION

The present invention relates to the field of laser devices and more particularly concerns methods which stabilize the output of a pulsed laser system controlled by a platform having the capability to provide pulse shaping control.

### BACKGROUND

Many industrial fields require laser processing capability and for such applications, the primary concern is often to generate optical laser pulses with, to some extent, real-time control over the pulse amplitude, duration, shape, peak power and repetition rate. In some applications, such as laser-based material-processing, the rise time and fall time of the shaped optical pulses are also important functional specifications.

U.S. Pat. No. 7,348,516 (SUN et al.), entitled "Methods of and laser systems for link processing using laser pulses with specially tailored power profiles" presents many arguments in favour of pulsed laser systems providing fine control over the pulse temporal power profile in the nanosecond regime, for facilitating better link process quality and yield. Three different laser architectures providing a certain control over the laser pulse shapes are described therein. U.S. Pat. No. 7,126,746 (SUN et al.) further teaches a laser system providing control over the pulse shapes and having a Master Oscillator Power Amplifier (MOPA) configuration.

U.S. Pat. No. 6,281,471 (SMART), entitled "Energy-efficient, laser-based method and system for processing target material" describes many requirements and specifications concerning the temporal generation of square laser pulse shapes in material processing. The system presented therein includes, among its main components, a controller for generating a processing control signal, and a signal generator for generating a modulated drive waveform based on the processing control signal.

Optical pulse shaping implementation can originate from digital electronic means, where some electronic apparatus reads a given sequence of digital samples previously stored in a memory buffer, and writes these samples into a digital-to-analog converter (DAC). The shaped analog signal output by the DAC is then fed to a buffer amplifier having enough bandwidth and drive capability for directly modulating a light source such as a laser diode, or driving an electro-optic modulator.

One challenge which arises in pulsed laser systems providing a great flexibility over the conditions of operation (repetition rate, pulse shape, etc.) is that transient effects often occur in the laser system when these conditions of operation modify the population inversion in gain media found in this system, such as the gain medium of a laser diode or one used as an amplifier. For example, when the repetition rate of a laser is suddenly reduced, a number of pulses following the transition will have more energy than the steady state pulse energy at the modified repetition rate. It can be important to mitigate these transient effects in order to avoid detrimental

consequences for a given application using the laser output. For example, in semiconductor memory processing applications where a single pulse is used to process a given structure, such as severing conductive links for memory repair, it is important to keep the pulse energy within a well defined energy per pulse process window. If the pulse energy is too low, then the link may be incompletely removed.

In cases where the energy per pulse exceeds the allowable energy process window, excess pulse energy may be coupled into adjacent or underlying link structures, or the substrate itself, causing highly undesirable damage to the device. In multiple pulse laser processes, such as laser drilling of microvias in semiconductors, or laser scribing of thin film photovoltaic devices, it is important that successive pulses remain substantially uniform in order to produce laser processed features that possess the desired dimensions and feature surface quality. The control of the laser transient response is also important in laser surgery, as the amount of energy deposited in living tissues must be accurately controlled in order to avoid inducing damages to the neighbouring tissues.

High value is placed upon the throughput of work pieces satisfactorily produced by a laser processing system. Therefore, methods to achieve pulse stabilization in lasers, particularly in lasers employed in laser processing or medical laser systems, are highly desirable. When laser processing device target structures whose layout requires laser processing pulses to be emitted at interpulse periods which are not fixed, methods for generating laser processing pulses with substantially equal pulse amplitude and energy per pulse are highly desirable. Industrially important laser applications, such as laser repair of dynamic random access memory (DRAM), laser scribing of photovoltaic cells, and laser drilling of microvias in semiconductor, flexible interconnects, IC packages, dielectrics, including glass, and metals, typically require pulsed laser output characterized by more than one characteristic interpulse period. As those skilled in the art will appreciate, q-switched solid state lasers and pulsed fiber lasers are commonly employed in these and similar laser processing applications in which laser processing pulses are required to be emitted under such conditions. Therefore, improved techniques for pulse stabilization of advanced pulse laser sources are of substantial interest and value to practitioners in such industries.

Previous workers have described methods for mitigating pulse transients, such as actively controlling the pump power level in the gain medium of a solid state medium so as to control the population inversion dynamics. For example, DAVENPORT et al, in U.S. Pat. No. 5,151,909 disclose laser systems having programmable pump power modes to actively control the amount of power delivered by solid state lasers. In one embodiment disclosed by DAVENPORT et al, the pump control capability solves a problem for transitions between different modes of operation of the laser. However, for several material processing applications where the laser beam quality is critical, such an approach would bring detrimental beam quality fluctuations and beam pointing issues induced by thermal changes in the laser system, originating from varying gain medium pumping conditions.

CHAN et al in U.S. Pat. No. 5,226,051, entitled "Laser Pump Control for Output Power Stabilization" teach a method for modulating the pump power supplied to the laser in a diode-pumped q-switched solid state laser, such that pulses emitted with differing interpulse periods will possess comparable pulse energy and pulse amplitude. In this method, the pump power is delivered at a first high value for a time  $\tau_p$ . If the laser receives a signal within a  $t < \tau_p$ , then a laser pulse is emitted by the laser with the energy stored

during time  $t$ . In the case where  $t > \tau_p$ , then the pump current is reduced to a pre-programmed lower value, which may be time dependent, to maintain the stored energy at a pre-determined level to keep the laser pulse output at a specific value. As those skilled in the art will appreciate, this method can advantageously produce a laser pulse output with substantially equal pulse energy and pulse amplitudes over a wide dynamic range of interpulse periods by substantially equalizing the stored energy independently of the interpulse period. As those skilled will further recognize, a key limitation of this technique is the maximum energy per pulse that may be produced, as the energy per pulse and pulse amplitude substantially correspond to that produced at the shortest interpulse period. This limitation on the maximum energy per pulse becomes increasingly severe with increasing pulse repetition frequency (see Koechner, "Solid-State laser engineering", Springer-Verlag, Chap. 8. FIG. 8.8). For maximum Pulse Repetition Frequencies (PRF) substantially greater than about 30 KHz, the resultant efficiency losses introduced by the method of CHAN et al are unacceptable for most industrial applications.

For laser applications requiring very high throughput of laser processed workpieces, methods which employ pulse picking techniques are commonly employed to achieve stable laser processing output are often employed. BAIRD et al in U.S. Pat. No. 6,172,325, "Laser Processing Power Output Stabilization Apparatus and Method Employing Processing Position Feedback", describe the use of a q-switched solid state laser which operates in cooperation with a pulse processing control system that employs an autopulse mode and a pulse on position mode to stabilize the laser output delivered to dynamic target locations on a workpiece moved by a positioner. To overcome the deficiencies of the prior art of CHAN et al, the laser pulses are consistently emitted at a near maximum PRF in an autopulse mode in order to maintain a stable laser output. In the autopulse mode, an external optical modulator, such as an acousto-optic modulator, blocks the laser output and prevents the pulses from reaching the workpiece targets. In the pulse-on-position mode, the laser emits a pulse each time the beam position moves through a target workpiece coordinate position. The processing control system commands the external optical modulator to an "open" position whenever the target workpiece coordinate position matches a position requiring a process pulse to be transmitted to the target workpiece. The essence of this technique is that it provides a near constant interpulse period for the laser, thereby resulting in stable pulse output for processing of target workpieces. As can be appreciated by those skilled in the art, this technique is particularly suitable for the employment of lasers utilizing harmonic output since the harmonic output from a frequency converted laser is very sensitive to any variations in the fundamental input to the nonlinear conversion elements, including pulse repetition frequency and interpulse period variations.

For laser processing of target structures arrayed periodically, such as DRAM link structures, it is often useful to employ pulse triggering techniques which cause the emission of one or more process correction pulses for the purpose of introducing temporal offsets to the process interpulse period. BRULAND et al, in US Patent Application No. 2007/0228024 A1, "Methods and systems for decreasing the effective pulse repetition of a laser" describe techniques for emitting various types of process correction pulses, referred to by BRULAND et al as dummy pulses and pre-pulses. A dummy pulse is a pulse triggered coincidentally with a target position but which is blocked by an external optical modulator from propagating to strike target on the workpiece, for example for

structures where only selective targets are to be actually processed. Typically, dummy pulses are blocked by an external optical modulator, such as an acousto-optical modulator or electro-optical modulator, from propagating to strike the target structures on the workpiece.

The purpose of dummy pulse emission is to maintain laser emission at a near constant  $PRF = \text{velocity}_{stage} / \Delta x_{target\ pitch}$ . However, as described by BRULAND et al, for the case of laser processing of memory links, beam positioning requirements to move to neighboring link banks may require changes in stage velocity. In addition, other positioning offsets may be present. Therefore, to achieve stable laser process pulse output, the laser may be commanded to emit non-process pre-pulses in addition to dummy pulses and process pulses. The key attribute of non-process pre-pulses is to introduce a positive or negative interpulse time offsets which yield emission of dummy pulses and process pulses at an interpulse time  $= 1 / PRF_{process}$ , thereby substantially improving the uniformity of the energy per pulse and pulse amplitude of the process pulses.

Intrinsic to the methods described by BAIRD et al and BRULAND et al is the use of a pulsed laser source capable of producing stable pulse output following a dynamic change in the interpulse period. BAIRD et al teach the use of Q-switched solid state lasers, including diode-pumped Q-switched solid state lasers and further teach that lasers which produce harmonic converted outputs may be advantageously employed. BRULAND et al similarly teach the use of a Q-switch laser in some embodiments. In another embodiment, BRULAND et al describe employing a semiconductor laser in a pulsed fiber laser configured in a master oscillator power amplifier configuration. In this embodiment, BRULAND et al mention that a semiconductor laser employed as a master oscillator to pump the gain fiber in the master oscillator power amplifier configuration can be controlled to create the pre-pulse and process pulse at the appropriate times. BRULAND et al provide no further teaching concerning the construction or operation of such a configuration.

In spite of the advances in the art described above, there remains a need for improved methods to provide efficient transient response in the field of pulsed laser oscillators and pulsed laser oscillator-amplifiers which are capable of substantial pulse shape and pulse repetition rate flexibility.

#### SUMMARY OF THE INVENTION

In accordance with one aspect of the invention, there is provided a method for stabilizing an output of a pulsed laser system following a transition from a QCW regime to a pulse shaping regime. In the QCW regime, the output is controlled by a periodic signal and has a maximum energy lower than a processing threshold, whereas in the pulse shaping regime, it is controlled by a pulse shaping signal and has a maximum energy above the processing threshold. The method includes a step of controlling a duty cycle and an amplitude of the periodic signal when the pulsed laser system is in the QCW regime to output an average QCW optical power substantially corresponding to an average pulsed optical power outputted in a steady-state operation of the pulsed laser system in the pulse shaping regime.

The controlling step above may be performed for an entire duration of the QCW regime or for a transitory period immediately preceding the pulse shaping regime.

In accordance with another aspect of the present invention, there is also provided another method for stabilizing an output of a pulsed laser system following a transition from a QCW regime, wherein the output is a periodic signal, to a pulse

shaping regime, wherein the output is controlled by a pulse shaping signal defined by a pulse repetition frequency and a pulse amplitude profile. The method includes:

- a) providing a transitory period between the QCW and pulse shaping regimes. The transitory period has a duration substantially corresponding to a population inversion stabilization time of at least one gain medium in the laser system; and
- b) controlling the pulsed laser system during the transitory period with a transitory pulsed signal having the same pulse repetition frequency as the pulse shaping signal, and a pulse amplitude profile selected to provide substantially the same output as in the pulse shaping regime during the transitory period.

In accordance with yet another aspect of the invention, there is also provided a method for stabilizing an output of a pulsed laser system during consecutive series of process pulses. The consecutive series are separated by a time gap during which at least one non-process pulse is outputted. The output is controlled by a pulse shaping signal having a pulse amplitude profile. The method includes a step of adjusting the pulse amplitude profile of the pulse shaping signal for the at least one non-process pulse to affect available energy in at least one gain medium of the laser system, so that the laser system operates in a steady-state at the first process pulse of the series of process pulses subsequent to the time gap.

Embodiments of the invention may advantageously be realized through the use of a digital pulse shaping module for controlling a pulsed laser system according to a digital input waveform. The digital pulse shaping module may include a clock generator generating a plurality of phase-related clock signals at a same clock frequency, and a shape generator outputting a digital shape signal corresponding to the digital input waveform. The shape generator preferably operates in Double Data Rate in response to the clock signals; the digital pulse shaping module may also include a Digital-to-Analog Converter (DAC), receiving the digital shape signal and converting into an analog shape signal which is suitable for controlling the pulsed laser system. In cases where a QCW mode is required, the digital pulse shaping module may further include QCW means for outputting a quasi-continuous wave as the digital shape signal alternatively to the digital input waveform.

Other features and advantages of the invention will be better understood upon a reading of preferred embodiments thereof with reference to the appended drawings.

#### BRIEF DESCRIPTION OF THE DRAWINGS

FIG. 1 is a schematic representation of a pulsed laser system which includes a digital pulse shaping module.

FIG. 2 is a graph illustrating the timing relationships between input and output signals of the digital pulse shaping module of FIG. 1.

FIG. 3A is a block diagram of a digital pulse shaping module according to an embodiment of the invention; FIG. 3B is a block diagram of the shape generator of the digital pulse shaping module of FIG. 3A.

FIG. 4 is a graph schematically illustrating the phase-related clock signals outputted by a clock generator.

FIG. 5 shows a hysteresis output transition diagram for the frequency measurement module of the digital pulse shaping module of FIG. 3.

FIG. 6 schematically illustrates the architecture of an embedded microcontroller.

FIG. 7 schematically illustrates a command read/write register set.

FIG. 8 schematically illustrates a DDR implementation of an internal/external pulse trigger.

FIG. 9 is a timing diagram showing signal synchronization from the 50 MHz to the 200 MHz time-critical clock domains.

FIG. 10 schematically illustrates the DDR implementation of a QCW control sub-module.

FIG. 11 schematically illustrates the DDR implementation of a shape selection sub-module.

FIG. 12 schematically illustrates the DDR implementation of a delay control sub-module.

FIG. 13 schematically illustrates a dual-port pulse shape memory buffer.

FIG. 14 is a block diagram of a shape sub-module.

FIG. 15 is a logic flow chart of the operation of a shape state machine according to the embodiment of FIG. 14.

FIG. 16 is a timing diagram for the DDR generation of the 100 MHz QCW.

FIG. 17 is a timing diagram for the DDR generation of a user-defined arbitrary pulse shape.

FIG. 18 is a typical timing diagram of mode switching and pulse shape switching.

FIG. 19 is a block diagram of a gate sub-module.

FIG. 20 is a timing diagram schematically illustrating the DDR PREDR\_GATE output for w odd, n even.

FIG. 21 is a timing diagram schematically illustrating the DDR PREDR\_GATE output for w odd, n odd.

FIG. 22 is a timing diagram schematically illustrating the DDR PREDR\_GATE output for w even, n even.

FIG. 23 is a timing diagram schematically illustrating the DDR PREDR\_GATE output for w even, n odd.

FIG. 24 is a block diagram of the synchronization sub-module.

FIG. 25 is a timing diagram schematically illustrating the DDR SYNC\_OUT output for n even.

FIG. 26 is a timing diagram schematically illustrating the DDR SYNC\_OUT output for n odd.

FIG. 27 schematically illustrates a laser system including a pulsed laser source employing two electro-optic modulators and a seed laser diode operated in the CW regime.

FIG. 28 schematically illustrates the pulsed laser source emission wavelength tuning for amplification in a solid state optical amplifier.

FIG. 29 shows an example of optical shape at the output of the first modulator corresponding to a particular digital waveform (SHAPE signal).

FIG. 30 shows a typical pulse shape distortion resulting from optical gain saturation for rectangular SHAPE and GATE waveforms.

FIG. 31 shows an example of rectangular optical shape (OPTICAL\_SH\_OUT) obtained through appropriate adjustment of the different amplitudes values of the SHAPE signal so as to compensate optical gain saturation.

FIG. 32 [PRIOR ART] shows the prior art optical pulse shape for link severing, with a spike at the leading edge of the pulse.

FIG. 33 [PRIOR ART] shows the prior art optical pulse shape for link severing, with a spike appearing during the laser pulse.

FIG. 34 [PRIOR ART] shows the prior art optical pulse shape for link severing, with multiple spikes appearing during the laser pulse.

FIG. 35 shows an example of optical shape obtained with a digital pulse shaping module as described herein, corresponding to the optical shape type presented at FIG. 32.

FIG. 36 shows an example of optical shape obtained with a digital pulse shaping module as described herein, corresponding to the optical shape type presented at FIG. 33.

FIG. 37 shows an example of optical shape obtained with a digital pulse shaping module as described herein, corresponding to the optical shape type presented at FIG. 34.

FIG. 38 (PRIOR ART) is an illustration of the transient response of conventional pulsed lasers when switching from the QCW mode to the pulsed mode.

FIGS. 39A to 39C are illustrations of transient response that can be obtained in a situation similar to that of FIG. 38, using embodiments of the present invention.

FIG. 40 (PRIOR ART) is an illustration of the transient response of conventional pulsed lasers when inserting a non-process pulse.

FIG. 41 is an illustration of the benefits of the pulse shape agility according to an embodiment of the present invention for alleviating the throughput penalty arising from the laser transient response when inserting a non-process pulse.

FIG. 42 schematically illustrates a pulsed laser system using a seed laser diode operated in the pulsed regime.

FIG. 43 schematically illustrates a pulsed laser system using a seed laser diode operated in the pulsed regime and a modulator.

FIG. 44 schematically illustrates a pulse laser system providing femtosecond or picosecond shaped pulse trains.

FIG. 45 is an illustration of a shaped picosecond pulse train generated with a digital pulse shaping module as described herein.

FIG. 46 illustrates a lookup table for use in a method mitigating transient effects from the transition between a QCW and pulsed regime, according to one embodiment.

FIG. 47 schematically illustrates the processing of work-piece targets using process and non-process pulses.

FIG. 48 schematically illustrates the writing of laser scribe lines using process and non-process pulses.

FIG. 49 is a lookup table of pulse shapes for use in laser scribe processing.

#### DESCRIPTION OF PREFERRED EMBODIMENTS OF THE INVENTION

The present invention generally provides methods which mitigate transient effects in pulsed laser systems, thereby stabilizing the resulting output.

Laser systems controlled according to embodiments of the invention may advantageously be tailored for any specific laser processing-based method or system that would take advantage of its combined pulse shaping capability and good beam characteristics. Numerous applications already exist or may be developed in several fields, such as selective ablation of neighboring microstructures in the semiconductor industry, memory repair, sophisticated laser trimming of glass, plastic or metal, medical applications such as selective cell or tissue alteration, etc. It will be understood that this list is non-exhaustive and that numerous other applications could benefit from laser systems using embodiments of the invention.

Embodiments of the present invention may benefit from the use of a digital pulse shaping module enabling the versatile and adaptable generation of light pulses. One embodiment of such a module is given below. One skilled in the art will however understand that the scope of the present invention is not limited to the digital pulse shaping module as described herein, and that other systems providing the necessary pulse shaping capabilities could alternatively be employed.

#### Overview of the Pulsed Laser System

Referring to FIG. 1, there is shown a pulsed laser system 50 which includes a digital pulse shaping module 52 controlling the laser oscillator of a pulsed laser source 54.

The pulsed laser system 50 may be embodied by any system apt to generate laser pulses and where the pulse shaping functionality of a laser oscillator is controlled by one or more analog control signals. The expression "laser oscillator" is understood to refer to the portion of a laser source 54 which generates light pulses. The oscillator may include a laser cavity or alternatively be based on fluorescent emissions. The oscillator may be part of a larger system including amplifying, beam shaping or any other optical components further defining the properties of the optical pulses generated by the oscillator. In preferred embodiments, the pulsed laser oscillator has a Master Oscillator, Power Amplifier (MOPA) laser architecture, in which the control signals are used to drive gain-switched semiconductor lasers or high speed optical switches such as electro-optic modulators. Laser sources based on such oscillators, when incorporated into material processing systems, offer numerous advantages in terms of throughput and processing windows over traditional Q-Switched technologies. However, one skilled in the art will understand that embodiments of the pulse shaping module 52 may also be used to provide control signals to Q-switched or other equivalent oscillators without departing from the scope of the invention. Various exemplary embodiments of pulsed laser oscillator structures compatible with the present invention are given further below.

The digital pulse shaping module 52 controls the pulsed laser oscillator according to a digital input waveform. The digital input waveform is a sequence of digital samples which may be defined or selected by a user, and which determine the resulting temporal shape of at least one pulse to be outputted by the pulsed laser source 54. The digital pulse shaping module outputs a control shape signal adapted for controlling the pulse laser oscillator to produce the desired pulses.

The control shape signal outputted by the digital pulse shaping module 52 is also herein referred to as the SHAPE signal. The digital pulse shaping module 52 has a first output port 68, for outputting the SHAPE signal. Referring back to FIG. 1, the first port 68 may typically be connected to a MOD 1 input of the pulsed laser source 54, either for direct modulation in the case of a seed laser diode or for driving an electro-optic modulator, whichever is internal to the architecture of the laser source 54. Also, for compatibility with different laser source architectures, the digital pulse shaping module 52 may optionally have a second output port 69 provide a control gate signal, hereinafter the GATE signal, that may, or may not, be required to be connected to a MOD 2 input of the laser source. The GATE signal is preferably synchronized with the SHAPE signal and it can be used in some laser embodiments for gating or further shaping the optical pulse in-between optical amplifier stages. In a preferred embodiment, the amplitude of the GATE output is fixed, its length is user-adjustable in order to match the duration  $t_{sh}$  of the SHAPE output, and it always occurs after SHAPE by a delay  $d_{gt}$ , whose value is preferably user-adjustable. However, in alternative embodiments, the GATE signal may also be a shaped signal generated in a manner similar to the SHAPE signal.

Under the effect of SHAPE, and optionally GATE, the laser source outputs a shaped optical signal OPTICAL\_SH\_OUT that is typically amplified by a fiber amplifier inside the source before getting to the target.

The pulsed laser system 50 is preferably adapted to interact with user equipment 72, which enables a user to interact and control the laser system according to desired operation

parameters. A connector interface **70** preferably interfaces communication between the digital pulse shaping module **52** and the user equipment **72**. The user equipment **72** may be embodied by any appropriate device or combination of devices such as, for example, a memory repair system, a photovoltaic cell scribing system, a micro-via drilling system or a laser surgery system.

Each electrical shape output by the digital pulse shaping module **52** on SHAPE may be triggered either by an internal or an external trigger signal. Preferably, the connector interface **70** provides an external trigger signal EXT\_TRIGGER to the digital pulse shaping module **52** in response to a trigger command from the user equipment **72**. The connector interface **70** also optionally outputs a trigger synchronization signal SYNC\_OUT, having a predetermined timing relationship with respect to a light pulse emitted by the laser oscillator responsive to the external trigger signal EXT\_TRIGGER. Preferably, the trigger synchronization signal SYNC\_OUT has the same length  $t_r$ , as EXT\_TRIGGER and occurs after the SHAPE output by a delay  $d_{so}$  whose value may be fixed or user-adjustable. This delay  $d_{so}$  enables SYNC\_OUT to occur quasi-simultaneously with the optical output of the laser source (OPTICAL\_SH\_OUT). The so-defined timing relationships between the EXT\_TRIGGER, SHAPE, GATE, SYNC\_OUT and OPTICAL\_SH\_OUT are all illustrated in FIG. 2.

In one embodiment, the pulsed laser system allows to switch between two input waveforms pre-selected by the user, hereinafter referred to as SHAPE\_A and SHAPE\_B. The connector interface **70** preferably provides a shape switching signal SHAPE\_A/B for switching dynamically the SHAPE output from one of the preselected waveforms to the other, in response to a shape selection command from the user equipment **72**.

In one embodiment, when not triggered for pulse shape generation, the outputs SHAPE and GATE of the digital pulse shaping module **52** may default to a Quasi-Continuous Wave (QCW) signal, that is, a square-wave signal having a higher duty cycle than the typical duty cycle of the SHAPE signal, and an appropriate frequency, for example 100 MHz in the illustrated embodiment. The digital pulse shaping module **52** is preferably designed to switch dynamically between the square-wave QCW mode and the pulse-shaping mode. Such switching action can be subject to pre-programmed conditions that will be described in sections below. It is to be noted that in the embodiment of FIG. 1, the connector interface **70** provides a QCW trigger signal EXT\_QCW in response to a QCW command from the user equipment **72** to force QCW at the output of the digital pulse shaping module **52**. A QCW flag QCW\_ON is preferably returned to the user equipment **72** through the connector interface **70** to acknowledge activation of the QCW mode.

Preferably, an oscillator external to the digital pulse shaping module **52** provides a start-up clock signal of 100 MHz at power-on. Other time-critical clock signals for the QCW and pulse shaping modes are generated inside the digital pulse shaping module **52**, as will be seen further below.

The digital pulse shaping module **52** includes a microcontroller **62** (see FIG. 3A) that can communicate with a host computer **76** through a communication port **78**. The communication port **78** may for example be embodied by a RS-232, USB or Ethernet port or any equivalent thereto. In the illustrated embodiment, the host computer **76** is provided to command the digital pulse shaping module **52** to operate either in a SERVICE mode or in a USER mode, as will be explained in detail further below. The host computer **76** may be embodied by any appropriate device such as a general purpose com-

puter, driven by appropriate software. The software may also execute housekeeping functions in collaboration with the microcontroller in the digital pulse shaping module **52**, such as controlling and monitoring laser pump drivers **80** associated with the pulsed laser source **54**, and, if appropriate, controlling and monitoring the extinction point of electro-optic modulators that might be present in the source.

In some embodiments, all desirable laser functionalities such as digital control and monitoring of laser diodes, control of optical devices such as electro-optical modulators, management of laser modes of operations (QCW/pulsed), management of communications with the host computer, etc. may advantageously be implemented in a very compact architecture, for example on a single PCB board. As this approach minimizes the number of individual independent modules, many system functionalities can be easily implemented for a given application or laser system embodiment through modification of the Hardware Description Language HDL code alone without the need of addressing expensive hardware modifications.

Examples of the various components of pulsed laser systems as described in the above overview will be explained in more detail in the following sections. It is again pointed out that the various components and arrangements described below are given for illustrative purposes as one manner of providing the pulse shaping capabilities used by the methods of the present invention, but that the scope of the invention is not considered limited to the use of such a system.

#### 1. Digital Pulse Shaping Module

FIG. 3A generally illustrates a digital pulse shaping module **52** according to one embodiment.

The digital pulse shaping module **52** generally includes a clock generator **56** generating a plurality of phase-related clock signals at a same clock frequency, and a shape generator **58** outputting a digital shape signal DAC\_D corresponding to the digital input waveform. The phase-related clock signals from the clock generator **56** are used as timing signals in the shape generator **58** in a double data rate configuration, hereinafter referred to as "DDR", as will be explained in detail further below. In one embodiment, the digital shape signal DAC\_D from the shape generator **58** is either pulse shape data or quasi-continuous data. The pulse shaping module **52** further includes a Digital-to-Analog Converter **60**, hereinafter DAC, receiving the digital shape signal DAC\_D from the shape generator **58** and converting it into an analog shape signal DAC\_OUT. In the illustrated embodiment, the digital pulse shaping module **52** further includes a shape buffer driver **64** receiving the analog shape signal DAC\_OUT from the shape generator **58** through the DAC **60** and generating the control shape signal SHAPE adapted for controlling the pulse laser oscillator. The microcontroller **62**, which, as mentioned above, is used for controlling the communications with the internal sub-systems of the laser source (pump drivers, etc.) and with the host computer, is finally provided.

The plurality of phase-related clock signals generated by the clock generator **56** include a Clock\_0 signal, and Clock\_90, Clock\_180 and Clock\_270 signals respectively lagging a quarter of a period, half a period and three-quarters of a period behind the Clock\_0 signal. In one embodiment, the phase-related clock signals are driven at 200 MHz. The clock generator **56** further preferably outputs a slow clock signal CLKD4\_0 at 50 MHz, used mainly for clocking the slower elements in the design.

A frequency measurement module **96** is provided for measuring the frequency of the external trigger signal EXT\_TRIGGER from the connector interface.

The DAC **60**, may for example be embodied by the AD9736 model (trademark) from Analog Devices. The interface to the DAC **60** is preferably differential LVDS, 10-bit source-synchronous with the 200 MHz clock signal, DAC\_CLK. The data is input in double data rate into the DAC **60** on each rising edge and each falling edge of DAC\_CLK.

The analog output DAC\_OUT of the DAC **60** is either 100 MHz QCW or an analog pulse shape. This signal is input to the shape buffer driver **64** which is selected amongst devices appropriate for the external laser source. As such, the shape buffer driver **64** preferably has a very high slew rate and it is capable of driving a 50-Ohm load. This driver can use the Texas Instruments THS3102 amplifier (trademark), or a parallel combination of a few such amplifiers. Possibly also, the output of the shape buffer driver **64** may be transformer-coupled to the laser modulator. The resulting output signal is the SHAPE signal that is input to MOD 1 of the laser source.

A low-jitter frequency synthesizer **61**, such as the IDT IC8442 (trademark) with a 10 MHz crystal, outputs a 400 MHz clock signal to the DAC **60**. The DAC **60** divides this frequency down to 200 MHz to output the FPGA\_CLK signal used by the clock generator **56**.

In one embodiment, the shape generator **58** also outputs the PREDR\_GATE signal, either in QCW or pulse mode. This output has fixed amplitude and is also buffered similarly to the DAC\_OUT with a gate buffer driver **116**, for example a THS3102 type amplifier to drive the laser source. The resulting buffered output is the GATE signal that is input to MOD 2 of the laser source.

Peripherals such as a timer **82**, read/write registers **84**, dual-port shape memory buffers **86** and the like may additionally be provided as would be readily understood by one skilled in the art, all of which are preferably mapped on the bus of the microcontroller **62**. Detailed descriptions of these components according to examples of the implementation of the present pulsed laser system are given further below.

Preferably, the microcontroller **62**, clock generator **56**, shape generator **58** and related peripherals are embedded on a high speed digital logic circuit **53**. In the different embodiments described herein, high speed digital logic circuits available in technologies such as ASIC or FPGA or off-the-shelves digital ICs and high speed Digital-to-Analog Converters (DAC) may be used to implement a pulse shaping capability and other desirable industrial laser functionalities resulting in a very efficient, low cost and agile laser system platform that can be easily tailored to several laser processing applications and laser architectures. This integrated approach promotes low jitter and generation of time delays with high resolution for control of critical timings for the fast switching and synchronization of devices such as electro-optical modulators or semiconductor laser diodes.

In the preferred embodiment, the high speed digital logic circuit **53** is a FPGA, such as for example a Xilinx Virtex-2 Pro (trademark) which advantageously includes an embedded microcontroller. The advantages in implementing the FPGA-based architecture illustrated in FIG. 3A are felt in terms of reliability, repeatability and versatility. Especially, it allows for creating a fully-synchronous platform where all the timings, both inside the FPGA **53** and between the FPGA and the DAC **60**, are related to a single common timing reference, in this case clock input signal FPGA\_CLK. The timings, therefore, are homogeneous throughout the digital pulse shaping module.

Moreover, anyone familiar with FPGA technology and digital synchronous design is aware that the implementation described below is not limited to 200 MHz and that it allows for faster speed and, consequently, further temporal resolu-

tion in the optical shapes. To this end, the only requirement is to configure the frequency synthesizer **61** to output signals of an appropriate frequency such as 500 MHz or 600 MHz to the DAC **60**. The frequency of the input clock signal FPGA\_CLK shared by the DAC **60** and FPGA **53** therefore becomes 250 MHz or 300 MHz, which makes for a faster instrument. Of course, if desired the digital pulse shaping module may also be implemented in a slower regime.

Finally, it must be mentioned that the feasibility of the design described herein is subject to several good engineering practices such as appropriate usage of the software tools of the FPGA vendor, and also basic rules in the design of printed circuit boards such as proper part layout, impedance matching, skew analysis among bus signals, etc., as one skilled in the art will readily understand.

### 1.1 Shape Generator

In accordance with the embodiment of FIG. 3A, the shape generator **58** outputs in double data rate the following signals: the digital shape signal DAC\_D data and DAC clock signal DAC\_CLK which are both used as inputs to the high-speed DAC **60** to generate either a pulse shaping or QCW signal as the control shape signal SHAPE; the gate signal PREDR\_GATE, for generating either QCW or fixed-amplitude pulse signal with adjustable width and delay as the control gate signal GATE; the trigger synchronization signal SYNC\_OUT, with adjustable delay for synchronization with the output of optical pulse shapes from the laser source; and the QCW flag QCW\_ON to flag or acknowledge that QCW is the current output mode on the SHAPE and GATE output ports.

Referring to FIG. 3B, the shape generator **58** preferably includes the following sub-modules, embodiments of which will be described below with reference to the appended drawings:

- a shape sub-module **57** (FIG. 14)
- a gate sub-module **114** (FIG. 19)
- a synchronization sub-module **130** (FIG. 24)
- an internal/external trigger sub-module **85** (FIG. 8)
- a QCW control sub-module **106** (FIG. 10)
- a shape selection sub-module **142** (FIG. 11)
- a delay control sub-module **128** (FIG. 12).

#### 1.1.1 Shape Sub-Module

Referring to FIG. 14, there is shown a block diagram of the shape sub-module **57** according to a preferred embodiment. In the illustrated embodiment, as will be explained in detail below, the DDR scheme is preferably implemented through the use of parallelly operating first and second shape state machines **100** and **102**, respectively clocked by the Clock\_0 and Clock\_180 signals and each outputting a corresponding output shape D\_OUT. A shape DDR switch **104** alternately outputs the output shape D\_OUT from the first and second shape state machines **100** and **102** as the digital shape signal DAC\_D. The first and second shape state machines **100** and **102** therefore perform actions in parallel, but delayed by one half-clock period, or 2.5 ns in the 200 MHz frequency scheme. The signals they output at 200 MHz are forwarded to the shape DDR switch **104**, which may for example be embodied by a DDR flip-flop in the I/O block (**10B**) of the FPGA, in order to create an effective data flow of 400 MSample/s out of the system.

Each state machine **100** and **102** of the shape sub-module **57** may be operated in a shape mode, wherein the output shape corresponds to a selected predetermined waveform, or QCW mode, wherein the output shape is a quasi-continuous wave shape, depending on the command signal being asserted.

Preferably, by default, the shape state machines **100** and **102** are operated in QCW mode and signals QCW\_0, QCW\_180 force the output in 100 MHz QCW mode. Input signals QCW\_A\_0, QCW\_A\_180 determine the amplitude of the corresponding QCW signal and the outputted QCW\_ON signal flags or acknowledges the QCW output status to the connector interface (FIG. 1).

For each state machine **100** and **102**, when the corresponding command QCW\_0, QCW\_180 is deasserted, the shape sub-module **57** operates in shape mode, and the output from each shape state machine is pulse shape data at every occurrence of the trigger signal pair TRIG\_0, TRIG\_180.

Preferably, each shape state machine **100** and **102** includes selecting means for selecting the digital input waveform from any one of a plurality of predetermined waveforms stored in a corresponding shape memory buffer **86** (see FIG. 3A). For example, 32 pre-programmed predetermined waveforms may be available to choose from in the shape memory buffer, although, of course, any other appropriate number may alternatively be considered. For embodiments involving shape switching between shape A and shape B, each shape state machine **100** and **102** further includes shape switching means for dynamically switching the corresponding output shape between two of the predetermined waveforms stored in the shape memory buffer. In the illustrated embodiment of FIG. 14, SHAPE\_A\_ADDR\_0, SHAPE\_B\_ADDR\_0, SHAPE\_A\_ADDR\_180, SHAPE\_B\_ADDR\_180 are the addresses of the two preferred shapes among the 32 shapes available and SHAPE\_A/B\_0, SHAPE\_A/B\_180 actuate dynamic switching from shape A to shape B. The address of the selected shape, either shape A or shape B, is output to Shape\_Addr\_0, Shape\_Addr\_180 in order to read the shape data samples out of the shape memory buffers. The sequence of the shape sample addresses is output on Sample\_Addr\_0, Sample\_Addr\_180 while the sequence of the shape data samples is read from Shape\_Data\_0, Shape\_Data\_180.

Still referring to FIG. 14, the shape sub-module **57** further preferably includes a DDR clock switch **112** clocked by the Clock\_90 and Clock\_270 signals providing a DAC clock signal DAC\_CLK as a clock to the double data rate port of the DAC. Since DAC\_D and DAC\_CLK are output through DDR flip-flops located in the IOB's of the FPGA, DAC\_CLK lags the DAC\_D data by a precise and repeatable delay of 1.25 ns in order to ensure reliable setup and hold times between the data and the clock seen by the DAC.

FIG. 15 illustrates the flow chart of the logic implemented in the shape state machines. As mentioned earlier, the output defaults to QCW unless the QCW input is deasserted, in which case, a 32-sample shape is output to the DAC on each LOW-to-HIGH transition of the shape trigger.

FIG. 16 shows a timing diagram illustrating how the two shape state machines operate in parallel to output 100 MHz QCW. The shape state machines respectively output the D\_OUT\_0 and D\_OUT\_180 sequences. In the preferred embodiment, the values in the sequences alternate between 0 and the 10-bit user-set QCW amplitude.

The DAC\_D output is generated by the DDR switch **104**; on the rising edge of Clock\_0, the DDR switch outputs the D\_OUT\_0 data, and on the rising edge of Clock\_180 immediately after, the DDR switch outputs the D\_OUT\_180 data.

The 100 MHz QCW square-wave output seen at DAC\_OUT results from the DAC\_D present at the DAC input on each rising and falling edge of DAC\_CLK. Note that the diagram in FIG. 16 does not necessarily take into account possible pipelining latencies intrinsic to the DAC, but nevertheless, the diagram is valid for understanding the functional principle discussed herein.

The advantage of the double data rate approach is more apparent in the generation of pulse shapes. FIG. 17 shows how the two shape state machines work in parallel to generate 16 consecutive sample addresses. Assuming that the 32 samples of a shape, S(0), S(1), S(2), . . . , S(31), have been stored in the memory buffer according to an even-indexed/odd-indexed approach (see further below), when the triggers TRIG\_0, TRIG\_180 occur, identical addresses are generated on both Sample\_Addr\_0 and Sample\_Addr\_180, so that the double data rate sequence generated at DAC\_D is a well-ordered sequence that reproduces the shape sequence. This sequence is input in the DAC on rising and falling edges of DAC\_CLK.

In FIG. 17, a monotonically increasing pulse shape is represented at the output of the DAC. Since the double data-rate produces samples at each 2.5 ns, the effective output sampling rate is 400 MSPS.

Note that a shape may be defined to be 0 for all of its samples but the first one. Hence, a shape can be as short as 2.5 ns, or any length that is a multiple of 2.5 ns, up to 80 ns.

A feature of this embodiment is that it provides the user with a very agile laser pulse shaping instrument capable of fast switching between a QCW mode and a pulse shaping mode, as well as fast switching between the two preferred shapes Shape A and Shape B when in pulse shaping mode.

FIG. 18 illustrates a typical sequence of output mode switching and shape switching. In time interval (1), the DAC outputs 100 MHz QCW since the QCW command input is asserted. The output goes to 0 when the QCW command is deasserted. At the beginning of time interval (2), the QCW command is deasserted and pulse shaping mode is enabled. The output remains to zero until a rising edge occurs on the TRIG input. This triggers the output of Shape A since the command Shape NB is asserted for Shape A. The output of a shape is performed as a burst. No event at the QCW, the TRIG or the SHAPE NB inputs can interrupt or alter the output sequence of the 32 shape samples. Once the 32 shape samples have been output, the output goes back to 0. At the beginning of time interval (3), pulse shaping is still enabled. Note that Shape A/B has changed state during the preceding time interval. Shape B is output when the next rising edge of TRIG occurs. Then the output goes back to 0. Since QCW has been asserted during output of shape B, in time interval (4) the output immediately reverts back to default QCW mode.

As can be seen from this example, in the preferred embodiment switching from QCW to pulse shaping occurs as soon as QCW is deasserted, switching from pulse shaping to QCW occurs as soon as QCW is asserted at the end of a pulse shape. Furthermore, switching between shapes is always accomplished as requested at the rising edge of TRIG following the end of the preceding pulse shape.

#### 1.1.2 Gate Sub-Module

As mentioned above, the digital pulse shaping module preferably outputs a control gate signal GATE having a predetermined timing relationship with respect to the digital shape signal SHAPE. Referring to FIG. 19, the shape generator preferably includes a gate sub-module **114** generating an analog gate signal PREDR\_GATE. A gate buffer driver **116** (see FIG. 3A) receives the analog gate signal PREDR\_GATE and generates the corresponding digital control gate signal GATE.

In the illustrated embodiment, the gate sub-module **114** includes parallelly operating first and second gate state machines **118** and **120**, respectively clocked by the Clock\_0 and Clock\_180 signals and each outputting a corresponding gate output G\_OUT\_0 and G\_OUT\_180. First and second variable delay lines **122** and **124** are respectively associated

with the first and second gate state machines **118** and **120**, and impose a delay on the corresponding gate output  $G\_OUT\_0$  and  $G\_OUT\_180$ . In this manner, a delayed gate output  $DG\_OUT\_0$  and  $DG\_OUT\_180$  is obtained from each delay line **122** and **124**. A gate DDR switch **126** alternately outputs the delayed gate output  $DG\_OUT\_0$  and  $DG\_OUT\_180$  from the first and second delay lines **122** and **124** as the gate signal  $PREDR\_GATE$ .

In the preferred embodiment, the gate signal  $PREDR\_GATE$  outputted by the gate sub-module **114** is either QCW, or a fixed amplitude pulse signal with adjustable delay and width. Each pair of state machine and delay line **118-122** and **120-124** operate together in the 200 MHz domain. In one example of implementation, the variable delay lines **122** and **124** were configured for a minimum fixed delay value of 170 ns, plus and adjustable delay ranging from 5 to 160 ns in steps of 5 ns.

As shown in FIG. **19**, the first and second gate state machines **118** and **120** are preferably controlled by asserting the  $QCW\_0$ ,  $QCW\_180$  input pair in order to force the outputs  $G\_OUT\_0$ ,  $G\_OUT\_180$  in 100 MHz fixed-amplitude QCW mode. When the  $QCW\_0$ ,  $QCW\_180$  input pair is deasserted, the outputs of the gate state machines **118** and **120** switch to a fixed-amplitude pulse mode synchronized with the occurrence of  $TRIG\_0$ ,  $TRIG\_180$ .

1.1.3 Delay Control Sub-Module

Referring to FIG. **12**, there is shown a gate control sub-module **128** which is part of the shape generator according to one embodiment. FIG. **12** illustrates how  $dgt\_Delay$  and  $dso\_Delay$ , corresponding to software-selectable  $d_{gr}$  and  $d_{so}$  delay values in the timing diagram of FIG. **2**, are propagated down to the DDR hardware of the digital pulse shaping module. The resulting DDR signal pairs are, respectively,  $DGT\_0$ ,  $DGT\_180$  for the  $d_{gr}$  delay and  $DSO\_0$ ,  $DSO\_180$  for the  $d_{so}$  delay.

Referring back to FIG. **19**, the first and second gate state-machines **118** and **120** are preferably programmed so that the delayed outputs  $DG\_OUT\_0$  and  $DG\_OUT\_180$ , once combined into the gate DDR switch **126**, will produce a desired granularity of 2.5 ns in both the delay value  $d_{gr}$  and the pulse width  $t_{sh}$  of  $PREDR\_GATE$ . With the adjustable delay  $d_{gr}$  and pulse width  $t_{sh}$  shown in FIG. **2**, the  $DGT\_0$ ,  $DGT\_180$  is a G-bit integer n such that

$$170\text{ ns}+(n \times 2.5)\text{ ns}=d_{gr}$$

and  $Gate\_Data\_0$ ,  $Gate\_Data\_180$  is an integer w such that

$$(w \times 2.5)\text{ ns}=t_{sh}$$

The desired granularity can be achieved even if the temporal resolution of the signals output by the state machines and the delay lines is 5 ns. The reason is that there is an intrinsic delay of one-half clock cycle, or 2.5 ns, between the pulse outputs of the state-machines and that the DDR flip-flops in the IOB of the FPGA have the ability to update their output at each 2.5 ns, or one-half clock cycle interval.

The first rule is to take only the five most-significant bits of  $DGT\_0$ ,  $DGT\_180$  to set the variable delay value of the delay lines. This is equivalent to delaying the outputs of the state-machines by  $(n^* \times 5)$  ns, where  $n^*$  is half the greatest even integer less than or equal to n. Following this rule, the pulse outputs of the gate state machines are always delayed by an even multiple of 2.5 ns, since the delay lines are clocked at 200 MHz and consequently, they have a single-tap delay of 5 ns.

TABLE 1

Rules for setting the W0, W180 output pulse widths			
W	n	W0	W180
odd	even	$(w + 1)/2$	$(w - 1)/2$
	odd	$(w - 1)/2$	$(w + 1)/2$
even	even		w/2
	odd		

Second, for a given combination of width w and delay n, the pulse width  $W0$  and  $W180$  output by, respectively, the first gate state machine **118** and the second gate state machine **120**, are set according to the rules defined in Table 1 above.

Finally, for the case n odd, we further impose that there is one wait state prior to outputting the pulse of width  $W0$  at output  $G\_OUT\_0$  of the first gate state machine **118**.

FIGS. **20** to **23** are timing diagrams showing how applying these rules makes it possible to generate the  $PREDR\_GATE$  output with pulse width and delay values with the desired granularity of 2.5 ns.

w odd, n even. Here we choose  $w=1$  to output a pulse width of 2.5 ns, and  $n=2$  to produce a delay  $d_{gr}$  which is an even multiple of 2.5 ns. Following the rules defined above, we have  $n^*=1$  for a line delay which is  $(1 \times 5)$  ns, or  $(2 \times 2.5)$  ns; and  $W0=1$ ,  $W180=0$ , i.e., no pulse output by the second gate state machine **120**. This is illustrated in FIG. **20**, where we deliberately omit to represent the fixed offset of 170 ns of the delay lines. Note that even if a pulse of 5 ns is output by the first gate state machine **118**, the  $PREDR\_GATE$  output lasts only 2.5 ns because the  $D\_180$  input of the gate DDR switch **126** is always is 0. Further, it is fairly simple to demonstrate that for every other w odd, the pulse width  $t_{sh}$  is an odd multiple of 2.5 ns. Similarly, for every other n even, the delay  $d_{gr}$  is an even multiple of 2.5 ns.

w odd, n odd. Here, we set  $w=3$  and  $n=3$ . Applying our rules, we have  $n^*=1$  for a line delay which is  $(1 \times 5)$  ns, or  $(2 \times 2.5)$  ns; and  $W0=1$ ,  $W180=2$ . Also, since n is odd, there is one wait state at the output  $G\_OUT\_0$ . The result is illustrated and FIG. **21** and shows that the  $PREDR\_GATE$  pulse width is  $(3 \times 2.5)$  ns and that, with respect to the previous case illustrated in FIG. **20**, an additional delay tap of 2.5 ns has now been added, which accounts for an odd-valued delay. For every other w odd, the pulse width  $t_{sh}$  is an odd multiple of 2.5 ns. Similarly, for every other n odd, the delay  $d_{gr}$  is an odd multiple of 2.5 ns. Particularly, for  $w=1$ , we get  $W0=0$ ,  $W180=1$  and it is readily seen that the width  $t_{sh}$  of  $PREDR\_GATE$  reduces to 2.5 ns.

w even, n even. Setting  $w=2$ ,  $n=2$ . We get  $n^*=1$ ,  $W0=1$ ,  $W180=1$ . The result is illustrated and FIG. **22** and shows that the delay is an even multiple of 2.5 ns and that the  $PREDR\_GATE$  pulse width is  $(2 \times 2.5)$  ns. For every other w even, the pulse width  $t_{sh}$  is an even multiple of 2.5 ns. Similarly, for every other n even, the delay  $d_{gr}$  is an even multiple of 2.5 ns.

On the other hand, it is worth noting that for any n even delay, the first and second gate state machines **118** and **120** may be programmed to output the combination  $W0=1$ ,  $W180=1$ , and then  $W0=0$ ,  $W180=0$ , which will produce 100 MHz fixed-amplitude QCW on  $PREDR\_GATE$  when the input pair  $QCW\_0$ ,  $QCW\_180$  is asserted.

w even, n odd. Here,  $w=2$  and  $n=3$ . Applying our rule, we have  $n^*=1$  for a line delay which is  $(1 \times 5)$  ns, or  $(2 \times 2.5)$  ns; and  $W0=1$ ,  $W180=1$ . Also, since n is odd, there is one wait state added at the output  $G\_OUT\_0$ . The result is illustrated in FIG. **23** and shows that the  $PREDR\_GATE$  pulse width is  $(2 \times 2.5)$  ns and that, with respect to the case illustrated previously in FIG. **22**, an additional 2.5 ns delay tap has now been



added, which accounts for an odd delay value. For every other  $w$  even, the pulse width  $t_{sh}$  is an even multiple of 2.5 ns. Similarly, for every other  $n$  odd, the delay  $d_{gr}$  is an odd multiple of 2.5 ns.

On the other hand, it is worth noting that for any  $n$  odd delay, the first and second gate state machines **118** and **120** may be programmed to output the combination  $W0=1$ ,  $W180=1$ , then  $W0=0$ ,  $W180=0$ , which will produce 100 MHz fixed-amplitude QCW on PREDDR\_GATE when the input pair QCW\_0, QCW\_180 is asserted.

#### 1.1.4 Synchronization Sub-Module

Optionally, with reference to FIG. 24, the shape generator may include a synchronization sub-module **130** for generating the trigger synchronization signal SYNC\_OUT. Preferably, the output of the trigger synchronization signal output SYNC\_OUT is either a logical 0, or a fixed amplitude pulse signal with an adjustable delay. In the preferred embodiment, the synchronization generator **130** includes a first and second parallel operated synchronization state machines **132** and **134**, and corresponding variable synchronization delay line **136** and **138**, each pair of synchronization state machine and corresponding synchronization delay line being operated together in the Clock\_0 and Clock\_180 domains. In the preferred embodiment, the variable delay lines **136** and **138** are preferably configured for a minimum fixed delay value of 245 ns, plus an adjustable delay ranging from 5 to 160 ns in steps of 5 ns.

The first and second synchronization state machines **132** and **134** are preferably controlled by asserting the QCW\_0, QCW\_180 input pair in order to force the synchronization outputs SO\_OUT\_0, SO\_OUT\_180 into the logical 0 state.

When the QCW\_0, QCW\_180 input pair is deasserted, the synchronization outputs SO\_OUT\_0, SO\_OUT\_180 of the first and second synchronization state machines **132** and **134** switch to fixed-amplitude pulse mode, synchronized to the occurrence of TRIG\_0, TRIG\_180.

The first and second synchronization variable delay lines **136** and **138** impose a delay DSO\_0, DSO\_180 on the corresponding synchronization output SO\_OUT\_0, SO\_OUT\_180, thereby generating a delayed synchronization output DSO\_OUT\_0, DSO\_OUT\_180.

In accordance with the adjustable delay  $d_{so}$  and pulse width  $t_r$ , shown in FIG. 2, the DSO\_0, DSO\_180 is a 6-bit integer  $n$  such that

$$245 \text{ ns} + (n \times 2.5) \text{ ns} = d_{so}$$

and the duration  $t_r$  of the pulse signal pair SO\_OUT\_0, SO\_OUT\_180 follows the duration of TRIG\_0, TRIG\_180 input pair.

A synchronization DDR switch **140** alternately outputs the delayed synchronization output DSO\_OUT\_0, DSO\_OUT\_180 from the first and second synchronization state machines **132** and **134** as the trigger synchronization signal SYNC\_OUT. The first and second synchronization state machines **132** and **134** are programmed so that the delayed synchronization outputs DSO\_OUT\_0 and DSO\_OUT\_180, once combined into the synchronization DDR switch **140**, will produce the desired granularity of 2.5 ns in the delay value  $d_{so}$  of SYNC\_OUT.

Referring back to FIG. 12, there is shown how the delay control module **128** provides the DSO\_0, DSO\_180 signal pair. In this embodiment, the first rule is to take only the five most-significant bits of DSO\_0, DSO\_180 to set the variable delay value of the delay lines. This is equivalent to delay the outputs of the state-machines by  $(n \times 5)$  ns, where  $n^*$  is half

the greatest even integer less than or equal to  $n$ . Following this rule, the pulse output of the state-machine is always delayed by an even multiple of 2.5 ns.

The second rule is that for the case  $n$  odd, we further impose that there is one wait state prior to outputting the pulse at output SO\_OUT\_0 of the first synchronization state machine **132**. As shown in FIG. 24, note that this condition will not apply to the instantiation of the second synchronization state machine **134**, since its DELAY input is permanently tied to the logical 0 state.

In FIGS. 25 and 26, it is shown that applying these rules makes it possible to generate the SYNC\_OUT output with pulse delay values at a desired granularity of 2.5 ns.

$n$  even. Here, we set  $n=2$ . Applying our rules, we get  $n^*=1$  for a line delay which is  $(1 \times 5)$  ns, or  $(2 \times 2.5)$  ns. As shown in FIG. 25, this produces a delay  $d_{so}$  which is an even multiple of 2.5 ns and it is readily seen that for every other  $n$  even, the delay  $d_{so}$  is an even multiple of 2.5 ns.

$n$  odd. Here, we set  $n=3$ . Applying our rule, we get  $n^*=1$  for a line delay which is  $(1 \times 5)$  ns, or  $(2 \times 2.5)$  ns. Also, since  $n$  is odd, there is one wait state added at the output SO\_OUT\_0. The result is illustrated and FIG. 26 and shows that, with respect to the case illustrated previously in FIG. 25, an additional 2.5 ns delay tap has now been added which accounts for an odd delay value. It is readily seen that for every other  $n$  odd, the delay  $d_{so}$  is an odd multiple of 2.5 ns.

#### 1.1.5 QCW Control Module

The shape generator preferably includes a QCW control sub-module **106** which is illustrated in FIG. 10, and which provides the signals QCW\_0, QCW\_180, QCW\_A\_0, and QCW\_A\_180. The QCW control sub-module **106** includes a QCW\_Amplitude register **108** and a SOFT\_QCW register **110**. The output of the SOFT\_QCW register **110** defaults to asserting QCW, and is logically OR-ed with the EXT\_QCW hardware command and the FM\_QCW command generated by the measurement of the frequency of the external trigger. All of EXT\_QCW, FM\_QCW and SOFT\_QCW must be deasserted for pulse shaping to be enabled.

As illustrated in FIG. 10, the hardware/software assertion control and amplitude settings of the QCW mode are propagated down to the DDR QCW\_0, QCW\_180 and the QCW\_A\_0, QCW\_A\_180 signal pairs.

#### 1.1.6 Shape Selection Sub-Module

FIG. 11 illustrates a shape-selection sub-module **142** through which the Shape A/B shape select command and the Shape A, Shape B software-selectable addresses are propagated down to the DDR hardware of the digital pulse shaping module. The resulting DDR signal pairs are, respectively, SHAPE\_A/B\_0, SHAPE\_A\_B\_180 for shape select, SHAPE\_A\_ADDR\_0, SHAPE\_A\_ADDR\_180 for shape A address and SHAPE\_B\_ADDR\_0, SHAPE\_B\_ADDR\_180 for shape B address.

Referring back to FIG. 3A, other components of a digital pulse shaping module **52** according to a preferred embodiment will now be described in more detail.

As suggested above, one skilled in the art will understand that other embodiments may be considered to provide the user with much higher output sampling rates, hence higher temporal resolution than what has been presented so far. One principle of interest put forward in the embodiments described above is that time-critical aspects of high-speed pulse shaping involve digital state-machines that output data to sequential elements located in the I/O blocks of the FPGA. The more recent FPGA components such as the Virtex-5 (trademark) from Xilinx now include sequential primitives like the IOSERDES in their I/O blocks. These allow DDR output at an effective rate higher than 1 GSPS. Although it

might be necessary to store pulse shape data in memory in a different way than the one described above, the IOSERDES primitive might certainly be used in an embodiment that would achieve sub-nanosecond temporal resolution for QCW and pulse shaping.

### 1.2 Clock Generator

The clock generator **56** is preferably driven at power-on startup by the startup clock signal EXT100MHz from the external oscillator **74** (see FIG. 1). A clock input signal, FPGA\_CLK, at 200 MHz in the preferred embodiment, is used as a timing reference to output the phase-related clock signals Clock\_0, Clock\_90, Clock\_180 and Clock\_270.

Preferably, the clock generator **56** also outputs a slow clock signal ClockD4\_0, at 50 MHz in the preferred embodiment. The slow clock signal CLKD4\_0 is used mainly for clocking the slower elements in the design, such as the microcontroller **62**, the registers **84** and memory buffers **86**.

In one embodiment, the clock generator **56** is based on a Digital Clock Manager (DCM) primitive available in state-of-the-art FPGA components. The DCM is configured to output the five clock signals Clock\_0, Clock\_90, Clock\_180, Clock\_270 and ClockD4\_0.

Referring to FIG. 4, Clock\_0, Clock\_90, Clock\_180 and Clock\_270 preferably have the same frequency as FPGA\_CLK, namely 200 MHz in the preferred embodiment. ClockD4\_0 is at 50 MHz. Apart from a negligible latency, Clock\_0 is a copy of the 200 MHz input FPGA\_CLK. There is a strict phase relation among the outputs of the DCM: ClockD4\_0 is in phase with Clock\_0, Clock\_90 is lagging Clock\_0 by a quarter of a period, or 1.25 ns, Clock\_180 is lagging Clock\_0 by half a period, or 2.5 ns and Clock\_270 is lagging Clock\_0 by three quarters of a period, or 3.75 ns.

The five outputs of the clock generator **56** are used to clock all the synchronous elements throughout the system, such as flip-flops, processor, or memories. Moreover, one skilled in the art and familiar with FPGAs knows that the software tools of the FPGA vendor take into account the phase relation between the different clock domains as defined in FIG. 4. As a consequence, the place-and-route of the design is performed iteratively so as to resolve any setup and hold time issue between synchronous elements, even if located in different clock domains, until reliable timing closure is achieved.

The 200 MHz Clock\_0 and Clock\_180 are the main clocks used to generate either a QCW or pulse shape as the digital shape signal DAC\_D. The phase-relation between these two clocks is purposely set to one half-period in order to create a double data rate output for the samples sent to the DAC.

The 200 MHz Clock\_90 and Clock\_270 are used to create the DAC clock signal DAC\_CLK for proper data setup and hold times in the source synchronous interface between the FPGA and the DAC.

As mentioned above, in the preferred embodiment, an external 100 MHz signal, EXT100MHz, is used for startup at power-on. Its only use is for clocking an elementary state-machine who releases system reset once (1) the DCM has locked on the FPGA\_CLK input and (2) the DCM outputs are ready to clock the digital pulse shaping module **52**.

### 1.3 Frequency Measurement Module

The digital pulse shaping module **52** preferably includes a frequency measurement module **96** (see FIG. 1), which is preferably embodied by a state-machine having an internal counter clocked at 50 MHz. The frequency measurement module **96** asserts a QCW output flag FM\_QCW, to force the QCW output mode when the frequency of the external trigger signal EXT\_TRIGGER is outside of a pre-defined range.

Preferably, the frequency range of EXT\_TRIGGER for which the QCW output flag FM\_QCW is asserted is lower

and upper bounded with hysteresis. For example, the transition diagram in FIG. 5 shows that the FM\_QCW output is asserted when the frequency of the EXT\_TRIGGER input decreases below 88 KHz or increases beyond 1.1 MHz. Reciprocally, the FM\_QCW output is deasserted when the EXT\_TRIGGER frequency increases beyond 90 KHz or decreases below 1.0 MHz.

In the preferred embodiment, it is up to the user to activate or de-activate the frequency monitoring function just-described. When monitoring is de-activated, no assertion of the FM\_QCW signal will occur.

### 1.4 Microcontroller

The microcontroller **62** preferably has an embedded architecture as shown in FIG. 6. In the preferred embodiment, it is implemented in the FPGA around a CPU core **88**, such as for example a Power PC **405** (trademark) from IBM. The platform of the microcontroller **62** is preferably tailored with the number and the type of peripherals needed by connecting FPGA vendor-supplied peripheral cores to the CPU address/data bus. First, a Program Memory core **90** is included for storing the programmed executable code for the CPU **88**. A Timer core **92** is used for generating an internal shape trigger signal INT\_TRIGGER. A Peripheral Interface core **94** is connected to the CPU **88** to perform address decode and generate read/write control signals for data access to/from the command registers and the shape memory buffers. Communications with the laser pump drivers and the host are implemented with UART interfaces **95**.

### 1.5 Command Registers and Control Signals

Referring to FIG. 7, there is shown a preferred embodiment of the command registers **84** for use in a digital shaping module according to FIG. 3. Preferably, the command registers **84** are embodied by read/write registers which are connected to the Peripheral Interface **94** of the microcontroller **62** (see FIG. 6). As such, they are clocked by the 50 MHz ClockD4\_0. The registers **84** are used for software-control of the QCW or pulse shape output modes on SHAPE, GATE and SYNC\_OUT outputs, together with the external hardware command signals EXT\_QCW, EXT\_TRIGGER and SHAPE\_A/B (see FIG. 2).

In the section below, the role of the command registers is outlined, with respect to the main functional aspects such as triggering the pulse shapes, switching between QCW and pulse shaping, and switching between shape A and shape B. It will be understood by one skilled in the art that the command registers **84** as described is given by way of example only. In addition, other command registers might be present in the platform for other useful purposes, but their description is not essential to the understanding of the present description.

Referring to FIG. 7, the registers are described as follows: INT/EXT\_TRIG\_SEL: for selecting the external or the internally generated pulse trigger.

SOFT\_QCW: allows QCW output mode to be set by user software command.

QCW\_Amplitude[9:0]: 10-bit register for setting the amplitude of the 100 MHz QCW square wave signal.

Shape A Address[4:0]: 5-bit register for setting the address of Shape A among the 32 available shapes.

Shape B Address[4:0]: 5-bit register for setting the address of Shape B among the 32 available shapes.

dgt\_Delay[5:0]: 6-bit register for setting the delay value  $d_{gr}$  of GATE after SHAPE.

dso\_Delay[5:0]: 6-bit register for setting the delay value  $d_{gr}$  of SYNC\_OUT after SHAPE.

The command registers are clocked by the 50 MHz ClockD4\_0 for read/write accesses by the CPU through the Peripheral Interface. For proper operation of the QCW or

pulse shaping in double data rate mode, the state of the register must propagate down to the time-critical 200 MHz Clock<sub>0</sub> and Clock<sub>180</sub> clock domains altogether. This is accomplished by using cross-domain synchronization flip-flops. FIG. 8 shows an internal/external trigger sub-module 85 according to one embodiment, where synchronization flip-flops are used in the case of the external and internal pulse shape triggers, together with the INT\_EXT\_TRIG\_SEL register.

First, as shown at the left-hand side of FIG. 8, the EXT\_TRIGGER connected to the pad of the FPGA is synchronized to the 50 MHz ClockD4\_0 right after entering the FPGA. This is done by using a dedicated input flip-flop located into the I/O block (IOB FF) of the FPGA.

The internally-generated trigger INT\_TRIGGER (FIG. 6) and the trig-select output of register INT/EXT\_TRIG\_SEL, by definition, are synchronized to ClockD4\_0. The trig-select output of the register orders a multiplexer to output either INT\_TRIGGER or the synchronized EXT\_TRIGGER. Since there is a well-defined phase-relationship between all clock signals, the TRIG output of the multiplexer is synchronous with respect to all three clocks ClockD4\_0, Clock\_0 and Clock\_180 and it can be readily propagated down to the TRIG\_0, TRIG\_180 signal pair through the synchronization flip-flops.

FIG. 9 illustrates that under the effect of the synchronization flip-flops, when a signal transition occurs in the ClockD4\_0 domain, it is followed by a corresponding transition of the signal pair in the Clock\_0, Clock\_180 domains. This typical dual-propagation principle is useful for the proper generation of the QCW or the pulse shapes at double data rate and it is used extensively throughout the practical implementation of the digital pulse shaping module described herein.

#### 1.6 Shape Memory Buffers

Referring to FIG. 13, there is shown a shape memory buffer 86 according to one embodiment. The shape memory buffer 86 stores a plurality of predetermined waveforms Shape\_Data, which are provided to the shape state machines of the shape sub-module upon selection.

The shape memory buffer 86 according to the illustrated embodiment includes two components, a pulse shape data buffer 143 storing the digital samples of the pulse shapes that are output on the SHAPE port, and a pulse duration data buffer 147 for storing the pulse durations  $t_{sh}$  of the pulses shapes (Refer to FIG. 2). This data is used to output the fixed-amplitude pulse signal on the GATE port. Each of data buffers 143 and 147 includes even and odd memory components 144, 146 and 148, 150.

The four memory components 144, 146, 148 and 150 are preferably dual-port type with their left port used for read/write accesses through the CPU's Peripheral Interface previously outlined with respect to FIG. 6. The left port of these memories, therefore, is clocked at 50 MHz by ClockD4\_0, and it is used primarily for initializing the pulse shapes and the pulse durations.

In one embodiment, the shape memory buffer 86 is configured in the FPGA for a capacity of 32 shapes and 32 corresponding pulse durations. Each of the 32 pulse shapes is user-defined with 32, 10-bit samples, and each of the 32 pulse durations has a 6-bit value  $n$ , such that  $(n \times 2.5)$  ns is equal to the duration  $t_{sh}$  of the corresponding pulse shape.

The even pulse shape buffer component 144 of the pulse shape data buffer 143 contains the even-indexed samples of the shapes, while the odd pulse shape buffer component 146 contains the odd-indexed samples. More specifically, if a shape has 32 consecutive samples indexed as S(0), S(1),

S(2), . . . , S(30), S(31), then the 16 even-indexed samples S(0), S(2), . . . , S(30) are stored in the even pulse shape buffer component 144 and the odd indexed samples S(1), S(3), . . . , S(31) are stored in the odd pulse shape buffer component 146.

This partitioning of the pulse shape data buffer 143 is defined for the practical implementation of the double data rate output to the DAC. To this end, it is to be noted that the right port of the even pulse shape buffer 144 is clocked at 200 MHz by Clock\_0 and that the right port of the odd pulse shape buffer 146 is clocked at 200 Mhz by the shifted Clock\_180.

The right port of each of the pulse shape data buffer components 144 and 146 is read-only. It is connected to the shape generator 58 shown in FIG. 3A and described above. Shape generator supplies the shape addresses Shape\_Addr\_0, Shape\_Addr\_180, the sample addresses Sample\_Addr\_0, Sample\_Addr\_180 and reads the data out of the memory.

The 5-bit Shape\_Addr\_0[4:0] is for selecting one shape among the 32 shapes available. In the even pulse shape buffer 144, the 4-sample Shape\_Addr\_0[3:0] are for addressing each of the 16 even-indexed samples of the addressed shape. Data is read out of memory on the 10-bit bus Shape\_Data\_0.

Similarly, Shape\_Addr\_180[4:0] and Sample\_Addr\_180 [3:0] are for doing the equivalent on the odd pulse shape buffer 146. Data is read out of memory on the 10-bit bus Shape\_Data\_180.

The pulse duration data buffer 147 is similarly implemented in the even and odd pulse duration buffer components 148 and 150. These two buffers components 148 and 150 are provided two keep with the double data rate approach that is put forward in this embodiment. Hence, the even pulse duration buffer component 148 is clocked by Clock\_0, while the odd pulse duration buffer component 150 is clocked by Clock\_180.

Both pulse duration buffer components 148 and 150 preferably contain 32 values. For a given address, the 6-bit value stored at this address is the same in both even and odd components 148 and 150, and is preferably a 6-bit integer  $n$  such that  $(n \times 2.5)$  ns is equal to the duration  $t_{sh}$  of the corresponding pulse shape. As with the pulse shape data buffer 143, the shape generator 58 of FIG. 3A is responsible for generating the addresses for the pulse duration data buffer 147 and for reading its data output. FIG. 13 clearly illustrates that Shape\_Addr\_0 will simultaneously address a given pulse shape data and its duration since the shape address seen by both buffer components 144 and 148 is the same. Similarly Shape\_Addr\_180 simultaneously addresses a given pulse shape data and its duration on the odd pulse shape component 146 and odd pulse duration component 150.

Finally, it is to be noted that the data bus outputs of the even and odd components 148 and 150 have been named, respectively, Gate\_Data\_0 and Gate\_Data\_180, since these information are used in the generation of the fixed-amplitude pulse output on the GATE port of the digital pulse shaping module.

#### 2. Service Mode

As mentioned above, in one embodiment, the digital pulse shaping module operates either in a SERVICE mode or in a USER mode.

Referring back to FIG. 1, according to one embodiment, in SERVICE mode, the software on the host computer 76 allows the user to define up to 32 arbitrary pulse shapes of his choice. Each shape is written as 32, 10-bit consecutive samples in the memory buffer of the digital pulse shaping module 52. Such fine granularity in the adjustable amplitudes of the sample enables very precise optical shapes to be output by the instrument. The software can select either the internal or the external EXT\_TRIGGER input to trig the shapes out of the digital pulse shaping module 52 to the laser source 54.

Each sample in a shape is 2.5 ns long so that a shape output by the digital pulse shaping module 52 has length varying between 2.5 and 80 ns. This allows internal or external triggering of the pulse shapes at a rate faster than 10 MHz.

Optionally, appropriate components such as a power splitter and a photodetector can be used in order to implement in the laser source 54 an output port for an analog electrical signal whose shape matches the optical shape output at OPTICAL\_SH\_OUT. This is the purpose of the FDBCK output appearing on the laser source 54, as illustrated in FIG. 1. By connecting this FDBCK signal to a display such as an oscilloscope, the user gets a visual feedback so that corrections can be sent to the digital pulse shaping module 52 to perform fine tuning in real-time of the optical pulse shaping at OPTICAL\_SH\_OUT.

The software preferably on the host computer 76 also preferably allows the user to select two shapes among the 32 pulse shapes available. These are defined as Shape A and Shape B and they are provided for being output alternately on the SHAPE port under the action of the external SHAPE\_A/B signal input. Once selected by the user, Shape A and Shape B addresses are stored in the registers internal to the digital pulse shaping module 52. This allows dynamic switching from Shape A to Shape B at a rate faster than 10 MHz.

Finally, the amplitude of the QCW output can be set by writing to a specific register, in case the register's default value is not suitable for the application.

It must be noted that other embodiments might implement pulse shaping with more than 10 bits per sample, more than 32 samples per shape, or more than 32 shapes. This simply is a matter of system architecture as it is defined in the HDL coding of the FPGA component.

In order to make the digital pulse shaping module 52 adaptable to different laser source architectures, the software on the host preferably allows the user to define the timing parameters for the GATE output.

Referring to FIG. 2, the value of the delay  $d_{gr}$  of GATE with respect to SHAPE is user-selectable by writing to a single register internal to the digital pulse shaping module 52. The allowed value for  $d_{gr}$  is in the range 170 ns up to 327 ns in steps of 2.5 ns.

On the other hand, the digital pulse shaping module 52 may provide a memory buffer for storing 32 values for the duration  $t_{sh}$  of GATE, as explained above. Each of these 32 values for  $t_{sh}$  must be equal to the duration of each of the 32 pulse shapes. When a given pulse shape of duration  $t_{sh}$  is triggered to be output on SHAPE, the corresponding fixed-amplitude pulse of equal duration  $t_{sh}$  is simultaneously triggered to be output on GATE, after a delay equal to  $d_{gr}$ . Accordingly with the allowed shape durations described above, the allowed value for  $t_{sh}$  is from 2.5 ns up to 80 ns in steps of 2.5 ns.

It must be noted that other embodiments might implement different ranges of values for  $d_{gr}$  and  $t_{sh}$ . Moreover, the GATE output is not restricted to a fixed amplitude signal. In another embodiment, it could as well be designed and implemented as an arbitrarily shaped signal.

The software on the host computer 76 may also allow the user to set the value of the delay  $d_{so}$  after SHAPE for the SYNC\_OUT pulse (refer to FIG. 2). The digital pulse shaping module 52 contains a single register for writing the value of  $d_{so}$ . The allowed value for this parameter is preferably in the range 245 ns up to 403 ns in steps of 2.5 ns.

The SYNC\_OUT pulse has nominally the same duration  $t_{tr}$  as the EXT\_TRIGGER input. Other embodiments might implement different ranges of values for  $d_{so}$  and  $t_{tr}$ .

### 3. User Mode

Referring still to FIG. 1, once shapes, timing parameters and shape addresses have been defined in the memories and registers of the digital pulse shaping module 52, the digital pulse shaping module 52 is ready to be set in USER mode for material processing. In USER mode, the external user equipment 72 can drive the digital pulse shaping module 52 through the connector interface 70. The default output mode on SHAPE and GATE is preferably square-wave 100 MHz QCW. In the preferred embodiment, this mode has priority over pulse shaping.

### 4. Pulsed Laser Source

Numerous pulsed laser source architectures can be controlled by the digital pulse shaping module so as to constitute a complete industrial laser system having all desirable embedded functionalities for a given laser processing application. Exemplary embodiments are described below. It will be obvious to those ordinary skilled in the art that many changes may be made to the details of the pulsed laser source architectures that are presented in this document without departing from the scope of this invention.

#### 4.1 Pulsed Laser Source Based on Electro-Optic Modulators and CW Seed Source

Referring to FIG. 27, in a first embodiment the pulsed laser source 54 includes a seed source 160 operated in the CW regime and two electro-optic modulators 162 and 164.

The seed source 160 is preferably a continuous wave (CW) seed light source generating a continuous light beam. Preferably, the seed light source 160 is a laser diode, but any other light source generating an appropriate continuous beam could be considered, such as for example a filtered ASE source, a superfluorescent source, a CW fiber laser or a fiber coupled CW bulk solid-state laser source. The continuous light beam preferably has a spectral shape which will determine the spectral shape of the light outputted by the entire pulsed light source. Advantageously, the laser diode may be selected or replaced depending on the required spectral profile of the outputted light. Alternatively, a wavelength tunable diode may be used. Additional components may optionally be provided downstream the laser diode to modify its spectral shape. An optical isolator may also be provided downstream the seed laser diode to prevent feedback noise from reaching it.

In one exemplary embodiment, the seed source is an external cavity semiconductor laser diode with a central emission wavelength of 1064.3 nm and a nominal output power of 100 mW. The emission is single longitudinal mode with a fairly narrow line width (less than 10 MHz). The emission wavelength can be easily fine-tuned by the user with a simple user command giving access to the seed laser diode operating temperature. This functionality, preferably embedded in the digital pulse shaping module, has been used for example in a memory repair application where the pulsed laser source output was amplified with a Nd:YVO<sub>4</sub> solid state amplifier (See patent WO 2008/014331). In such a configuration, the overlap of the pulsed laser source emission spectrum with respect to the relatively narrow spectrum (~0.5 nm FWHM) of the solid state medium should be controlled for optimizing the amplification, especially under different pumping conditions of the crystal. With increasing pump power, the crystal gain spectrum tends to shift to higher wavelengths. In the mentioned application, the emission wavelength flexibility provided by the embedded laser platform is used to fine-tune the pulsed laser source emission wavelength with a simple user command sent by a computer, in order to optimize the spectral overlap of the source with respect to the solid state amplifier gain spectrum under varying pumping conditions (see FIG. 28). In typical conditions, the seed laser diode

wavelength shifts by about 50 pm when changing the diode operating temperature by 1° C. The laser platform provides a resolution of 0.1° C. for the laser diode operating temperature set point, thus allowing for a resolution of about 5 pm in the wavelength adjustments. In another embodiment, the seed source is a Fiber Bragg Grating (FBG) stabilized laser diode emitting at 1064.3 nm with a nominal output power of 150 mW. The FBG temperature is controlled with a resolution of 0.1° C. with a user command, also allowing for fine wavelength tuning by the user.

Those embodiments are particularly suitable for applications requiring a great stability of the pulsed laser source spectrum under change of the conditions of operation (e.g. pulse repetition rate, pulse shape, quasi-CW vs pulsed operation, etc.) since the seed laser diode spectrum is not affected by the modulation as it would be for those configurations where the seed diode current is modulated.

Referring back to FIG. 27, the pulsed laser light source 54 further includes a first modulator 162 for temporally modulating the continuous light beam, and thereby generate a plurality of optical pulses defining a pulsed light beam. The created optical pulses have a pulse shape determined by the amplitude profile of the SHAPE signal outputted by the digital pulse shaping module 52 which drives the RF port of the modulator as will be explained further below. The first amplitude modulator 162 is preferably a Lithium Niobate Mach-Zehnder electro-optic modulator of the APE type having a bandwidth of at least 3 GHz at 1064 nm with an On/Off extinction ratio of at least 25 dB. Such modulators are suitable devices for generating optical pulses with controlled features at the nanosecond scale. In other embodiments, other modulation scheme, such as based on an acousto-optic modulator, an electroabsorption modulator, etc. could also be considered. The optical input port of the first modulator 162 is optically coupled to the CW source 160 to receive the continuous light beam therefrom. Preferably, the whole pulsed fiber source 54 is an all-fiber source, but it will be understood by one skilled in the art that additional optical components such as mirrors, lenses, spectral shaping elements or any other appropriate element may be provided between the CW light source and the first modulator without departing from the scope of the present invention.

As explained above, each of the drive pulses outputted by the SHAPE output of the digital pulse shaping module has an adjustable width  $t_{sh}$ , defining the period of time the modulator will be open to allow passage of light, and a shape, which is used to shape the intensity of the light allowed to pass through the modulator 162 during the period of time the modulator is open. In the preferred embodiment, a digital pulse shape is programmed in the digital pulse shaping module 52 with a time resolution of 2.5 ns or better and a minimum amplitude resolution of 10 bits. The shape address is programmed using a computer with a simple command or with a user interface where the number of temporal bins as well as their corresponding amplitudes are determined by the user. For Mach-Zehnder amplitude modulators the transmitted optical intensity  $I$  is given by

$$I = I_o(1 - \Gamma) \left[ \sin^2 \left( \frac{\pi}{2} \times \frac{V_{RF}}{V_\pi} \right) + \frac{1}{E.R.} \right] \quad (1)$$

where  $I_o$  is the incident optical power impinging on the device,  $\Gamma$  is the device insertion loss,  $V_{RF}$  is the voltage applied on the RF port, E.R. is the modulator On/Off extinction ratio and  $V_\pi$  is the well-known in the art RF half-wave

voltage required to switch the transmission from maximum extinction to maximum transmission. This equation assumes that the modulator 162 is maintained at maximum extinction except in the time window corresponding to the pulse duration, which is the preferred case. It is known in the art that this maximum extinction condition during the inter-pulse period can be maintained by controlling the so-called bias voltage applied to a second port of the modulator 162, called the bias port or the DC port. Different controlling schemes can be implemented, the most common one being a closed-loop control based on the application of a dither signal on the bias electrode. Typically, a sample of the light transmitted by the modulator 162 is detected with a photodiode located downstream the modulator. The frequency of this signal is then compared to the dither signal frequency and appropriate corrections are applied to the bias electrode for compensating the drift of the voltage at which the extinction is maximal. In the preferred embodiment, the bias servo-loop control function is embedded in the pulse shaping module 52 with adjustable parameters such as the dither frequency and amplitude as well as the PID parameters of the servo-loop.

An example of a pulse shape of the SHAPE signal and of the resulting generated optical shape at the output of the first modulator 162 is shown in FIG. 29. In this example, the pulse shape is irregular, such as may for example be desired for particular applications such as for example selective ablation, drilling or other material processing-related applications. The pulse may however be rectangular shaped or have any other desired variation in time.

Referring still to FIG. 27, the pulsed laser source 54 also preferably includes a first optical amplifier 166, positioned downstream the first modulator 162 for amplifying the pulsed light signal generated thereby. An appropriate pump signal from pump source P1, propagating either backward or forward through the gain medium of the amplifier 166, maintains the required population inversion therein. In a particular embodiment, the first amplifier 166 may include a 35 m length of single-clad, polarization-maintaining Yb-doped aluminosilicate optical fiber having a core diameter of 5  $\mu$ m and an Yb concentration of approximately  $2 \times 10^{24}$  ions/m<sup>3</sup>. In the same embodiment, a FBG-stabilized pump laser diode P1 emitting at 976 nm with a nominal output power of 280 mW is used to pump the Yb-doped fiber. In specific embodiments, a WDM fiber pump combiner is used to launch the pump power into the gain fiber. In other embodiments the fiber amplifier 166 includes a length of optical fiber doped with other rare earth elements, such as Erbium, Holmium, Praseodymium, Neodymium or Thulium. In yet other embodiments, additional optical components are employed for optimizing the pulsed laser source stability, such as isolators, polarizers, filters, etc. In a specific embodiment, the optical amplifier includes a Semiconductor Optical Amplifier (SOA).

The pulsed light source 54 next includes a second modulator 164 which also temporally modulates the pulsed light beam. The second modulator 164 is used to control the pulse temporal profile in conjunction with the first modulator 162, and it has an input optically coupled to the output of the first optical amplifier 166 for receiving the pulsed light beam therefrom. Preferably, it is a Lithium Niobate Mach-Zehnder electro-optic modulator of the APE type having a bandwidth of at least 3 GHz at 1064 nm with an On/Off extinction ratio of at least 25 dB. In other embodiments, other modulation schemes, such as based on an acousto-optic modulator, an electroabsorption modulator, etc. could also be considered.

The RF port of the second modulator is driven by the GATE signal outputted by the digital pulse shaping module. As with

the SHAPE drive signal, the GATE drive signal can be made of a plurality of different drive pulses of predetermined widths  $t_{sh}$  and shapes selected according to their desired effect on the pulsed light beam. The shape of the GATE drive pulses may simply be rectangular as shown in FIG. 2, or may present a more complex shape such as the one presented as an example at FIG. 29.

According to the described embodiments, the final shape of the optical pulses of the pulsed light beam will be determined by both modulators 162 and 164. The first and second modulators 162 and 164 may be partially or completely synchronized with each other, depending on the desired shape of the resulting pulses of the pulsed light beam. The term "synchronized" is used herein as describing the joint timing of the opening and closing of the first and second modulators 162 and 164, taking into account the travel time  $t_{gt}$  of light between both modulators. For example, the two modulators 162 and 164 will be considered fully synchronized if the second modulator 164 opens exactly at the instant the leading edge of the pulse generated by the first modulator 162 reaches it, and closes at the instant this pulse ends. It is an advantageous aspect of the present system that the synchronicity between the two modulators 162 and 164 may be used advantageously to control the width and shape of the pulses of the pulsed light beam. For example, by setting the two modulators 162 and 164 partially out of synchronization, pulses of a very small width may be obtained. Combining drive pulses of different widths and shapes may also advantageously be used to tailor the resulting pulses of the pulsed light beam to a wide range of specifications and with a very high resolution. Practically speaking, the digital pulse shapes programmed by the user will therefore generally include amplitude values and duration values for both the first and the second modulator.

For the sake of example, we consider the case where the modulators 162 and 164 are fully synchronized, as explained above. In that case the optical pulse intensity profile  $I_2(t)$  at the output of the second modulator will be given by

$$I_2(t) = I_0(1 - \Gamma_1) \left[ \text{Sin}^2 \left( \frac{\pi}{2} \times \frac{V_{RF1}(t)}{V_{\pi 1}} \right) + \frac{1}{E.R_1} \right] \times G_{net1}(t) \times (1 - \Gamma_2) \left[ \text{Sin}^2 \left( \frac{\pi}{2} \times \frac{V_{RF2}(t)}{V_{\pi 2}} \right) + \frac{1}{E.R_2} \right] \quad (2)$$

where the indexes 1 and 2 refer to the first and second modulator 162 and 164, respectively, and where  $G_{net1}(t)$  is the net optical gain of the first optical amplifier 166.  $G_{net1}(t)$  is generally time-dependent due to gain saturation (more gain is available at the beginning of the pulse than at the end of the same). In the literature about pulse amplification with homogeneous gain saturation, one can find that the time dependence of the gain is given by

$$G_{01}(t) = \frac{G_{01}}{G_{01} - (G_{01} - 1) \exp[-U_{in1}(t)/U_{sat1}]} \quad (3)$$

where  $G_{01}$  is the initial available gain of the first amplifier,  $U_{in1}(t)$  is the energy fluence ( $J/cm^2$ ) of the pulsed signal launched in the first amplifier and  $U_{sat1}$  is the saturation fluence ( $J/cm^2$ ) of the first optical amplifier 166, which depends on the first amplifier active ion emission cross-section. The ratio  $G_{01}/U_{in1}(t)/U_{sat1}$  in general determines the

amount of pulse shape distortion attributable to gain saturation in the first amplifier.  $G_{net1}(t)$  is given by

$$G_{net1}(t) = (1 - \Gamma_{amp1}) G_{01}(t) \quad (4)$$

where  $\Gamma_{amp1}$  is the sum of the insertion losses of all optical components located in between the two modulators 162 and 164. Equations (2), (3) and (4) can thus be used to predict the actual optical pulse shape at the output of the second modulator 164 for given SHAPE and GATE signals. Conversely, from a given optical shape obtained with a given pulse shape address it is possible to determine  $U_{sat1}$ . From equation (2) one can infer that the distortion induced by gain saturation can be compensated easily with appropriate adjustments of the SHAPE and/or GATE waveforms for obtaining a given optical pulse shape, thereby importantly reducing the impact of gain saturation. In some embodiments, the gain saturation parameters of the first amplifier 166 as well as the modulators parameters ( $V_{\pi}$ , E.R.) are stored in the digital pulse shaping module 52 and used for automatically setting the SHAPE and/or GATE signal characteristics for obtaining a target optical pulse shape entered by the user.

According to some embodiments, the modulators 162 and 164 may also provide time-domain filtering of the seed and of the amplified spontaneous emission (ASE) generated by the optical amplifier stages. This limits the saturation of the optical amplifier stages since it is maintained in the maximum extinction state during most of the interpulse time period. For example, the ASE background generated by the gain medium of the first optical amplifier 166 is blocked by the second modulator 164 during the interpulse time, this background would otherwise partially deplete the population inversion in any gain medium located downstream, which could limit the laser output pulse peak power to a lower value due to the reduced extractable energy. The second modulator 164 also filters, in the time domain, the amplified leakage coming from the first modulator 162 (resulting from the finite value of E.R.<sub>1</sub> in equation (2)).

Still referring to FIG. 27, the pulsed laser source 54 also preferably include a second optical amplifier 168 provided downstream the second modulator 164 for final pulse amplification. As for the first optical amplifier 162, an appropriate pump signal, propagating either backward or forward through the gain medium of the amplifier 168, maintains the required population inversion therein. In a particular embodiment, the second amplifier 168 includes a 3.5 m length of single-clad, polarization-maintaining Nd-doped aluminosilicate optical fiber having a core diameter of 5  $\mu m$  and a Neodymium concentration of approximately  $5 \times 10^{24}$  ions/ $m^3$ . In the same embodiment, a FBG-stabilized pump laser diode P2 emitting at 808 nm with a nominal output power of 125 mW is used to pump the Nd-doped fiber. In specific embodiments, a WDM fiber pump combiner is used to launch the pump power into the gain fiber.

In other embodiments the second fiber amplifier 168 includes a length of single-clad or multi-clad optical fiber doped with other rare earth elements, such as Erbium, Holmium, Praseodymium, Ytterbium, Samarium or Thulium. In yet other embodiments, additional optical components are employed for optimizing the pulsed laser source stability, such as isolators, polarizers, filters, etc. In a specific embodiment, the second optical amplifier 168 includes a Semiconductor Optical Amplifier (SOA). Although FIG. 27 shows a single stage amplifier for the final pulse amplification, it is not required by the present invention. In specific embodiments, an amplifier chain comprising several cascaded amplifier stages is used for the final pulse amplification. In a particular embodiment, the amplifier chain includes at least one DPSS

amplifier stage based on gain medium like Nd:YAG or Nd:YVO<sub>4</sub>. Those skilled in the art will recognize numerous variations and alternatives.

The pulsed signal OPTICAL\_SH\_OUT outputted by the pulsed laser source will have an optical intensity  $I_{out}(t)$  given by

$$I_{out}(t) = I_2(t) \times G_{net2}(t) \quad (5)$$

where  $I_2(t)$  is given by equation (2) and where  $G_{net2}(t)$  is the net optical gain of the second optical amplifier 168, given by

$$G_{net2}(t) = (1 - \Gamma_{amp2}) G_{02}(t) \quad (6)$$

where  $\Gamma_{amp2}$  is the sum of the insertion losses of all optical components located downstream to the second modulator 164 and where  $G_{02}(t)$  is the optical gain of the second amplifier 168. As for  $G_{01}(t)$ , a theoretical expression for  $G_{02}(t)$  is

$$G_{02}(t) = \frac{G_{02}}{G_{02} - (G_{02} - 1) \exp[-U_{in2}(t)/U_{sat2}]} \quad (7)$$

where  $G_{02}$  is the initial available gain of the second amplifier,  $U_{in2}(t)$  is the energy fluence (J/cm<sup>2</sup>) of the pulsed signal launched in the second amplifier and  $U_{sat2}$  is the saturation fluence (J/cm<sup>2</sup>) of the second optical amplifier 168, which depends on the second amplifier active ion emission cross-section. The ratio  $G_{02}U_{in2}(t)/U_{sat2}$  in general determines the amount of pulse shape distortion attributable to gain saturation in the second amplifier. This distortion can be compensated by appropriate adjustments of the SHAPE and/or GATE waveforms for obtaining a given optical pulse shape, thereby importantly reducing the impact of gain saturation. In some embodiments, the second amplifier gain saturation parameters are stored in the digital pulse shaping module and used for automatically setting the SHAPE and/or GATE signal characteristics for obtaining a target optical pulse shape entered by the user.

To illustrate the effect of gain saturation we consider the case of fully synchronized rectangular SHAPE and GATE waveforms for the typical gain saturation conditions prevailing in the preferred embodiment (see FIG. 30). For the sake of simplicity and as an approximation, we use the ratio  $G_0 U_{in}/U_{sat}$  as an indicator for the gain saturation, where  $U_{in}$  is the energy fluence of the whole pulse, as if the gain media were punctual elements with negligible length with respect to the spatial distribution of the pulse, and where  $G_0$  is the optical gain values of the whole gain media. Obviously the longitudinal effects can be readily included through finite element analysis.

For some applications it is required that the pulsed laser system 54 can also operate in a CW or quasi-CW mode so as to emit CW or quasi-CW radiation instead of pulses. For example, in memory repair systems, such a mode of operation is used for target alignment purposes. It may also be required that the laser can be put on demand in the CW or quasi-CW mode and then back to the pulsed mode. In one embodiment, there is therefore provided a quasi-CW mode having those characteristics. In the preferred embodiment, both the SHAPE and the GATE signals are rectangular waveforms in the Quasi-CW mode, with a frequency of 100 MHz typically. Dynamic on-demand switching in between the pulsed mode and the quasi-CW mode is also provided, as well as programmable amplitude of the SHAPE rectangular wave.

Specific examples where the capabilities for industrial applications for embodiments as described above are demonstrated are given below. Those examples refer to IC link

severing or related applications, but those ordinary skilled in the art will recognize that numerous other applications can be envisioned.

In U.S. Pat. No. 6,281,471, SMART, present the advantages of using somewhat square pulse shapes for processing target material. FIG. 31 shows that such a shape can be readily obtained with a system as described hereinabove, with the SHAPE signal being programmed so as to compensate the pulse distortion induced by gain saturation (see FIG. 30) and with a rectangular GATE signal. Although the individual programmed digital amplitudes are visible because of remaining gain saturation, the effect is quite attenuated. Also, for the real optical pulse shapes obtained with the present embodiment the remaining amplitude oscillations are smaller than shown in FIG. 31, as the effect of the limited rise times and fall times are not considered in the calculated shape. In this example, the energy per pulse is 10  $\mu$ J at a repetition rate of 100 kHz, and both the rise time and the fall time are smaller than 1 ns. Preferably, the rise time and the fall time are of the order of 1 ns or less. The pulse repetition rate is preferably higher than 100 kHz, and most preferably it is higher than 200 kHz. The preferred pulse energy range is 0.1  $\mu$ J to 1 mJ, whereas the preferred pulse duration range is 1 ps to 1  $\mu$ s.

In U.S. Pat. No. 7,348,516 Sun et al. disclose pulse shapes comprising one or several spikes for IC link severing (see FIGS. 32 to 34 (PRIOR ART) of the present application). The spike peak power Pmax is preferably 10% to 50% over the average power of the pulse Pmin, and the spike duration is defined as the spike full duration at the middle power point Ps, between Pmax and Pmin. The spike ends at time,  $t_e$ , which is before the time,  $t_1$ , when the link material is totally removed.

FIGS. 35 to 37 show that optical pulse shapes of the type appearing in FIGS. 32 to 34 respectively can be easily obtained with the current system.

#### 4.2 Pulsed Laser Source Based on a Semiconductor Laser Diode Operated in the Pulsed Regime

Referring to FIG. 42, there is illustrated another embodiment, where the SHAPE signal is used to drive the current of a seed laser diode 160'. The pulsed laser source 54' in this case consists in the seed laser diode 160' followed by one or several cascaded optical amplifiers 166'. The GATE signal is not used.

The pulsed laser source 54' of FIG. 42 first includes the seed light source 160' generating a pulsed light beam when controlled by the SHAPE signal outputted by the digital pulse shaping module. Preferably, the seed light source 160' is a semiconductor laser diode. The pulsed light beam preferably has a spectral shape which will determine the spectral shape of the light outputted by the entire pulsed light source. Advantageously, the laser diode 160' may be selected or replaced depending on the required spectral profile of the outputted light. Alternatively, a wavelength tunable diode may be used. Additional components may optionally be provided downstream the laser diode 160' to modify its spectral shape. An optical isolator may also be provided downstream the seed laser diode 160' to prevent feedback noise from reaching it.

In the preferred embodiment, the seed source is an external cavity semiconductor laser diode with a central emission wavelength of 1064.3 nm and a nominal output power of 100 mW in the CW regime. The emission is single longitudinal mode with a fairly narrow line width (less than 10 MHz). The emission wavelength can be easily fine-tuned by the user with a simple user command giving access to the seed laser diode operating temperature. This functionality, embedded in the core of the pulse shaping module, has been used for example in a memory repair application where the pulsed laser source output was amplified with a Nd:YVO<sub>4</sub> solid state amplifier. In

such a configuration, it is important to control the overlap of the pulsed laser source emission spectrum with respect to the relatively narrow spectrum (~0.5 nm FWHM) of the solid state medium for optimizing the amplification, especially under different pumping conditions of the crystal. With increasing pump power, the crystal gain spectrum tends to shift to higher wavelengths. In the mentioned application, the emission wavelength flexibility provided by the embedded laser platform is used to fine-tune the pulsed laser source emission wavelength with a simple user command sent by a computer, in order to optimize the spectral overlap of the source with respect to the solid state amplifier gain spectrum under varying pumping conditions (see FIG. 28). In typical conditions, the seed laser diode wavelength shifts by about 50 pm when changing the diode operating temperature by 1° C. The laser platform provides a resolution of 0.1° C. for the laser diode operating temperature set point, thus allowing for a resolution of about 5 pm in the wavelength adjustments. In another embodiment, the seed source is a Fiber Bragg Grating (FBG) stabilized laser diode emitting at 1064.3 nm with a nominal output power of 150 mW. The FBG temperature is controlled with a resolution of 0.1° C. with a user command, also allowing for fine wavelength tuning by the user.

In this embodiment, the SHAPE signal is directly applied to the laser diode 160' for controlling the diode drive current with a shaped waveform. The amplitude of the SHAPE signal is controlled with a resolution of at least 10 bit in the preferred embodiment. The maximum current amplitude is preferably higher than 1 ampere and most preferably higher than 5 amperes. In some embodiments, a bias current (DC) is also applied to the laser diode 160' for controlling the diode transient response and gain-switch effects. In yet other embodiments, a feedback loop with a servo-control on the applied bias current is embedded in the pulse shaping module. As for the embodiments described above, the pulse shapes are programmed quite simply in the pulse shaping module. The shape address is programmed using a computer with a simple command that defines the number of temporal bins as well as their corresponding amplitudes.

The pulsed laser source 54' also preferably includes at least one optical amplifier 166', positioned downstream the seed laser diode 160' for amplifying the pulsed light signal generated thereby. An appropriate pump signal, propagating either backward or forward through the gain medium of the amplifier, maintains the required population inversion therein. In a particular embodiment, the amplifier includes a 35 m length of single-clad, polarization-maintaining Yb-doped aluminosilicate optical fiber having a core diameter of 5 μm and an Yb concentration of approximately  $2 \times 10^{24}$  ions/m<sup>3</sup>. In the same embodiment, a FBG-stabilized pump laser diode emitting at 976 nm with a nominal output power of 280 mW is used to pump the Yb-doped fiber. In one other particular embodiment, the amplifier includes a 3.5 m length of single-clad, polarization-maintaining Nd-doped aluminosilicate optical fiber having a core diameter of 5 μm and a Neodymium concentration of approximately  $5 \times 10^{24}$  ions/m<sup>3</sup>. In the same embodiment, a FBG-stabilized pump laser diode emitting at 808 nm with a nominal output power of 125 mW is used to pump the Nd-doped fiber. In specific embodiments, a WDM fiber pump combiner is used to launch the pump power into the gain fiber. In other embodiments the fiber amplifier includes a length of single clad optical fiber doped with other rare earth elements, such as Erbium, Holmium, Praseodymium, Samarium or Thulium. In the illustrated embodiment, an additional optical amplifier 168' is shown, although any appropriate number of such amplifiers may be provided. In yet other embodiments, additional optical components are

employed for optimizing the pulsed laser source stability, such as isolators, polarizers, filters, etc. In a specific embodiment, the optical amplifier includes a Semiconductor Optical Amplifier (SOA).

In other specific embodiments the fiber amplifier chain includes a length of multi-clad optical fiber doped with rare earth elements, such as Erbium, Holmium, Praseodymium, Ytterbium, Samarium, Neodymium or Thulium. In a particular embodiment, the amplifier chain includes at least one DPSS amplifier stage based on gain medium like Nd:YAG or Nd:YVO<sub>4</sub>. Those skilled in the art will recognize numerous variations and alternatives.

The pulse shape distortion induced by gain saturation effects in the optical amplifier stages can be readily compensated using the pulse shaping capability of the laser platform. For the different embodiments described herein, the SHAPE signal waveform is adjusted accordingly. In some embodiments, the amplifier chain gain saturation parameters are stored in the pulse shaping module and used for automatically setting the SHAPE signal characteristics for obtaining a target pulse shape entered by the user. In other embodiments, a servo-control on the SHAPE signal is embedded in the pulse shaping module for maintaining the optical pulse shape constant under varying conditions of operations of the laser.

In the preferred embodiments of the pulse laser system described herein, the SHAPE signal is a rectangular waveform in the Quasi-CW mode, with a frequency of 100 MHz typically. Dynamic on-demand switching in between the pulsed mode and the quasi-CW mode is also provided, as well as programmable amplitude of the SHAPE rectangular wave.

The examples presented above for a pulse light source of the type shown in FIG. 27 also apply to the different embodiments of a directly modulated seed source, except that the pulse shaping capability is provided via the SHAPE signal alone and that there is no modulator transfer function to take into account. Optical shapes such as the ones shown in FIGS. 31, 35, 36 and 37 can be readily obtained with appropriate SHAPE digital waveforms programmed in the pulse shaping module.

#### 4.3 Pulsed Laser Source Based on a Semiconductor Laser Diode Operated in the Pulsed Regime and an Amplitude Modulator

Referring to FIG. 43, there is shown a variant of the embodiment of FIG. 42 where an amplitude modulator 162' driven by the GATE signal is added downstream to the seed laser diode 160', at any particular location in the amplifier chain. Preferably, the modulator 162' is located in between two consecutive optical amplifiers 166' and 168', and will provide extended pulse shaping capability as explained above. Furthermore, the modulator 162' provides time-domain filtering of the seed leakage and of the amplified spontaneous emission (ASE) generated by the optical amplifier stages.

In this embodiment, the modulator 162' is used to control the pulse temporal profile in conjunction with the seed laser diode 160'. Preferably, it is a Lithium Niobate Mach-Zehnder electro-optic modulator of the APE type having a bandwidth of at least 3 GHz at 1064 nm with an On/Off extinction ratio of at least 25 dB. In other embodiments, other modulation schemes, such as based on an acousto-optic modulator, an electroabsorption modulator, etc. could also be considered.

The RF port of the modulator is driven by the GATE signal outputted by the digital pulse shaping module. As with the SHAPE drive signal, the GATE drive signal can be made of a plurality of different drive pulses of predetermined widths  $t_{s,i}$  and shapes selected according to their desired effect on the



pulsed light beam. The shape of the GATE drive pulses may simply be rectangular as shown in FIG. 2, or may present a more complex shape.

According to some embodiments, the final shape of the optical pulses of the pulsed light beam will be determined by both the seed laser diode **160'** controlled by the SHAPE signal and the modulator **162'** controlled by the GATE signal. The SHAPE and the GATE signals may be partially or completely synchronized with each other, depending on the desired shape of the resulting pulses of the pulsed light beam. The term "synchronized" is used herein as describing the joint timing of the leading edge and falling edge of the SHAPE signal and of the GATE signal, taking into account the travel time  $t_{gr}$  of light between the seed laser diode and the modulator. For example, the two signals will be considered fully synchronized if the leading edge of the GATE signal occurs exactly at the instant the leading edge of the optical pulse generated by the seed laser diode reaches it, and the falling edge of the GATE signal occurs at the instant this pulse ends. In other words, the seed laser diode and the modulator are considered completely synchronized when both signals have the same duration or pulse width and the leading edge of the GATE signal is delayed by  $t_{gr}$  with respect to the leading edge of the SHAPE signal, as shown in FIG. 2. It is an advantageous aspect that the synchronicity between the seed laser diode and the modulator may be used advantageously to control the width and shape of the pulses of the pulsed light beam. For example, by setting the seed laser diode and the modulator partially out of synchronization, pulses of a very small width may be obtained. Combining drive pulses of different widths and shapes may also advantageously be used to tailor the resulting pulses of the pulsed light beam to a wide range of specifications and with a very high resolution. Practically speaking, the digital pulse shapes programmed by the user will therefore generally include amplitude values and duration values for both the seed laser diode (SHAPE signal) and the modulator (GATE signal).

Still referring to FIG. 43, both the SHAPE and the GATE signals are preferably rectangular waveforms in the Quasi-CW mode, with a frequency of 100 MHz typically. Dynamic on-demand switching in between the pulsed mode and the quasi-CW mode is also provided, as well as a programmable amplitude of the SHAPE rectangular wave.

#### 4.4 Other Embodiments Using Q-Switched Laser, Gain-Switched Lasers and Mode-Locked Lasers

In alternative embodiments, the platform can control different types of pulsed laser so as to generate optical pulses with controlled shapes or pulse trains with different durations, amplitude profiles and repetition rates.

In some embodiments, the SHAPE signal is used to drive the pump laser diodes of a laser so as to generate gain-switched pulses with a controlled pulse shape. An optical amplifier or an optical amplifier chain following the gain-switched laser then amplifies the pulsed light beam. In particular embodiments, a modulator controlled by the GATE signal is inserted at a location in the amplifier chain, preferably in between two amplifier stages, and provides an extended pulse shaping capability.

In specific embodiments, the SHAPE signal is used to trigger a Q-switched laser so as to generate Q-switched pulses with a controlled pulse shape. An optical amplifier or an optical amplifier chain following the gain-switched laser then amplifies the pulsed light beam. In particular embodiments, an amplitude modulator controlled by the GATE signal is inserted at a location in the amplifier chain, preferably in between two amplifier stages, and provides an extended pulse shaping capability.

Referring to FIG. 44, there is illustrated a pulsed laser source **54''** according to such an embodiment. In this case the pulsed seed light source **160''** is preferably a mode-locked laser generating a light signal of sub-pulses, preferably optical pulses having durations in the femtosecond or picosecond time scale. A first modulator **162''** is disposed downstream the pulsed seed laser source and **160''** and modulates the light signal to form the laser pulses, with each laser pulse encompassing a train of the sub-pulses. The first modulator **162''** is controlled by the SHAPE signal. Preferably, a second modulator **164''**, controlled by the GATE signal, is disposed downstream the first modulator **162''**. A first optical amplifier **166''**, such as a fiber gain medium as explained above, is preferably disposed between the first and second modulators **162''** and **164''**. One or more additional gain medium or optical amplifiers **168''** may be provided downstream the second modulator **164''**.

In the illustrated embodiment of FIG. 44, a sampling assembly **170** provides a trigger signal synchronized with the sub-pulses from the mode-locked laser to the digital pulse shaping module **52**. The sampling assembly **170** preferably includes an optical coupling device **172** (e.g. tap coupler) coupling a small fraction of the light signal from the mode-locked laser out of the light path, and a detector **174** (e.g. a photodiode) for detecting the tapped fraction of the light signal and outputting a corresponding detected signal. A trigger generator **176** receives the detected signal and generates therefrom the trigger signal TRG\_MLK synchronized with the mode-locked laser.

In the preferred embodiment, the TRG\_MLK signal is used by the pulse shaping module **52** to perform pulse picking with the first amplitude modulator **162''**. Preferably, the SHAPE signal pulse repetition rate is  $n$  times smaller than the mode-locked laser repetition rate, where  $n$  is an integer. In the service mode, the SHAPE signal pulse repetition rate and pulse shape are controlled by the user with simple commands sent to the digital pulse shaping module **52**. In the user mode, an external trigger signal EXT\_TRIGGER can be used to trigger shaped trains of mode locked laser pulses (see FIG. 45), which can be very beneficial for advanced material processing applications. The optional second modulator **164''** provides an extended pulse train shaping capability.

#### Methods for Stabilizing an Output of a Pulsed Laser System

The sections above demonstrate that it is possible to control the output of various pulsed laser systems with great agility and flexibility, thereby tailoring the use of such systems to the demands of specific applications.

#### Transitions Between QCW and Pulse Shaping Regimes

One feature of interest of a pulsed laser system as described above is the option of operating this system in QCW mode or regime. In such a QCW mode, a periodic optical signal is emitted, which can be a square-wave signal as described above or have a different profile such as sinusoidal, triangular, Gaussian or the like. For typical applications, the QCW periodic signal may have a high duty cycle, for example 50%, and a high frequency (typically 100 MHz). The QCW mode is for example useful in the context of material processing applications for scanning targets without inducing damage. In such applications it is important in when performing such a scan to keep the maximum energy impinging on the work piece lower than a threshold value above which scorching of the material begins to take place. When in the QCW mode or regime, the periodic signal controlling the pulsed laser system therefore has a maximum energy which is lower than a processing

threshold, which corresponds to the limit on the maximum energy of the output above which the impinging light starts to negatively impact the target.

Typically, scanning and processing operations will alternate frequently, which involves frequent transitions between the QCW regime and the pulse shaping regime. Transient responses of the gain media in the pulsed laser system are observed at the transitions between both modes. This phenomenon is illustrated in FIG. 38 (PRIOR ART). The upper line of FIG. 38 illustrates the QCW-enabling signal, which enables the operation of the pulse laser system in this mode for a first portion of the time. Once the QCW-enabling signal is disabled, the external trigger, shown as a series of square pulses of constant PRF in the second line of FIG. 38, takes over. The corresponding optical output shows that the energy per pulse of the first few pulses coming after the transition from QCW to pulsed mode will be either higher or lower than the steady state energy (shown higher in the overshoot case illustrated in the third line of FIG. 38). This results from different steady state population inversion conditions in each mode, as depicted in the bottom line of FIG. 38. This difference in the population inversion levels usually comes from the different signal characteristics being injected in the gain media in each mode, in the time domain or in the spectral domain. Therefore, depending on the modulation parameters in the QCW mode and on the pulse shape and repetition rate in the pulsed mode, several pulses following a transition from the QCW mode to the pulsed mode may have an energy level per pulse lower than the acceptable processing pulse energy window. Similarly, during the transition from QCW mode, the pulse amplitude of one or more transient pulses may exceed the pulse amplitude window established for satisfactory processing. As the transient pulses must be discarded, this type of transient behavior represents a process throughput penalty. Further, in the case where overshoot pulses are emitted, as in FIG. 38, another drawback is that non-linear effects could occur in the fiber amplifiers during the propagation of these overshoot pulses. For example, Stimulated Brillouin Scattering (SBS) could take place in the gain fibers, resulting in instabilities in the amplifier which can ultimately generate damage to some of its optical components. From a practical standpoint, this represents a limitation in terms of the maximum energy per pulse that can be delivered by a laser system in the steady state (constant repetition rate) regime.

In accordance with one aspect of the invention, the output of the pulsed laser system following a transition between the QCW and pulse shaping regimes involves controlling the duty cycle and the amplitude of the periodic signal fed to the laser system in the QCW regime to output an average QCW optical power which substantially corresponds to the average pulsed optical power outputted during the steady-state operation of the pulsed laser system when in the pulse shaping regime. This may for example be accomplished by setting the amplitude of the QCW periodic signal to a value which induces a population inversion in the gain media of the pulsed laser system which is the same as the average population inversion in the pulse shaping regime. In other words, the gain media are seeded with nearly equal amount of average optical power in both modes, thereby avoiding the transition between different levels of steady state population inversion.

The gain medium controlled in this fashion may be any from any medium in the pulsed laser system which contributes to the outputted light through stimulated emission of radiation. For example, the output of the laser diode may be amplified by one or more. Optical Fiber Amplifiers (OFA), in which the time necessary for the stabilization of the population inversion levels to a steady state is typically of the order

100  $\mu$ s or more, which corresponds to several pulses for laser diodes operated with a repetition rate of at least 100 kHz. Alternatively or additionally, the gain media may be embodied by solid state amplifiers such as Nd:YAG or Yb:YAG media or any other appropriate solid state medium in which the required time for stabilization of the population inversion levels to steady state can be an issue.

Referring to FIG. 39A, there is shown an example where the characteristics of the QCW periodic signal in the situation illustrated in FIG. 38 have been adjusted as explained above for the entire duration of the QCW regime. In this example, the optical power outputted in the QCW regime has been raised to match the average output power in the pulse shaping mode, which lowers the corresponding level of population inversion, as can be seen in FIG. 39A. By avoiding the need for a stabilization of the population inversion levels to a new value, the overshoot pulses observed at FIG. 38 are completely avoided, making the very first pulse following the transition of regimes usable, and thereby improving the overall throughput of the laser system.

Referring to FIG. 39B, there is shown another embodiment of the invention where the adjustment of the average value of the QCW optical power is performed for a transitory period immediately preceding the pulse shaping regime. Preferably, the transitory period has a duration which substantially corresponds to the stabilization time of the population inversion of the gain media in the laser system.

In some embodiments, the amplitude of the QCW oscillation may dynamically controlled in the time domain in the following manner. The QCW oscillation amplitude is set to a programmed, idle value  $QCWamp_{idle}$  which advantageously minimizes the optical fluence impinging on the targets being scanned. After a high to low transition of the QCW mode enabling signal, the QCW amplitude is increased to a predetermined, tailored value  $QCWamp_{ij}$  corresponding to the processing pulse repetition frequency  $PRF_i$  and to the selected processing pulse amplitude profile  $PAP_j$ , as shown in FIG. 39B. Advantageously, as mentioned above, this increase preferably occurs during a programmed time interval  $\delta$  that roughly corresponds to the population inversion stabilization time, which is itself dependent upon the physical characteristics of the gain media. In this way, the population inversion is allowed to stabilize at a level that will minimize the transient overshoot or undershoot for the few first pulses following the transition. In other embodiments, the duty cycle of the quasi-CW oscillation is modified during the same interval of time  $\delta$ , so as to minimize the transient overshoots or undershoots.

As one skilled in the art will readily understand, the transient response between the QCW regime and the pulse shaping regime will be generally dependent on the PRF and on the PAP. In a preferred embodiment, a QCW amplitude lookup table as representatively shown in FIG. 46, may be programmed in controls of the laser system, listing, for given values of the duty cycle of the QCW periodic signal, a corresponding value for its amplitude in view of a desired PRF and of a desired PAP in the pulse shaping regime. In this embodiment, the system may automatically select the QCW oscillation amplitude  $QCWamp_{ij}$  as a function of the selected pulse amplitude profile  $PAP_j$  and of the operating  $PRF_i$ . In an alternative embodiment, the lookup table may provide values for adjusting the duty cycle of the QCW periodic signal in view of given values of its amplitude.

In accordance with another aspect of the invention the stabilization of the output of a pulsed laser system following a transition between a QCW and a pulse shaping regime may be accomplished by providing a transitory period between the

QCW and pulse shaping regimes, and controlling the pulsed laser system during this transitory period with a transitory pulsed signal having the same pulse repetition frequency as the pulse shaping signal and a pulse amplitude profile selected to provide substantially the same output as in the pulse shaping regime during the transitory period. The principle applied here is the same as that of the previous embodiments, that is, avoiding sharp changes in the inversion population levels at the time of the transition between the two regimes. However, in this case, instead of adapting the QCW periodic signal, the pulse shaping capability of the laser pulsed system is used.

The transitory period has a duration which substantially corresponds to the population inversion stabilization time of one or more gain media in the laser system. Preferably, the transitory pulsed signal may define a plurality of transitory pulses of increasing or decreasing average pulse amplitude over the transitory period.

Referring to FIG. 39C, an example is shown where this method is used to correct the overshoot pulses observed in FIG. 38. In this case, the laser system is programmed in such a way that a number of pulses following the transition will have different programmed pulse shapes, these programmed shapes being chosen so that the resulting amplified optical pulses are all usable for processing work. Each of those tailored, programmed seed pulses are denoted herein as an oscillator preform seed pulse (OPSP). As one skilled in the art will readily understand, this embodiment takes advantage of the pulse shaping flexibility of a pulsed laser system to seed the gain media in the system with OPSPs having shapes and amplitudes that will compensate for the population inversion dynamics at the transition, resulting in outputted optical pulses having the required energy and amplitude profile, as would the steady state optical pulses. In FIG. 39C, and comparing to FIG. 38, the first two optical pulses following the transition from the QCW mode are usable pulses because tailored OPSPs (OPSP #1 and OPSP #2 in FIG. 39C) have been employed to generate them.

For the third and fourth pulses following the transition, the steady state regime has been reached, corresponding to OPSP #3.

As one skilled in the art will readily understand, the stabilization methods described above may advantageously be put in practice, with a digital pulse shaping module as described above, which include a clock generator, a shape generator, QCW means, a DAC and any other appropriate components. However, it will be further understood that any other system capable of providing the required adjustments of either the QCW periodic signal or the pulse shaping signal may be used without departing from the scope of the present invention.

#### Consecutive Series of Process Pulses

In some material processing applications it is a current practice to locally insert a non-process pulse between consecutive series of pulses of fixed repetition rate for resynchronization purposes.

FIG. 47 shows an illustrative workpiece target diagram with two evenly-spaced groups of workpiece targets of spacing "a" separated by a gap of length "a+b". In the case where the beam positioner is moving the laser spot position at a constant velocity equal to workpiece spacing "a"×PRF<sub>process</sub>, a non-process pulse may be commanded corresponding to non-target position 105 such that the interpulse time prior to the laser spot position arriving at target position 106 is substantially equal to  $t_{process} = 1/PRF_{process}$ . The effect of the insertion of the non-process pulse is to trigger the laser at a shorter interpulse time than  $t_{process}$  in order to resynchronize

the subsequent series of laser pulses emitted at laser PRF<sub>process</sub> with the next series of targets.

As shown in FIG. 40, for such a situation, a transient response can penalize the throughput of the material processing system given the fact that a number of pulses following the non-process pulse will not have the energy level required by the processing window. The non-process pulse represents additional energy extraction from the gain media with respect to the steady state condition (constant repetition rate). Therefore the population inversion after the non-process pulse is typically lower than it is after a steady state pulse. This leaves less extractable energy from the gain media for a number of pulses following the non-process pulse, until the population inversion stabilizes again at its steady state level for the next sequence of evenly spaced targets. The exact number of pulses that will not be usable is dependent upon the processing pulse repetition rate PRR<sub>o</sub>, the delay  $t_d$  in between the non-process pulse and the pulse preceding it, the delay  $t_p$  between the non-process pulse and the pulse following it, the gain media characteristics, etc.

In accordance with one aspect of the invention, there is provided a method for stabilizing an output of a pulsed laser system in a situation such as the one described above, that is, during consecutive series of process pulses, preferably evenly spaced, separated by a time gap during which one or more non-process pulses are outputted. Taking advantage of pulse shaping capabilities of the pulsed laser system, the method involves adjusting the pulse amplitude profile of a pulse shaping signal controlling the output of the pulsed laser system for the non-process pulse to affect the available energy in gain media of the laser system so that it operates in a steady-state at the very first process pulse of the series subsequent to the time gap. In effect, sequential pulse shaping is used so that the non-process pulses extract an amount of energy from the gain media that yields an energy level lying in the processing window for all subsequent process pulses.

In one preferred embodiment, programmed oscillator preform seed pulses (OPSPs) are used to define both the process and non-process pulses. Referring to FIG. 41, a first OPSP denoted OPSP1 is selected to produce the process pulses shown in FIG. 47. A second OPSP denoted OPSP2 is dynamically employed to produce the non-process pulse shown in FIG. 47. It is of interest to note that, in addition to its amplitude profile, the duration of OPSP2 need not be the same as OPSP1, providing a great versatility in the selection of the parameters of the non-process pulse. The optimal OPSP for a given pulse in a specific sequence, in general, is dependent on the ratio b/a of interpulse periods shown in FIG. 47, on PRF<sub>process</sub>; and on the gain medium saturation level.

Those skilled in the art will appreciate that the non-process pulse may embody the pre-pulse taught by Bruland and that the process pulse of the present invention may be employed to produce pulse on position process pulses, which may be blocked or not from striking target workpiece structures, as taught by Baird and Bruland.

In one embodiment, the digital pulse shaping module as described above may be used to provide the process and non-process pulses according to this aspect of the invention, by having a user program both pulse shapes OPSP1 and OPSP2 in the pulse shaping module and assigning those shapes to the SHAPE A and SHAPE B respectively. SHAPE A and SHAPE B can be dynamically selected by the user with the SHAPE\_A/B input of the. When processing the target the SHAPE\_A/B input is then used to switch from SHAPE A to SHAPE B and back to SHAPE A so as to select SHAPE B for the non-process pulse only.

This method using sequential pulse shaping to improve pulse stability is broadly applicable to numerous additional laser processing applications. By way of example, an embodiment of this method may be employed to improve the pulse energy and pulse amplitude uniformity of pulses produced by spectrally tailored pulsed fiber oscillators employed in laser systems for laser scribing of photovoltaic devices.

The fabrication of thin film photovoltaic panels involves the manufacture of scribe lines produced by a pulsed laser system. As those skilled in the art will appreciate, thin film photovoltaic panels or superstates are patterned to form series of connected cells in order to achieve a desirable operating current for the interconnected cells on a single panel or superstrate.

FIG. 48 depicts an exemplary top view section of one adjoining pair of laser scribe lines on a thin film photovoltaic device. In this example, the beam positioner, which may effect beam spot movement through employment of galvo scanning elements or linear stages or an assemblage of scanning and linear stage elements, moves the laser spot over scribe line m towards the final laser spot position thereon labeled process pulse q. At this point, the beam positioner moves the laser spot position to the first laser spot position of scribe line n, indicated as process pulse 1. As figuratively represented in FIG. 48, during the interline move, non process pulses are emitted. As those skilled in the art will recognize, such non process pulses may be blocked from propagating to strike the thin film photovoltaic workpiece by external optical modulators or fast shutter mechanisms, such as galvanometric shutters. In order to achieve the desired pulse energy and pulse amplitude uniformity of pulses employed for the scribing of line n following the interline move between scribe lines m and n, non process pulses adapted according to an embodiment of the present invention having advantageous values of OPSP may be employed on a pulse by pulse basis, ensuring proper energy levels in the gain media of the laser system. An illustrative example of an OPSP lookup table for pulse by pulse programming of OPSP values during laser scribing of thin film photovoltaic devices is shown in FIG. 49.

A similar method may be employed to improve the hole to hole uniformity of laser microvias produced laser systems employing spectrally tailored pulsed fiber oscillators. As those skilled in the art will recognize, each processed microvia may be produced by one or more pulses, dependent on the material type and the relative thickness of the individual layers through which the microvia must be drilled. A common problem encountered in laser microvia production is underdrilling or overdrilling of the first via in a row following a beam positioned move which is performed in a length of time substantially different than the intrahole move time, resulting in laser pulse transient behavior related to variations in the interpulse time. The method of sequential pulse shaping described above can be employed to improve the uniformity of pulses during such transitions through pulse by pulse programming of OPSP values as previously described. As those skilled in the art will appreciate, this method is particularly valuable when the laser system operates at a harmonic wave-

length, such as 355 nm, corresponding to the third harmonic of a fundamental wavelength of 1064 nm. Laser microvia drilling systems commonly employ lasers with 355 nm output to laser drill microvias in flexible circuits and in integrated circuit packages.

Of course, numerous modifications could be made to the embodiments described above without departing from the scope of the present invention.

The invention claimed is:

1. A method for stabilizing an output of a pulsed laser system during consecutive series of process pulses, said consecutive series being separated by a time gap during which at least one non-process pulse is outputted, said output being controlled by a pulse shaping signal having a pulse amplitude profile, said method comprising a step of adjusting said pulse amplitude profile of the pulse shaping signal for the at least one non-process pulse to affect available energy in at least one gain medium of the laser system so that said laser system operates in a steady-state at a first process pulse of the series of process pulses subsequent to said time gap.

2. The method according to claim 1, wherein the at least one non-process pulse comprises a single non-process pulse having a square-shaped pulse amplitude profile.

3. The method according to claim 1, wherein said pulse shaping signal directly modulates a laser diode of said laser system, said laser diode incorporating the at least one gain medium.

4. The method according to claim 1, further comprising providing a lookup table listing at least one sequence of pulse shapes for selection as said process and non-process pulses.

5. The method of claim 1, comprising a preliminary step of providing a digital pulse shaping module for generating said pulse shaping signal, comprising:

a clock generator generating a plurality of phase-related clock signals at a same clock frequency;

a shape generator outputting a digital shape signal corresponding to a digital input waveform, said shape generator operating in Double Data Rate in response to said clock signals;

a Digital-to-Analog Converter, hereinafter DAC, receiving the digital shape signal and converting the same into an analog shape signal; and

a shape buffer driver receiving the analog shape signal and generating therefrom said pulse shaping signal.

6. The method according to claim 5, wherein said digital pulse shaping module further comprises a shape memory buffer storing a plurality of predetermined waveforms, and the shape generator comprises selecting means for selecting any one of said predetermined waveforms as the digital input waveform.

7. The method according to claim 6, wherein the predetermined waveforms stored in the shape memory buffer comprise a process waveform and a non-process waveform, the selecting means selecting the process waveform during said series of process pulses, and selecting the non-process waveform during said time gap.

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